

1/161

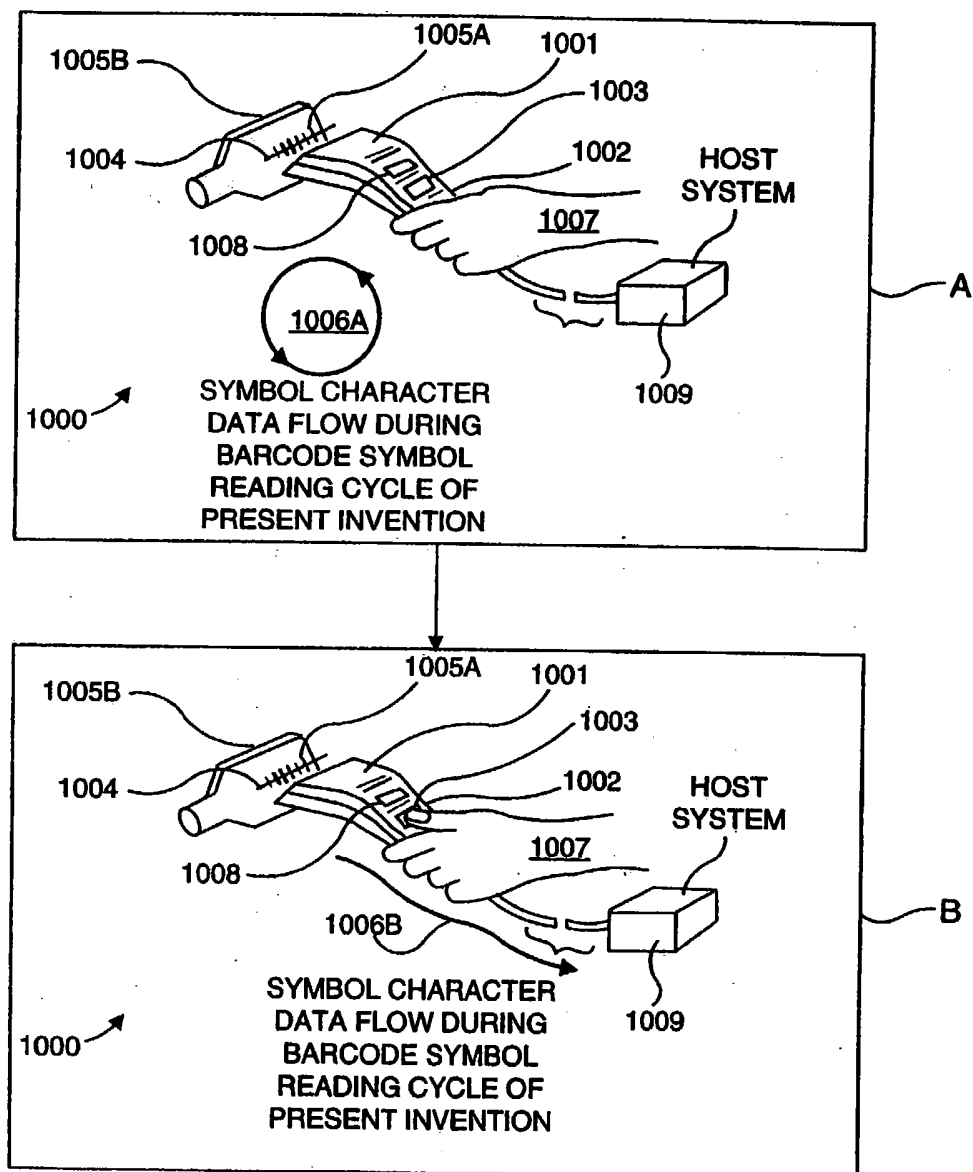
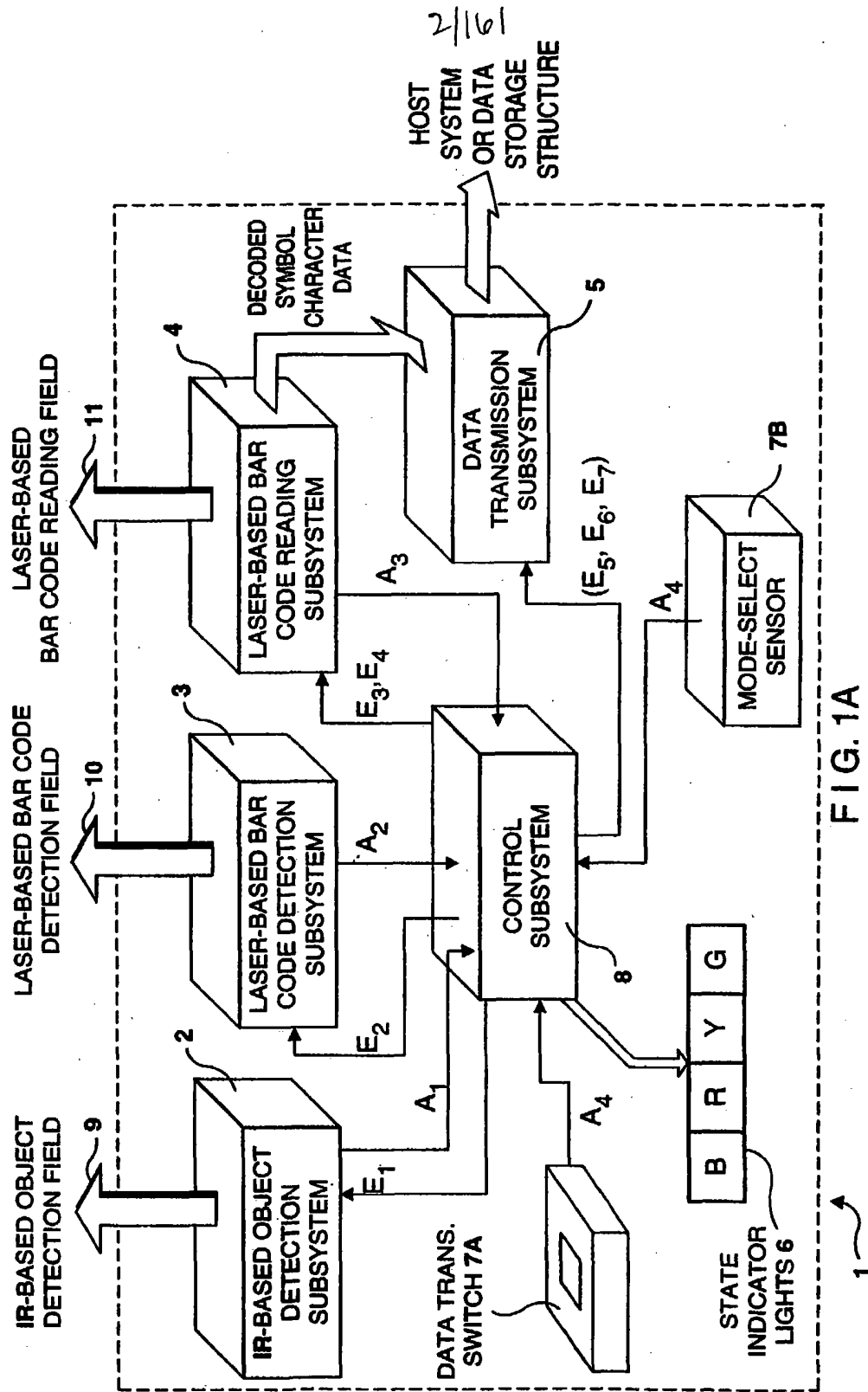


FIG. 1



3/161

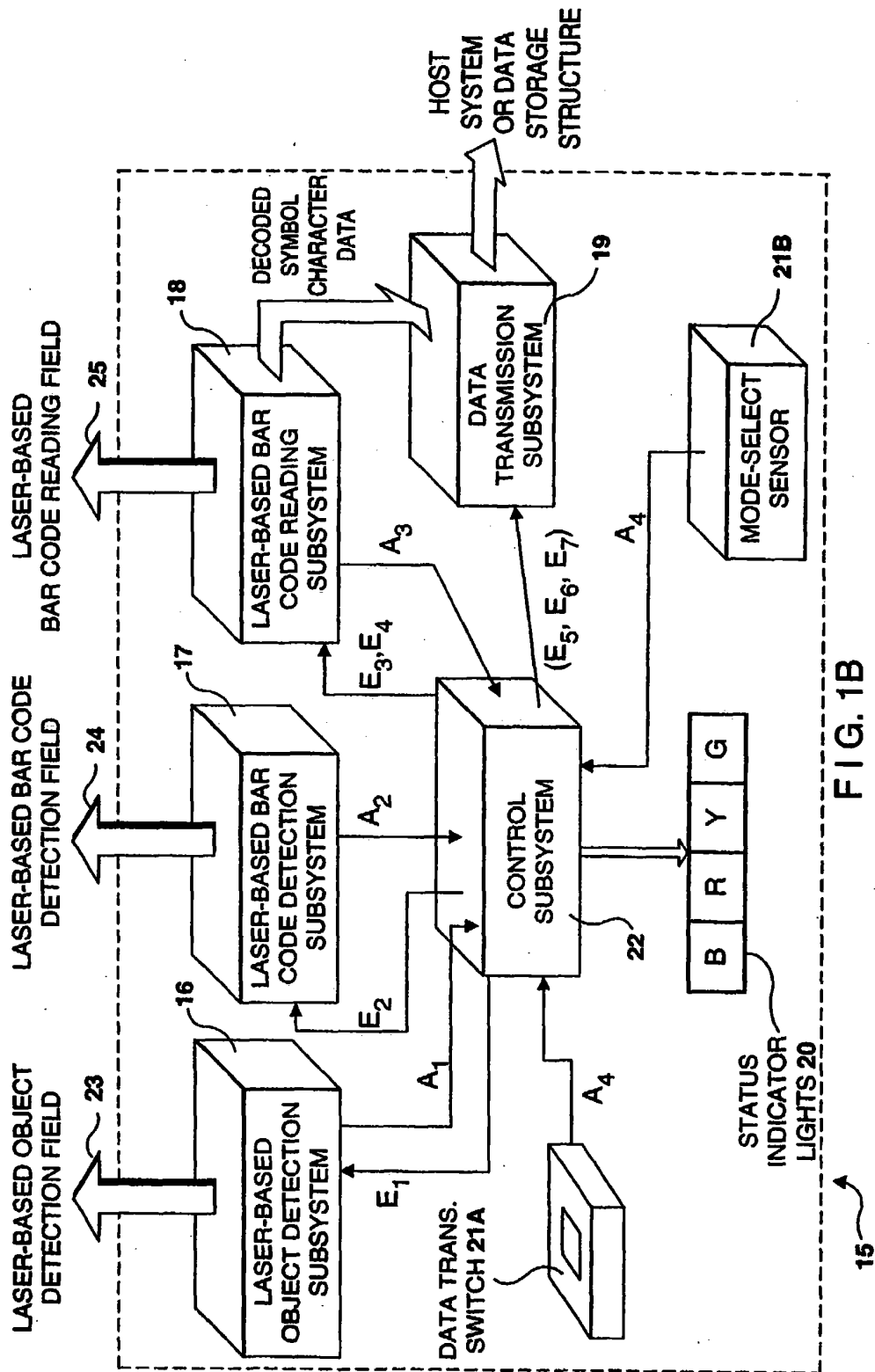
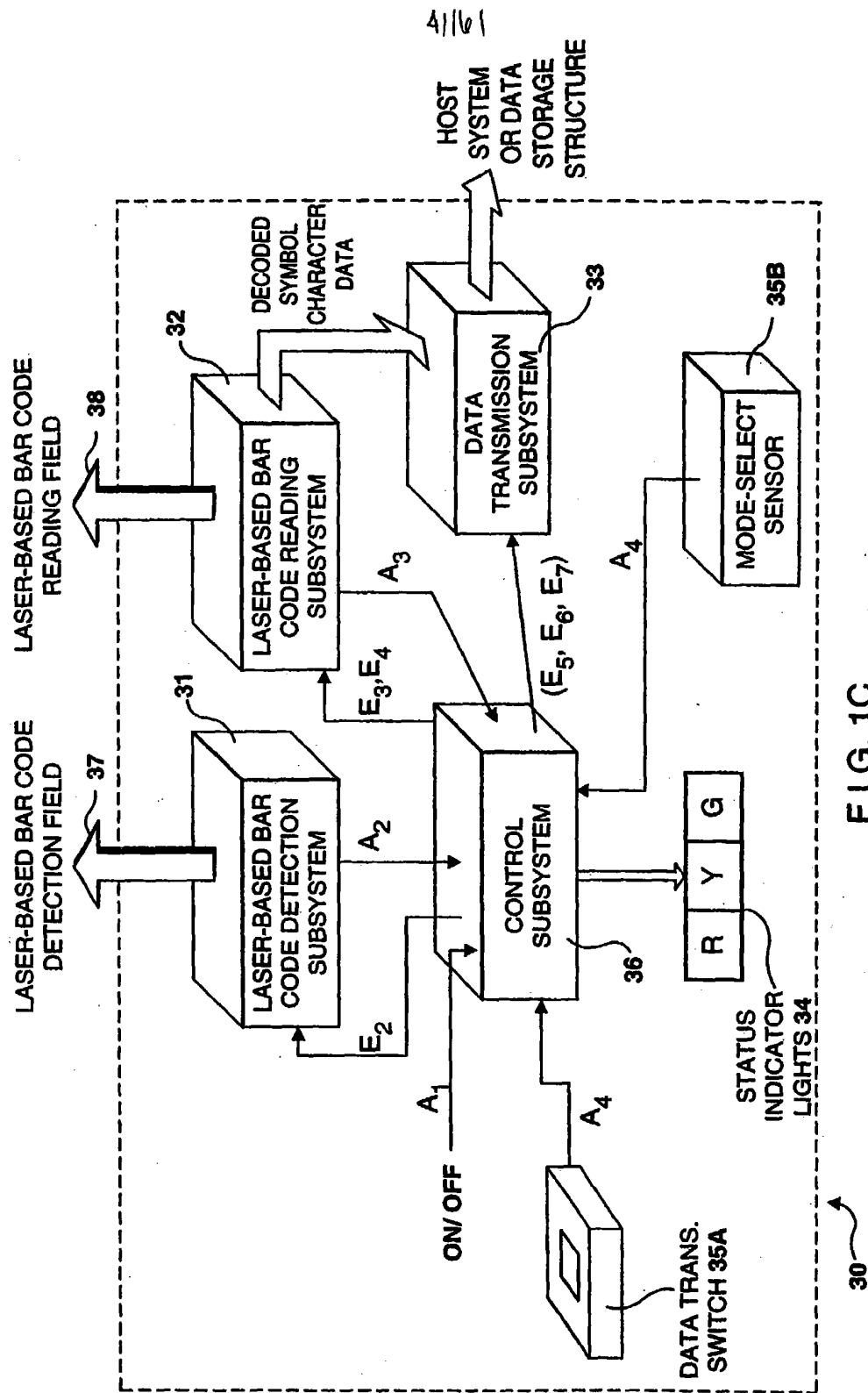


FIG. 1B



5/101

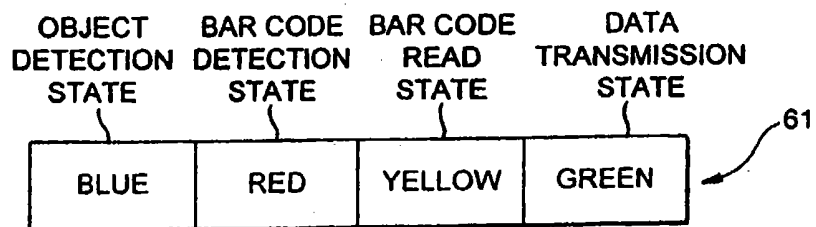
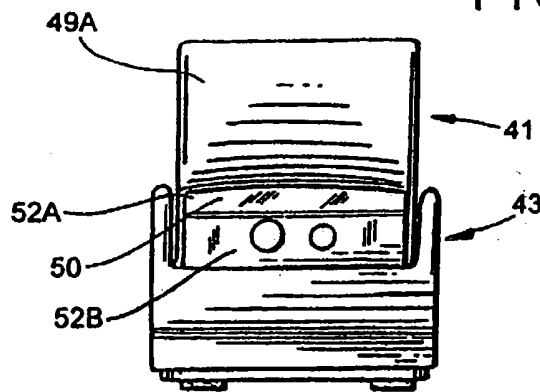
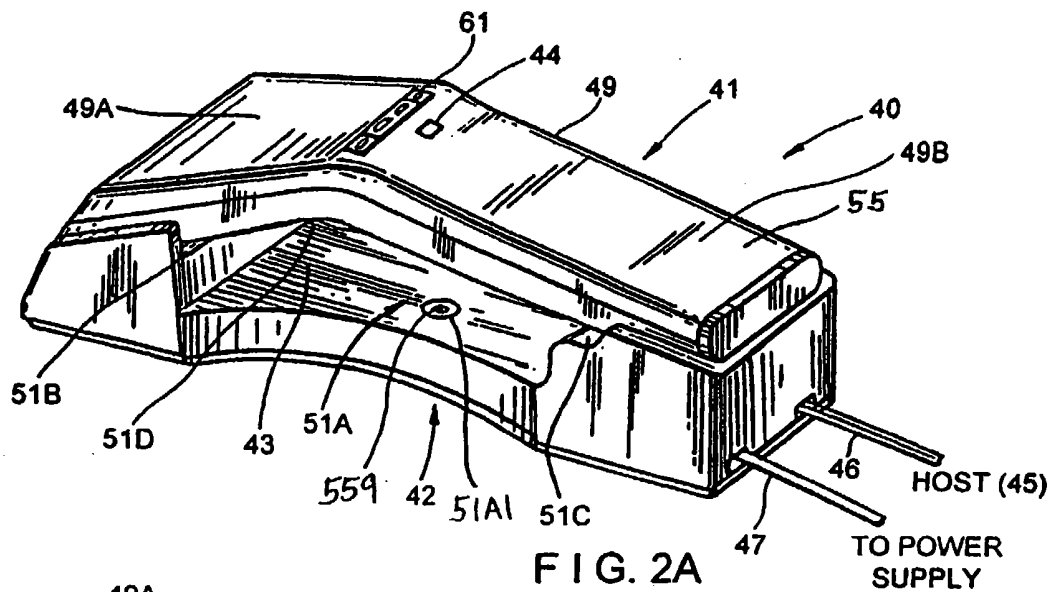
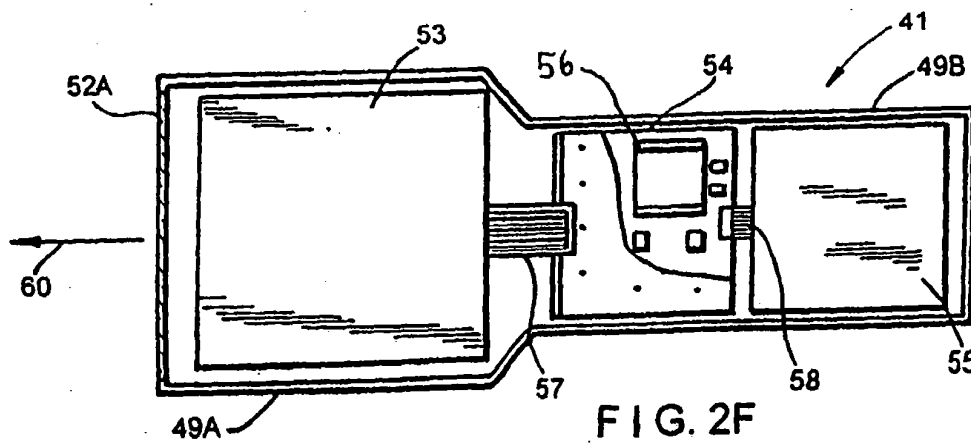
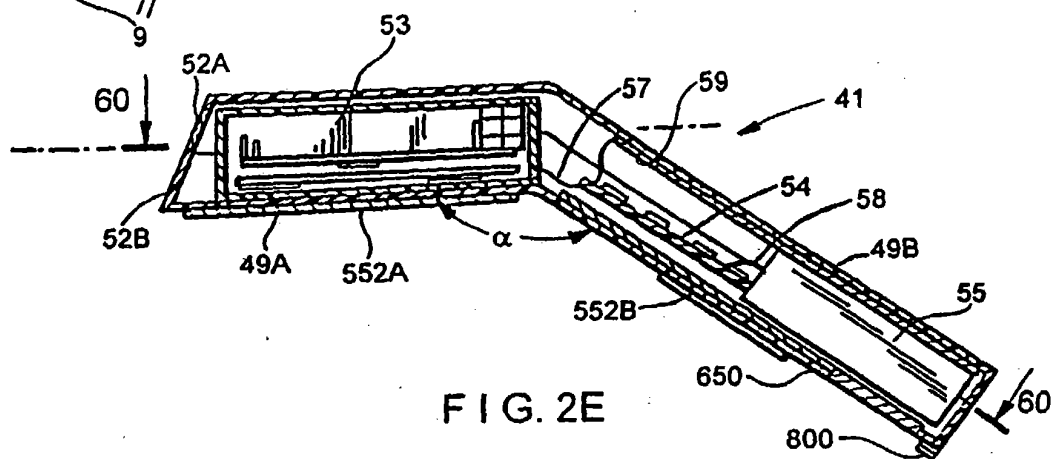


FIG. 2C



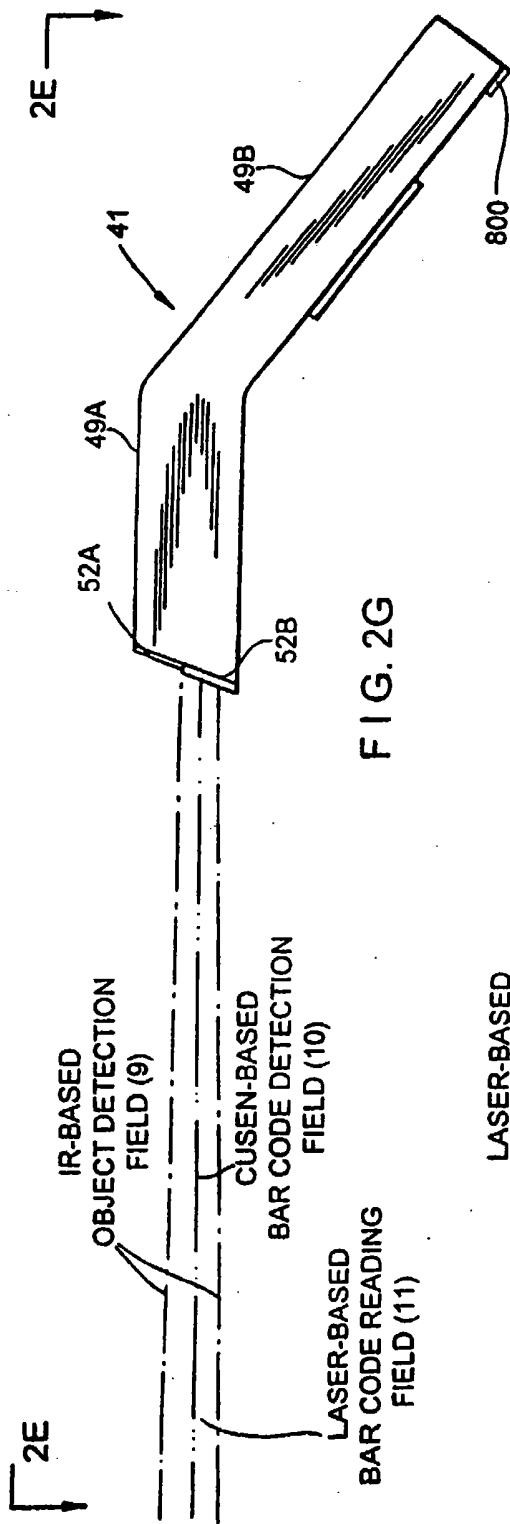


FIG. 2G

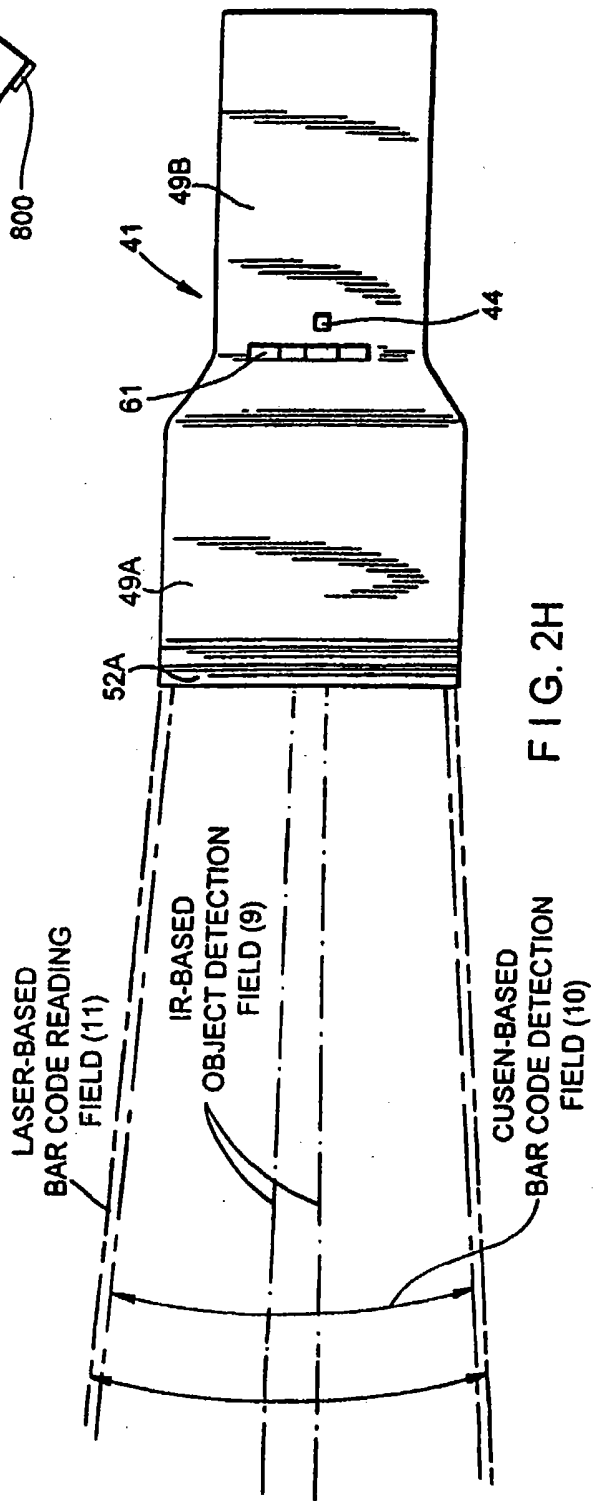


FIG. 2H

B/121

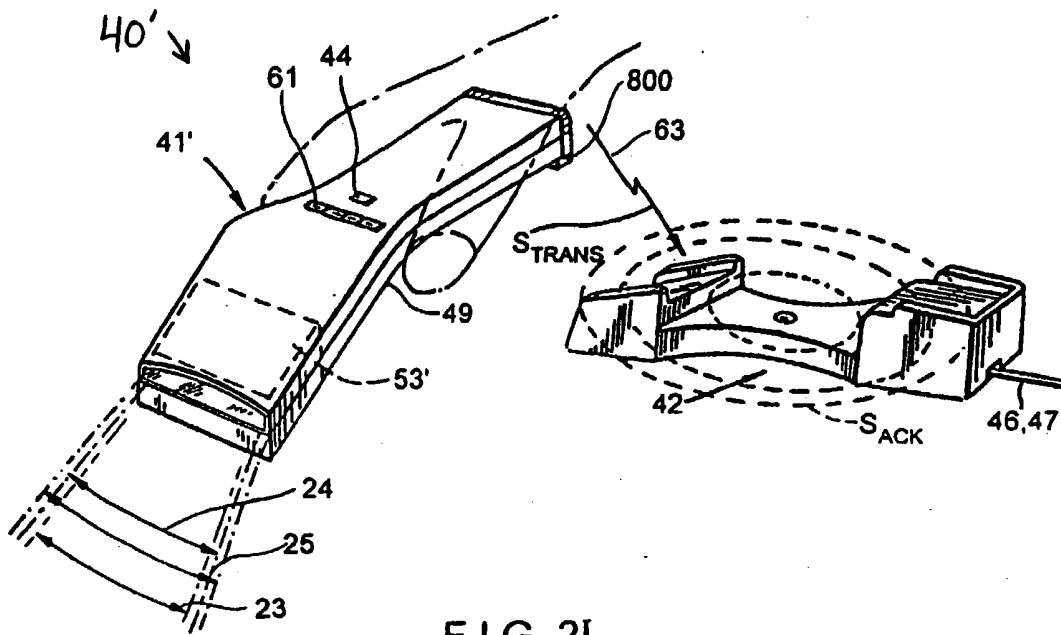


FIG. 2I

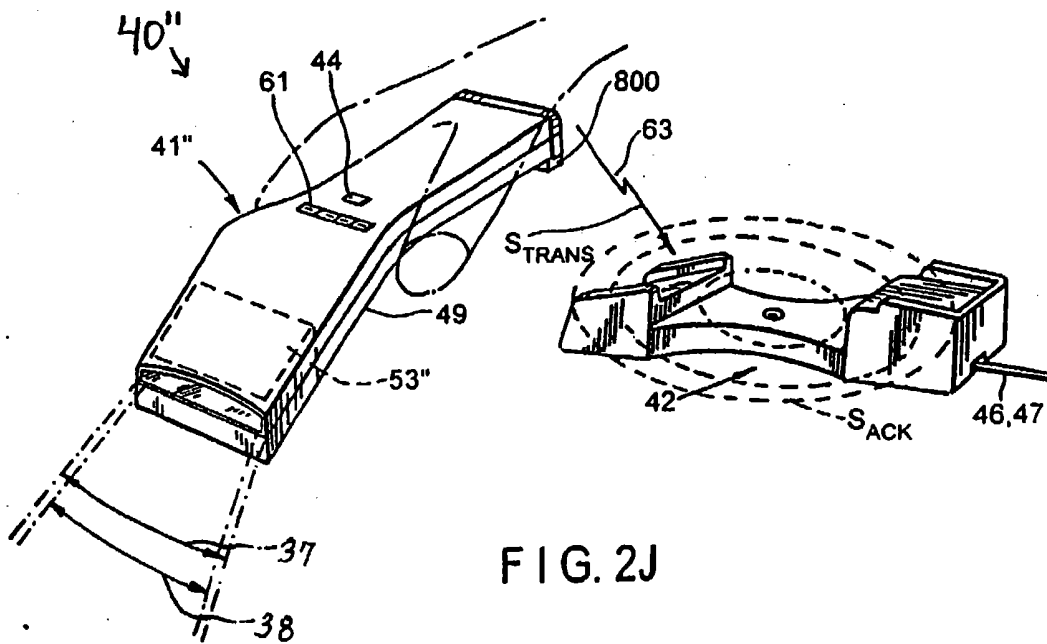
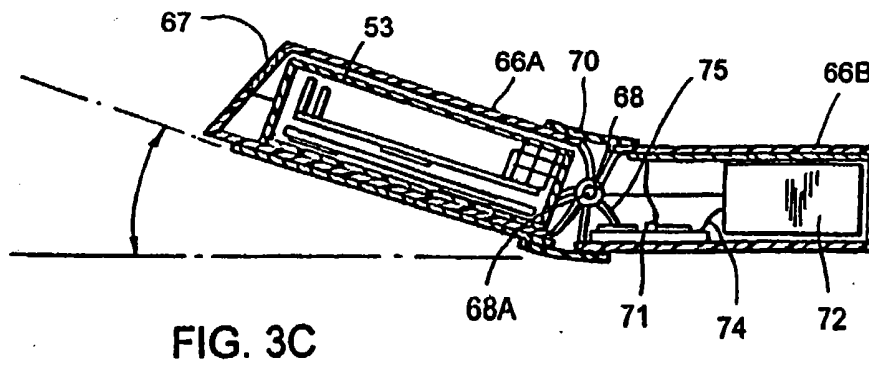
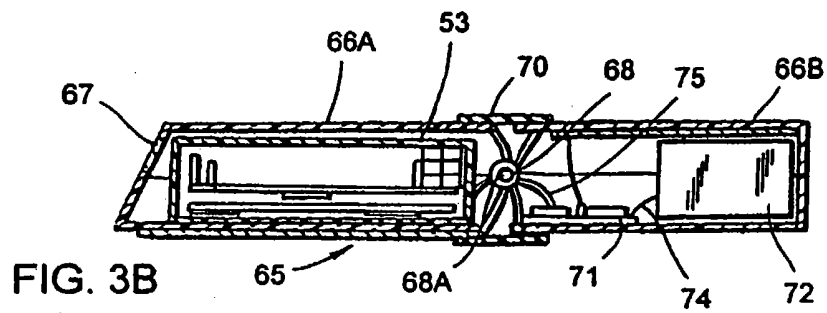
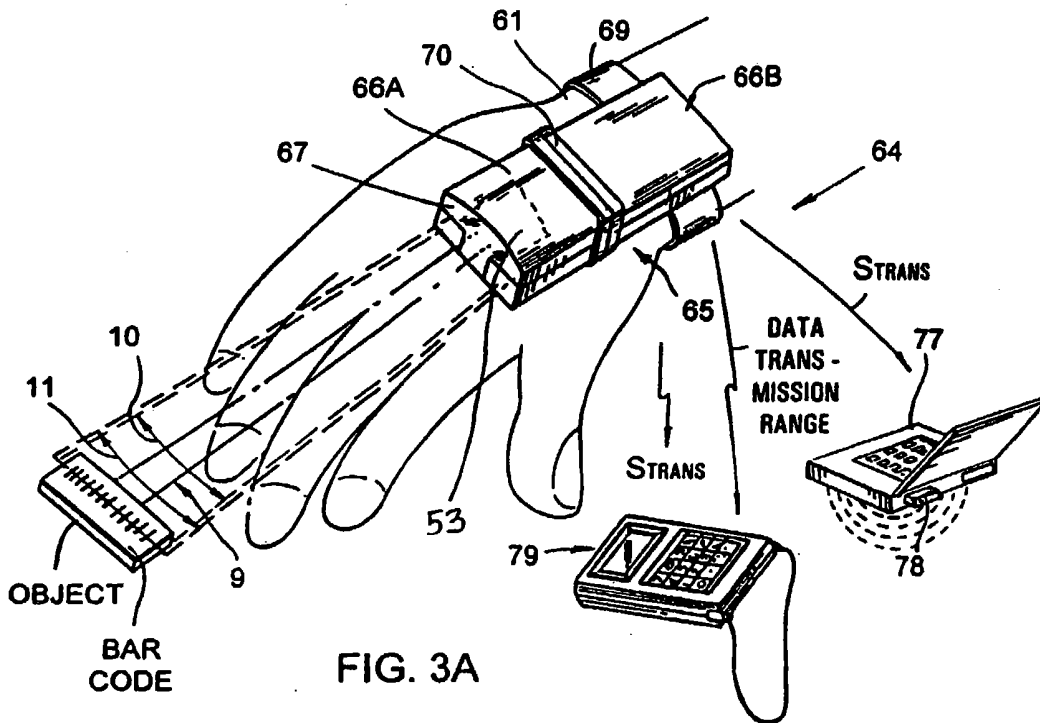


FIG. 2J

9/161



10/161

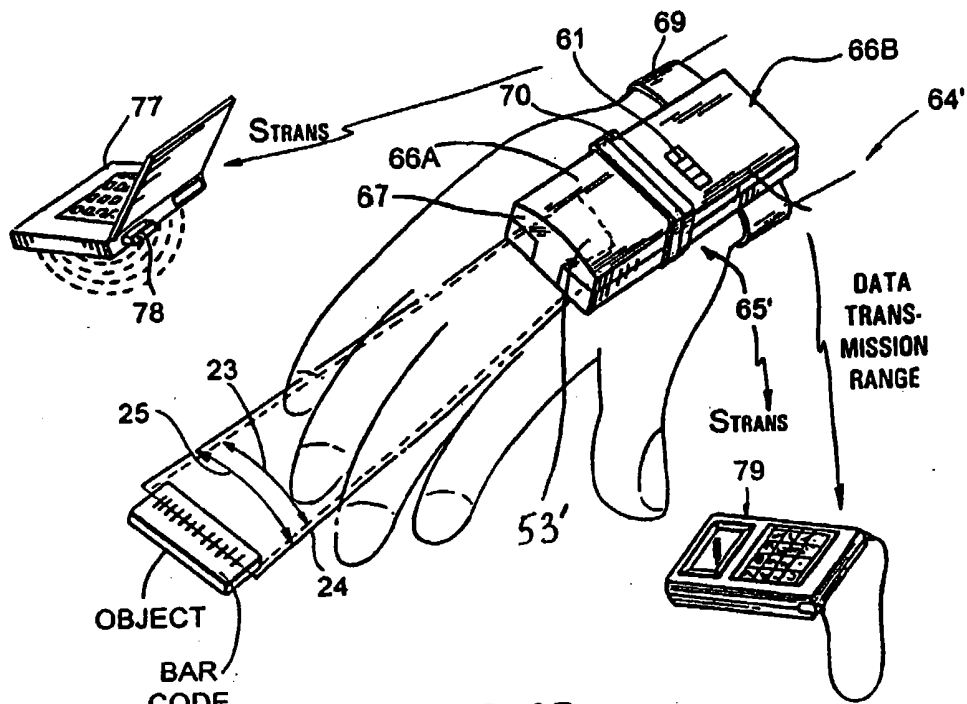


FIG. 3D

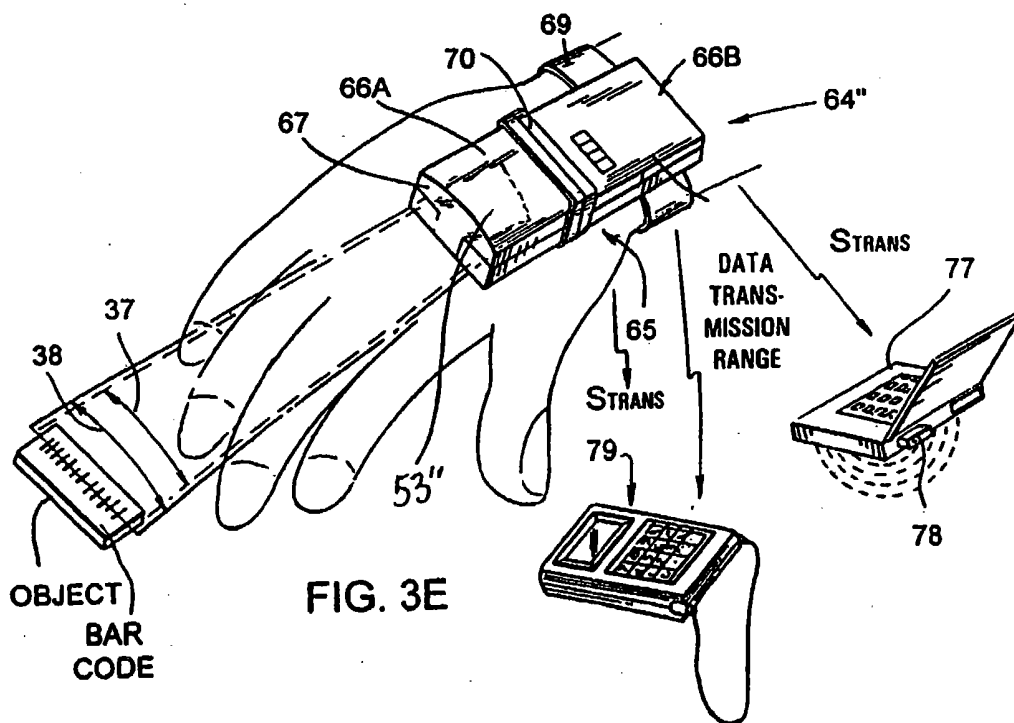
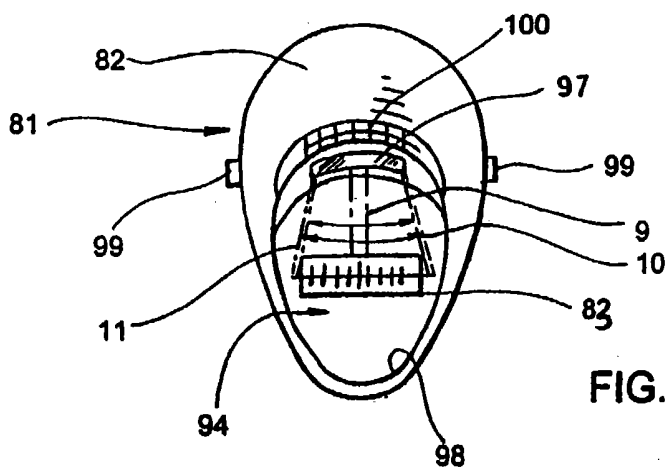
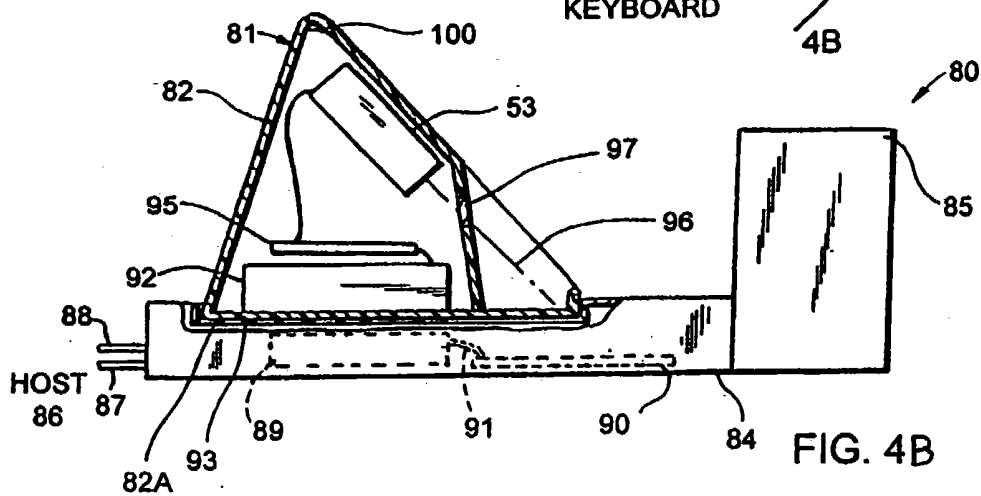
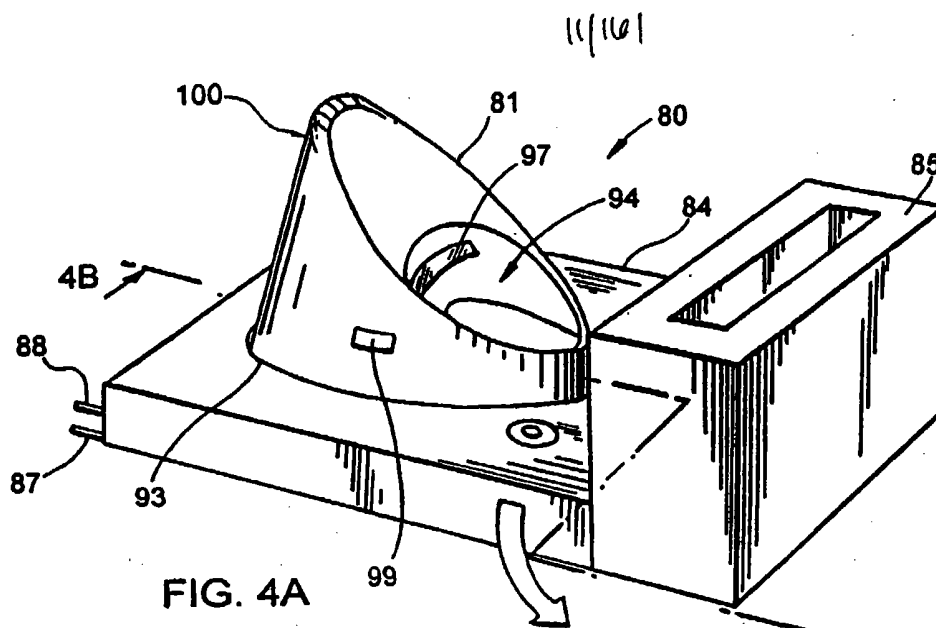
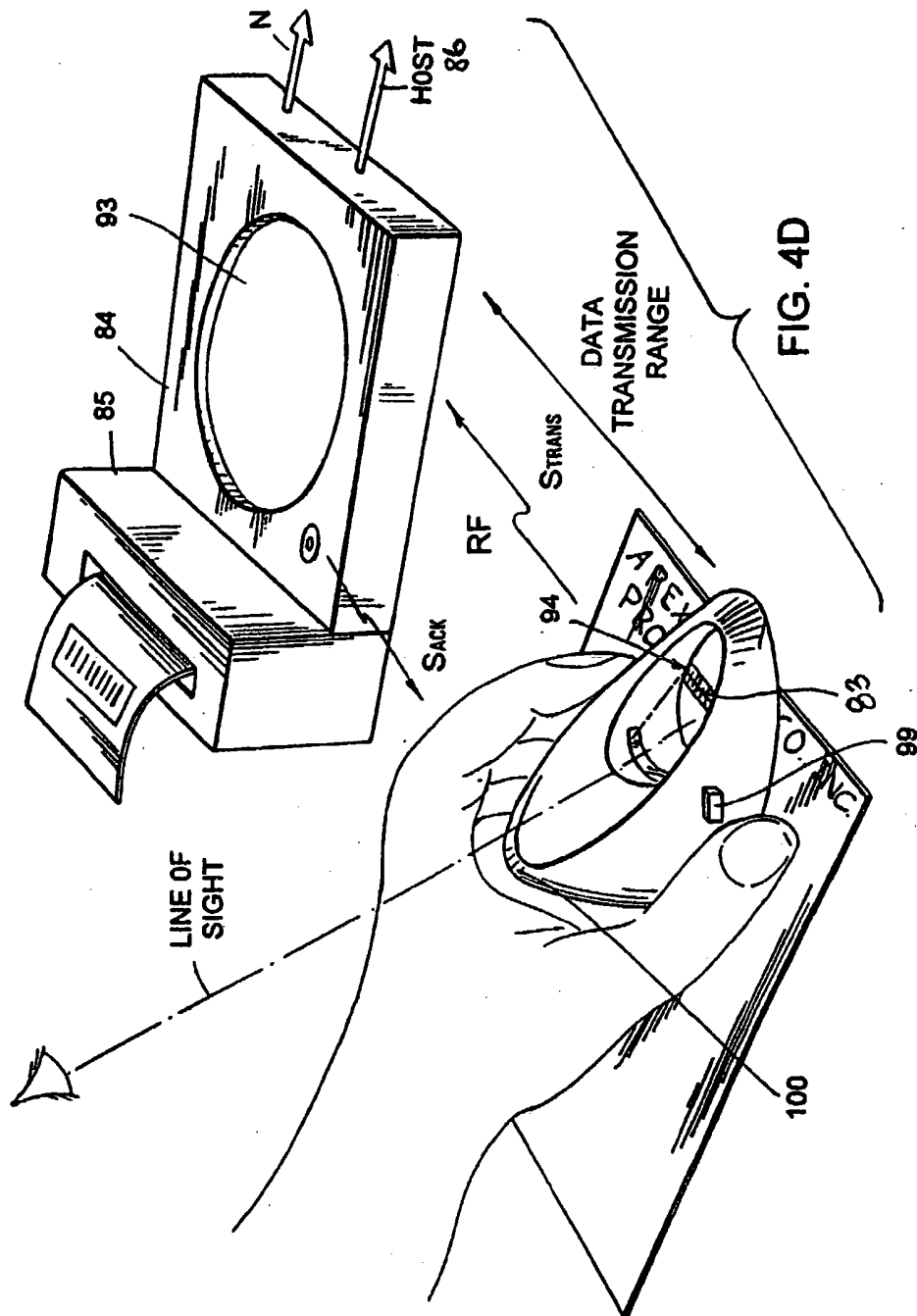


FIG. 3E



12/161



13/161

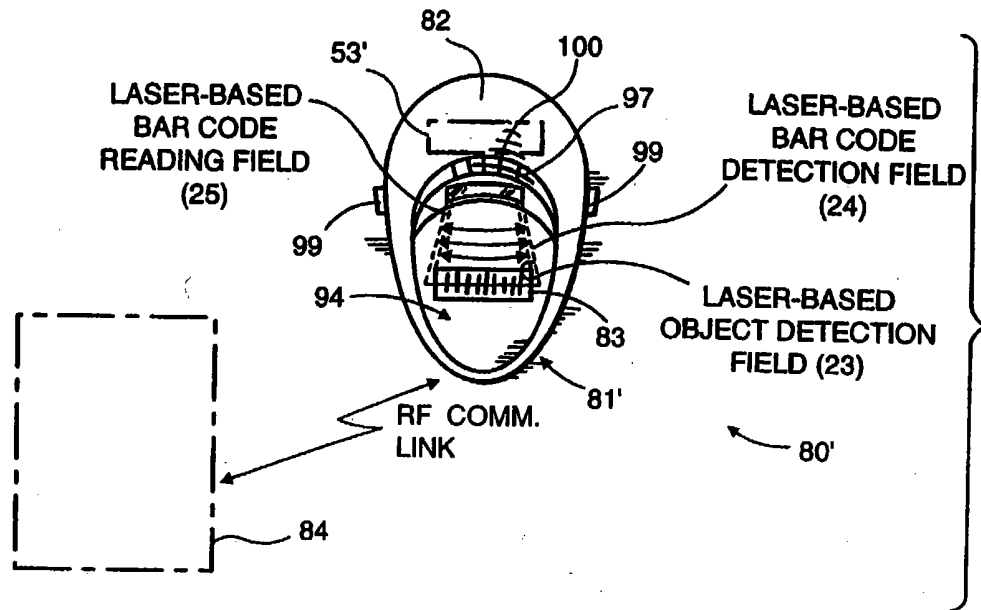


FIG. 4E

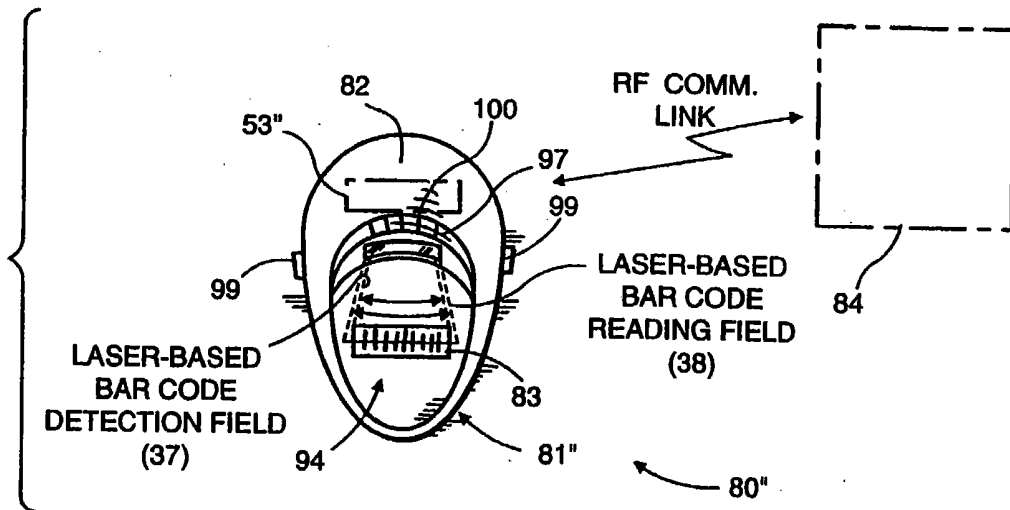


FIG. 4F

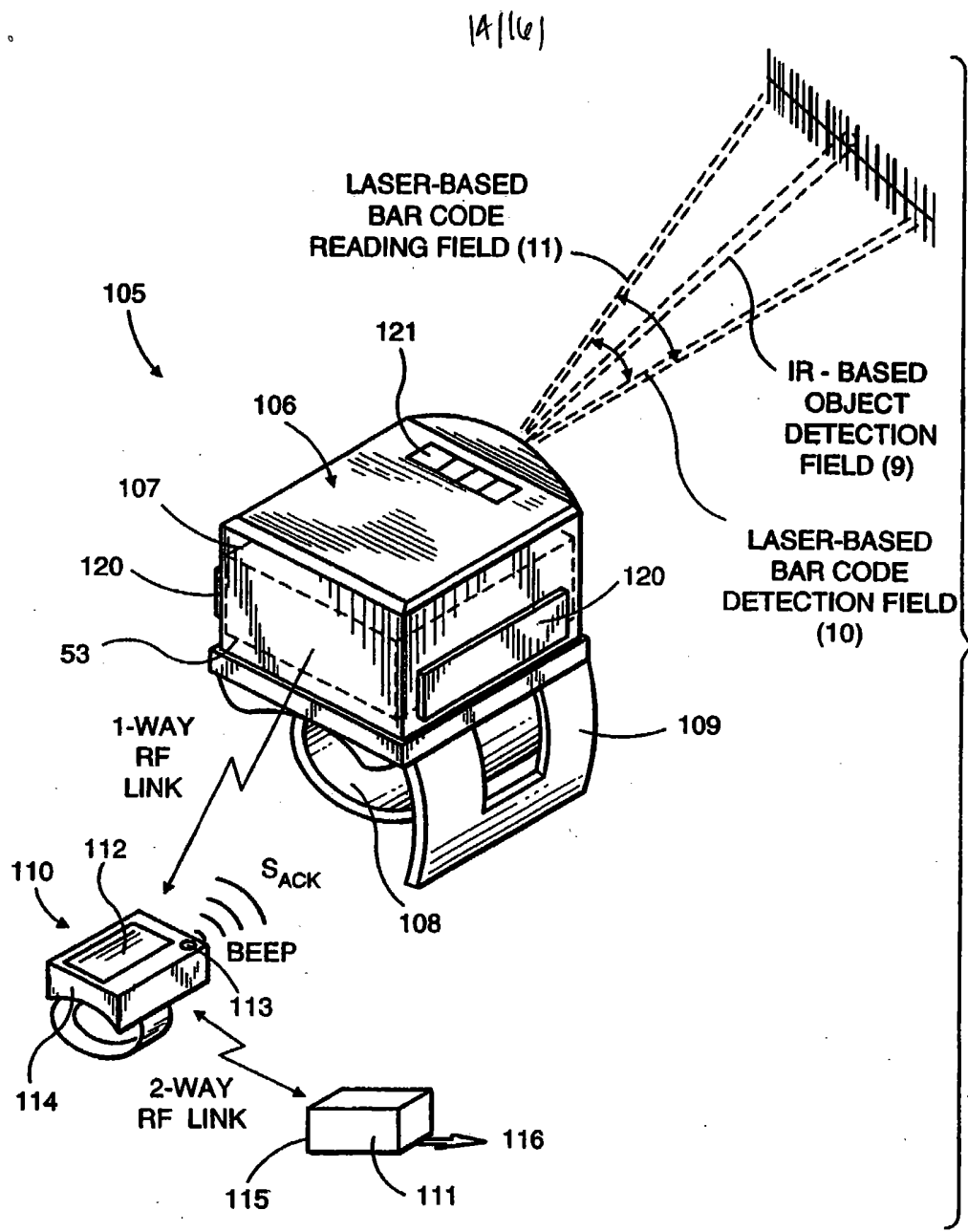


FIG. 5A

15/161

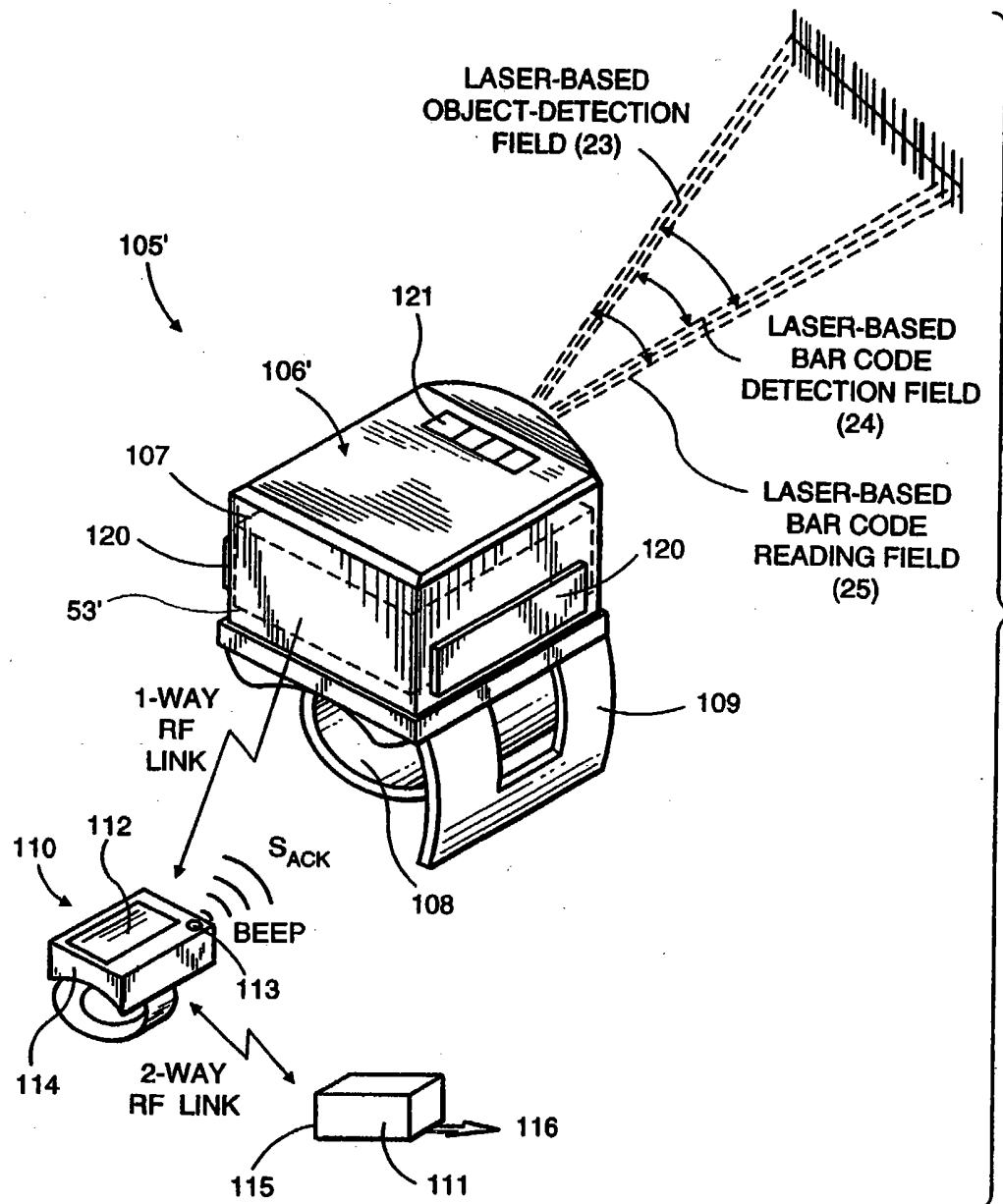


FIG. 5B

16/161

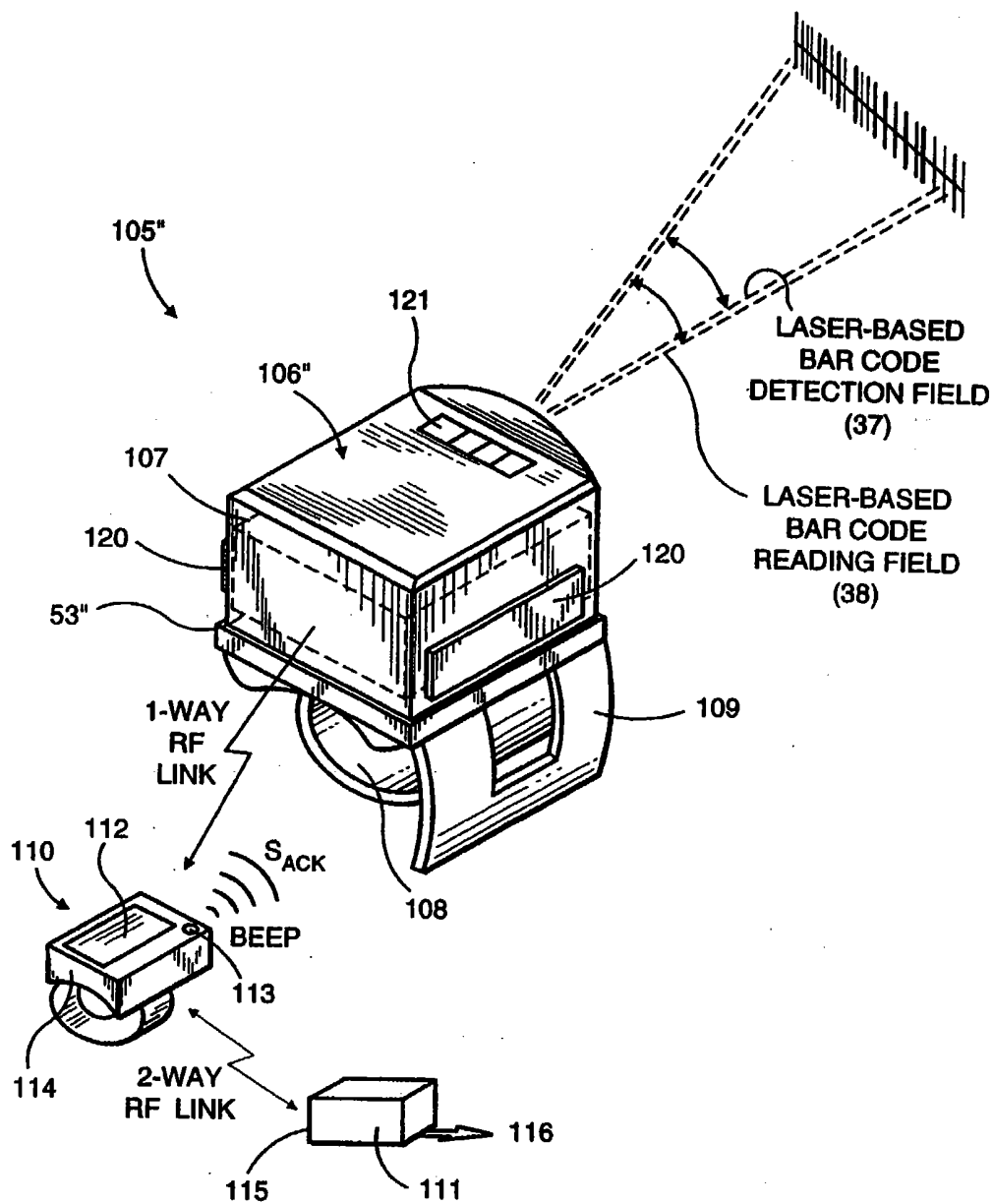


FIG. 5C

17/161

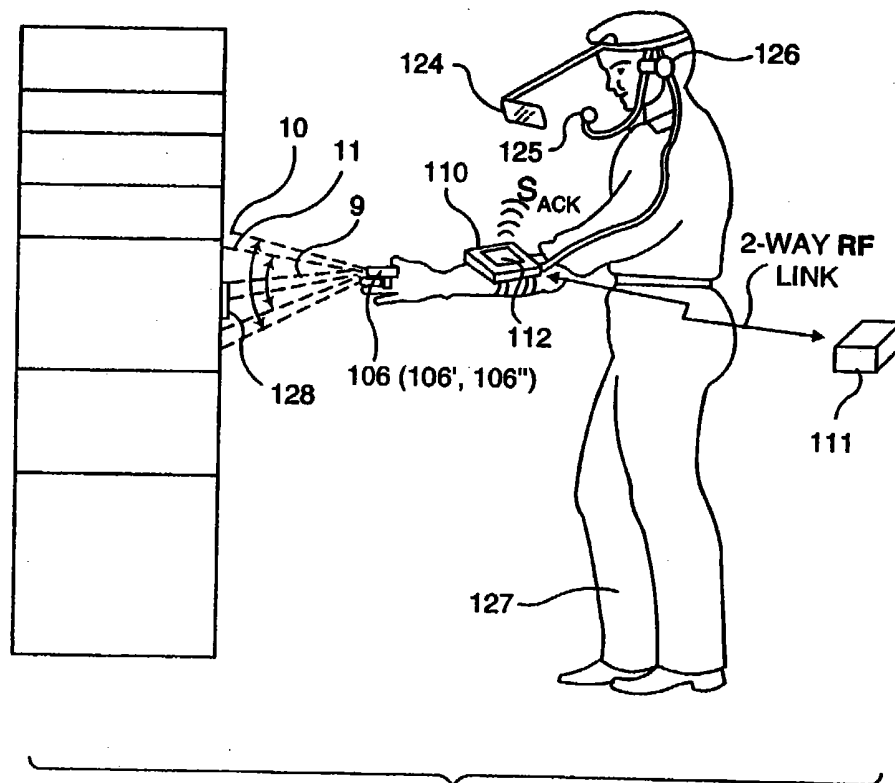
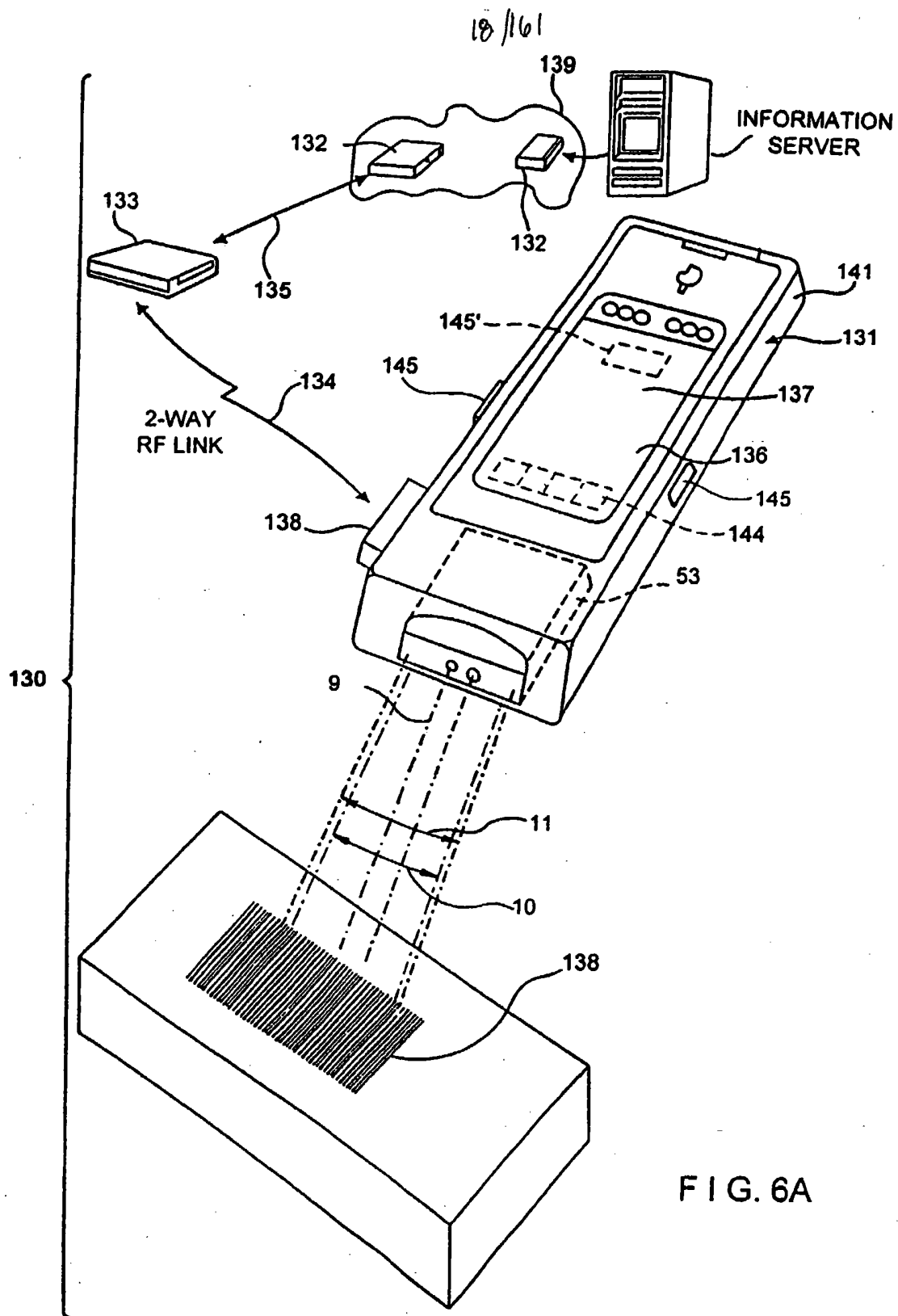


FIG. 5D



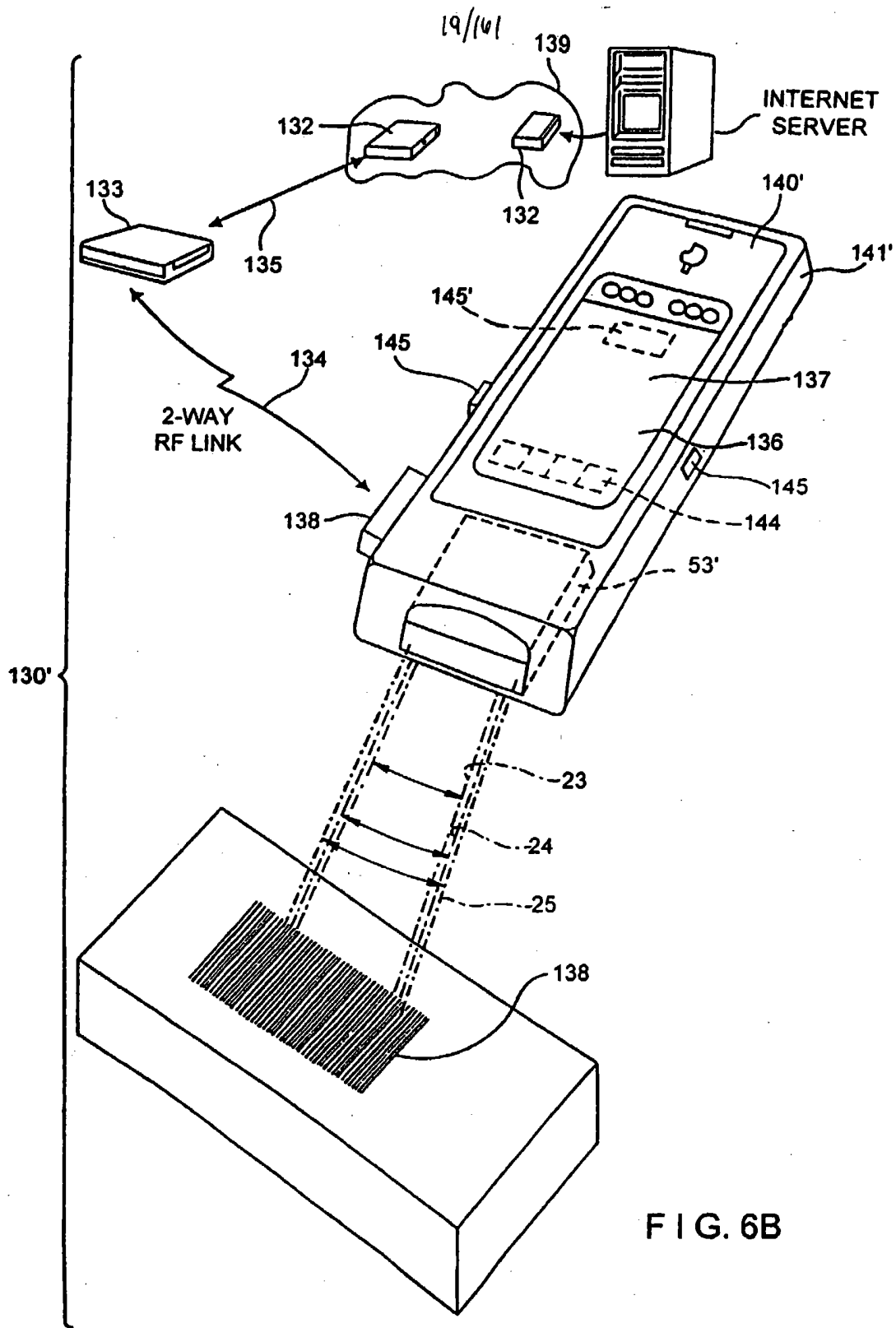


FIG. 6B

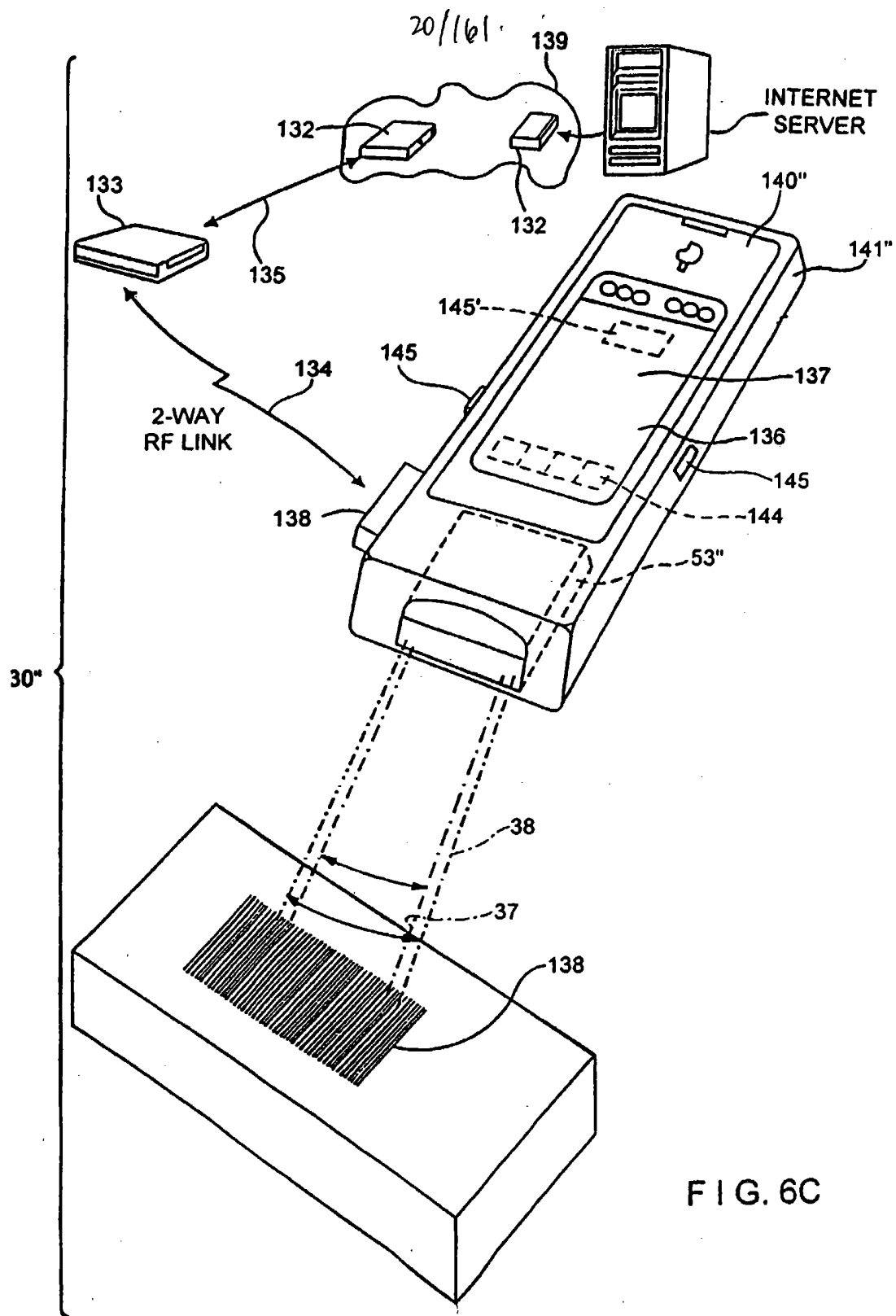


FIG. 6C

FIG. 7A

22/161

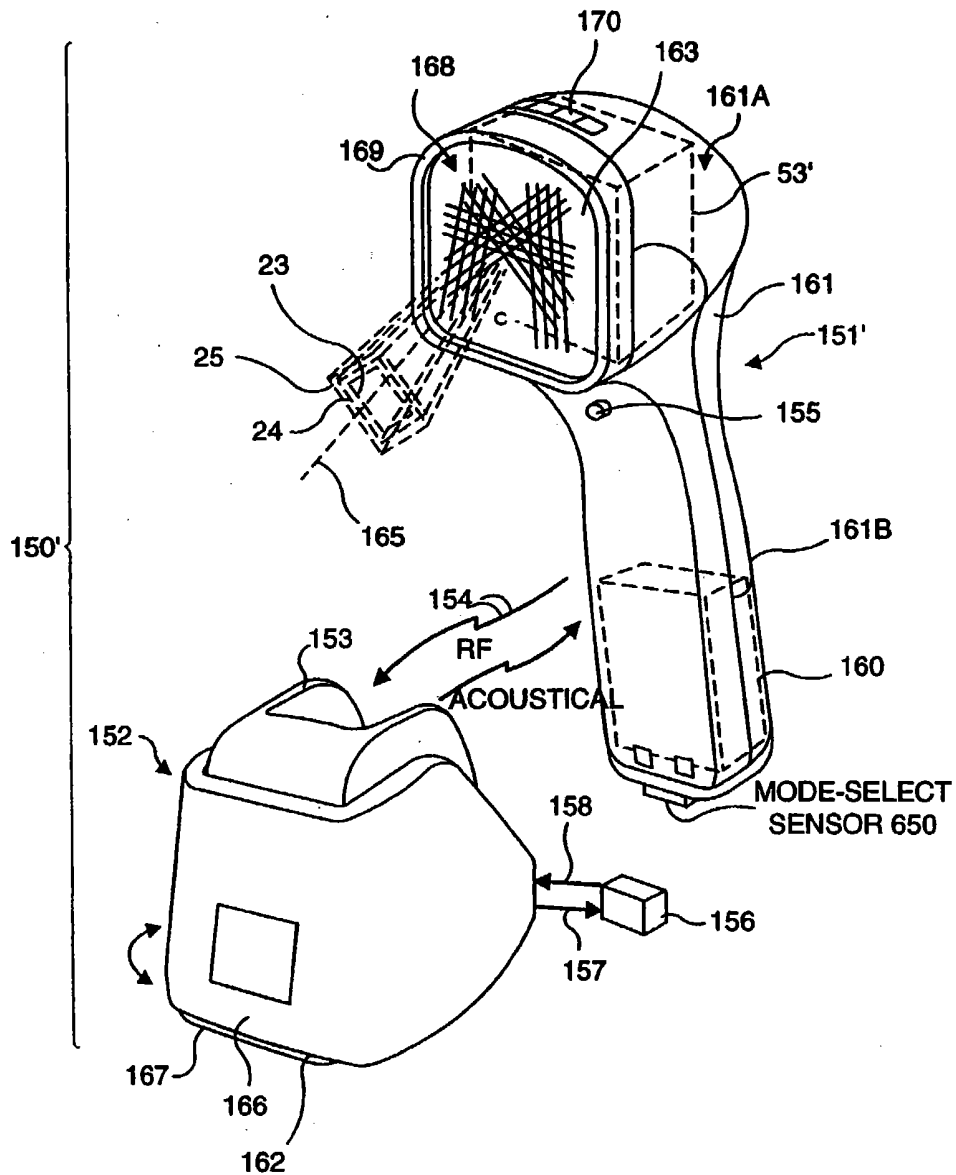


FIG. 7B

23/161

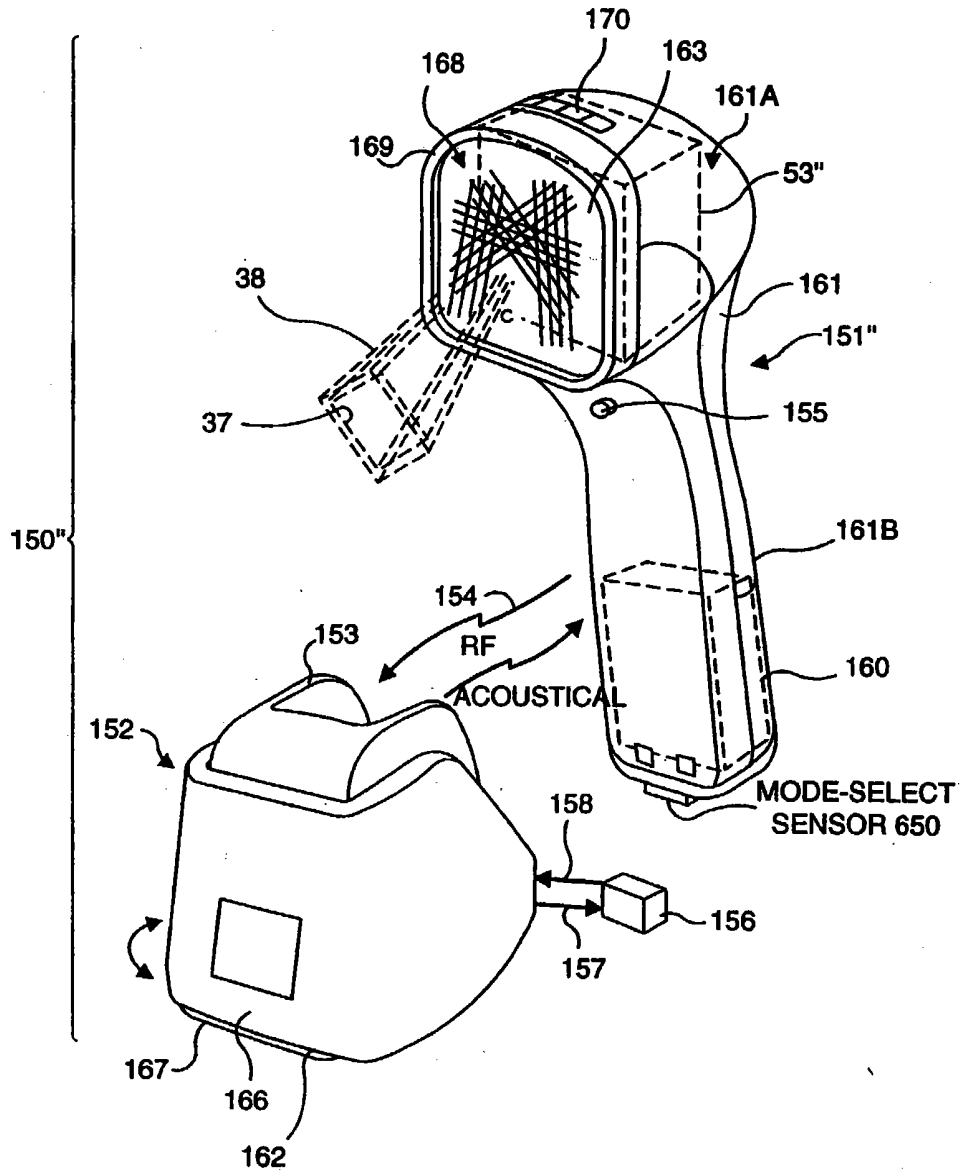


FIG. 7C



FIG. 8A

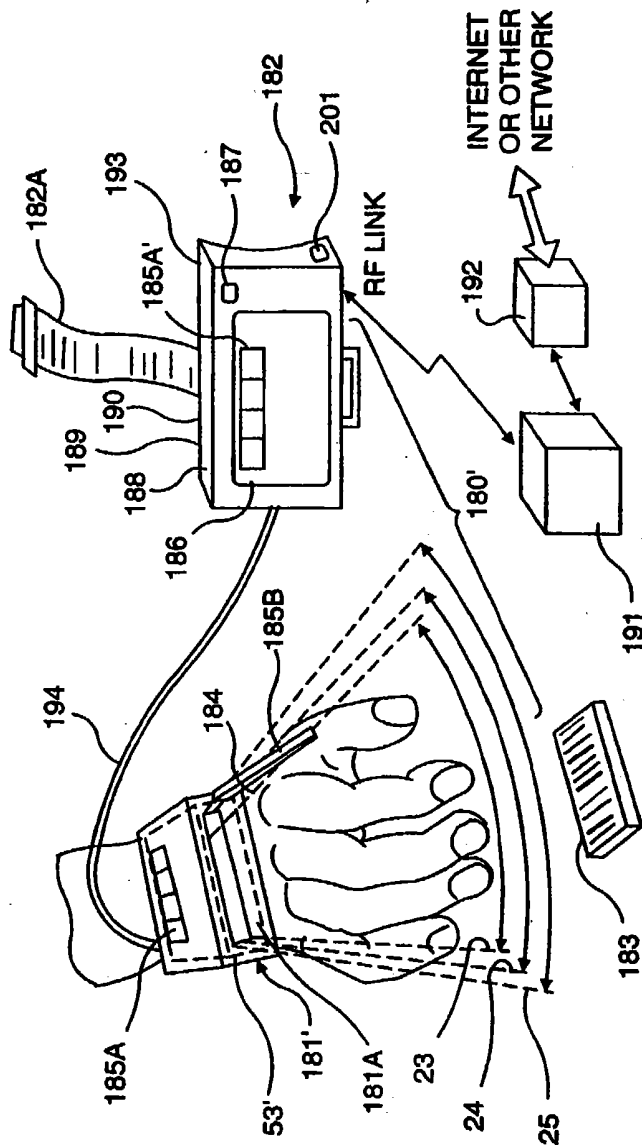


FIG. 8B

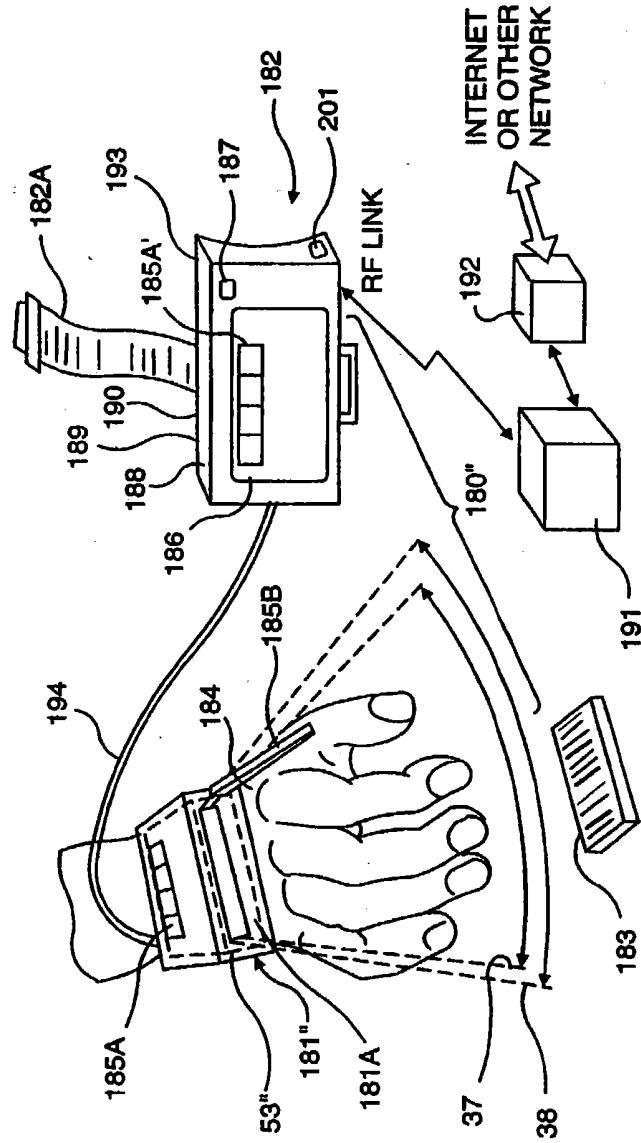


FIG. 8C

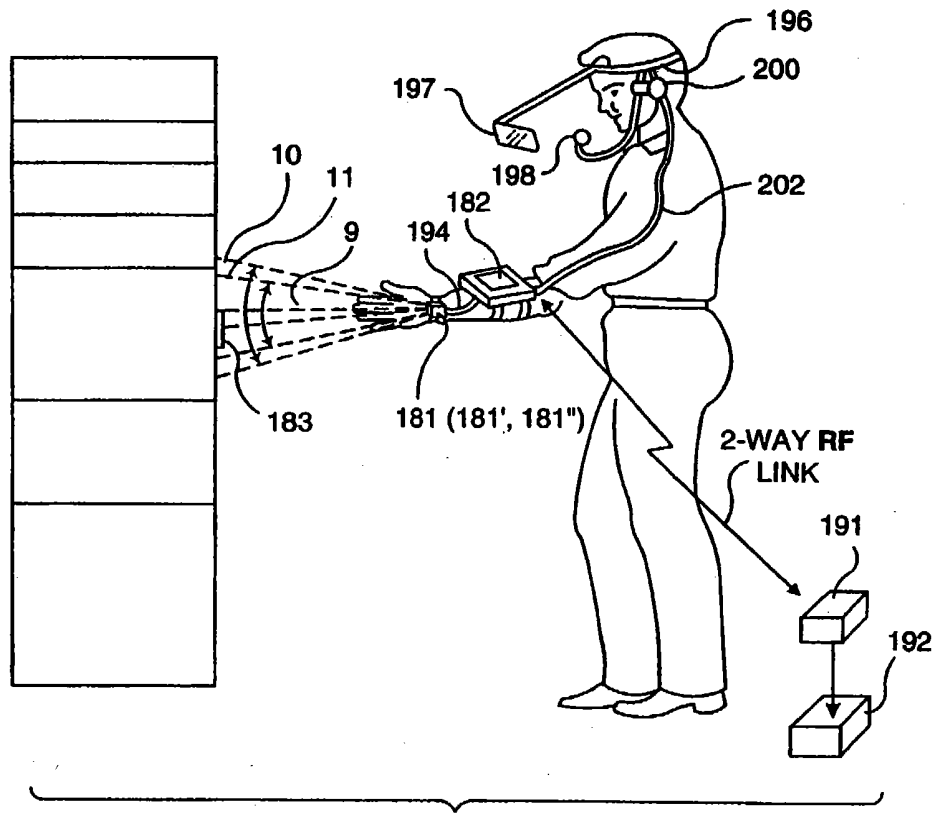
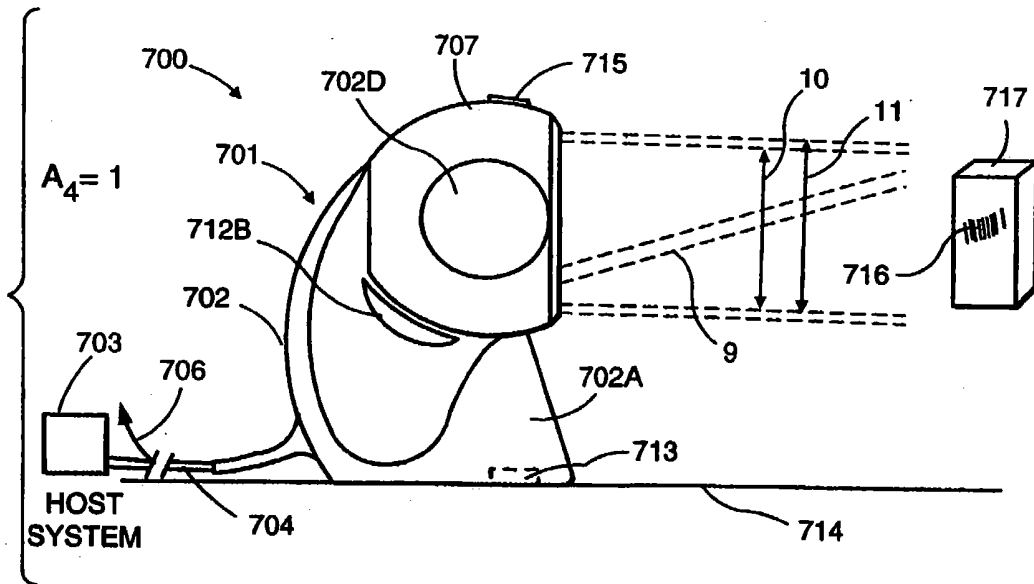
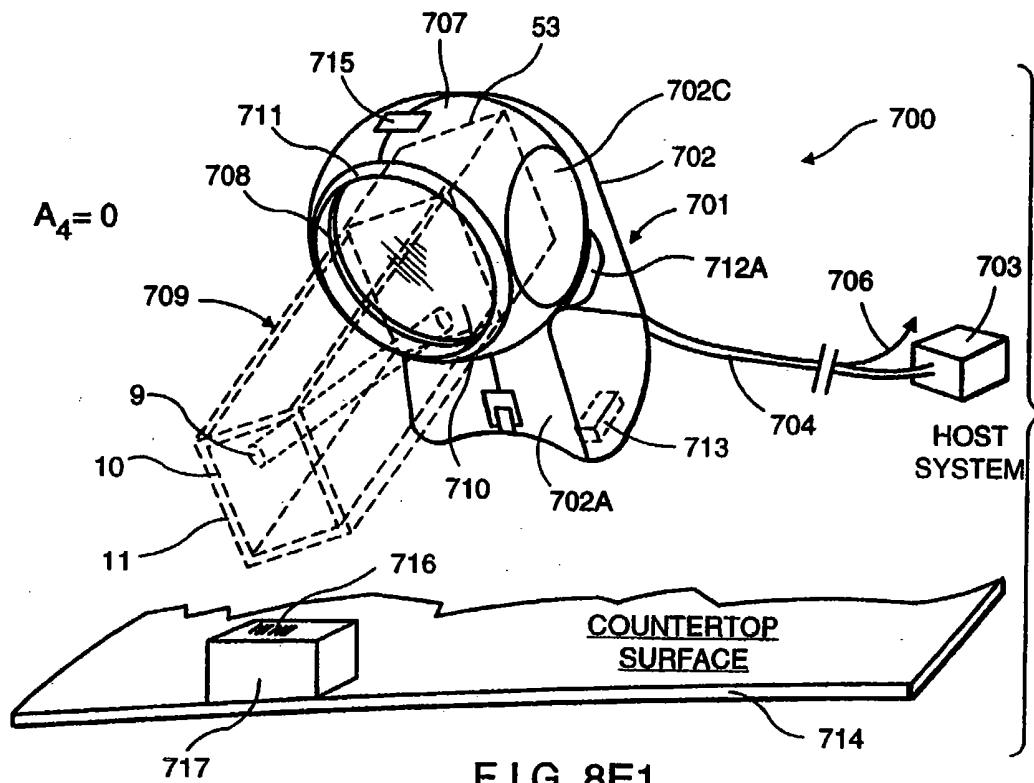


FIG. 8D

28/161



29/161

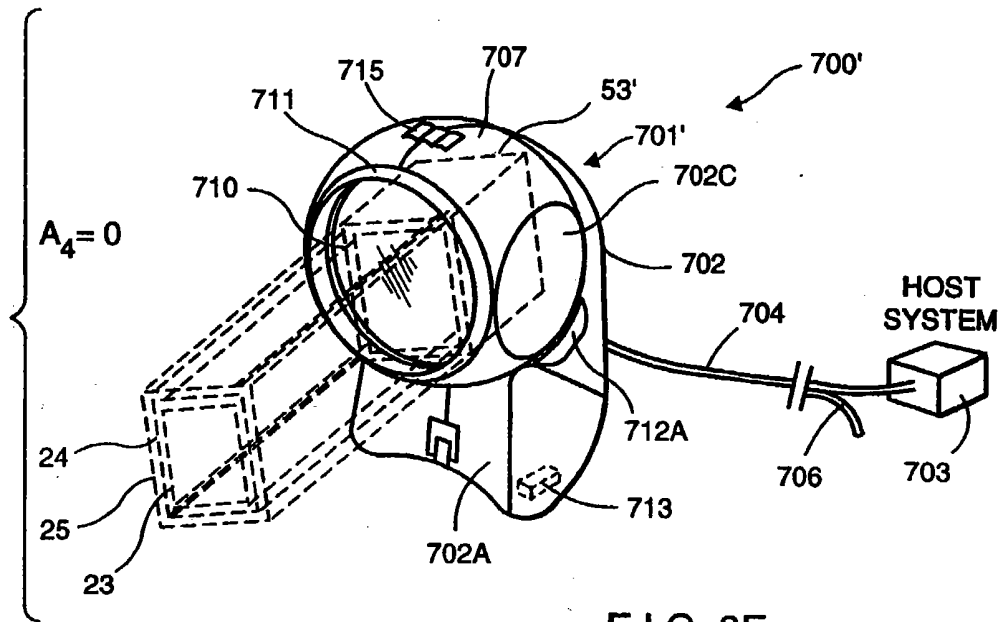


FIG. 8F

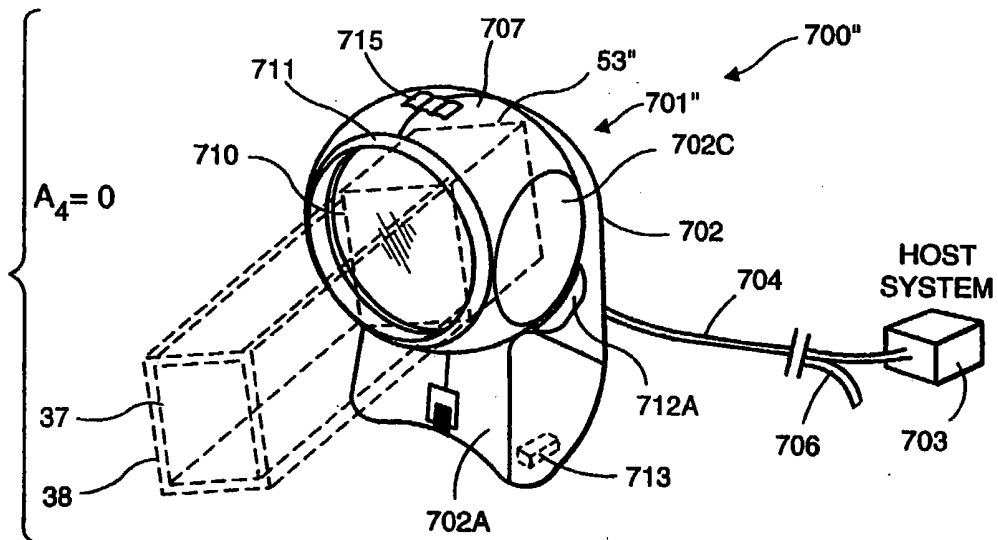


FIG. 8G

30/161

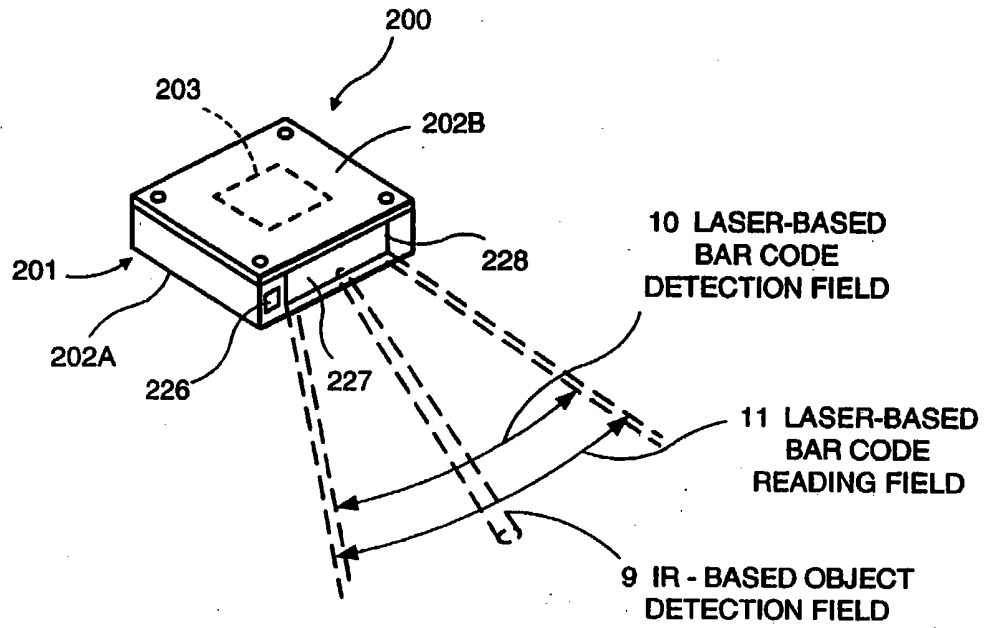


FIG. 9A

31/161

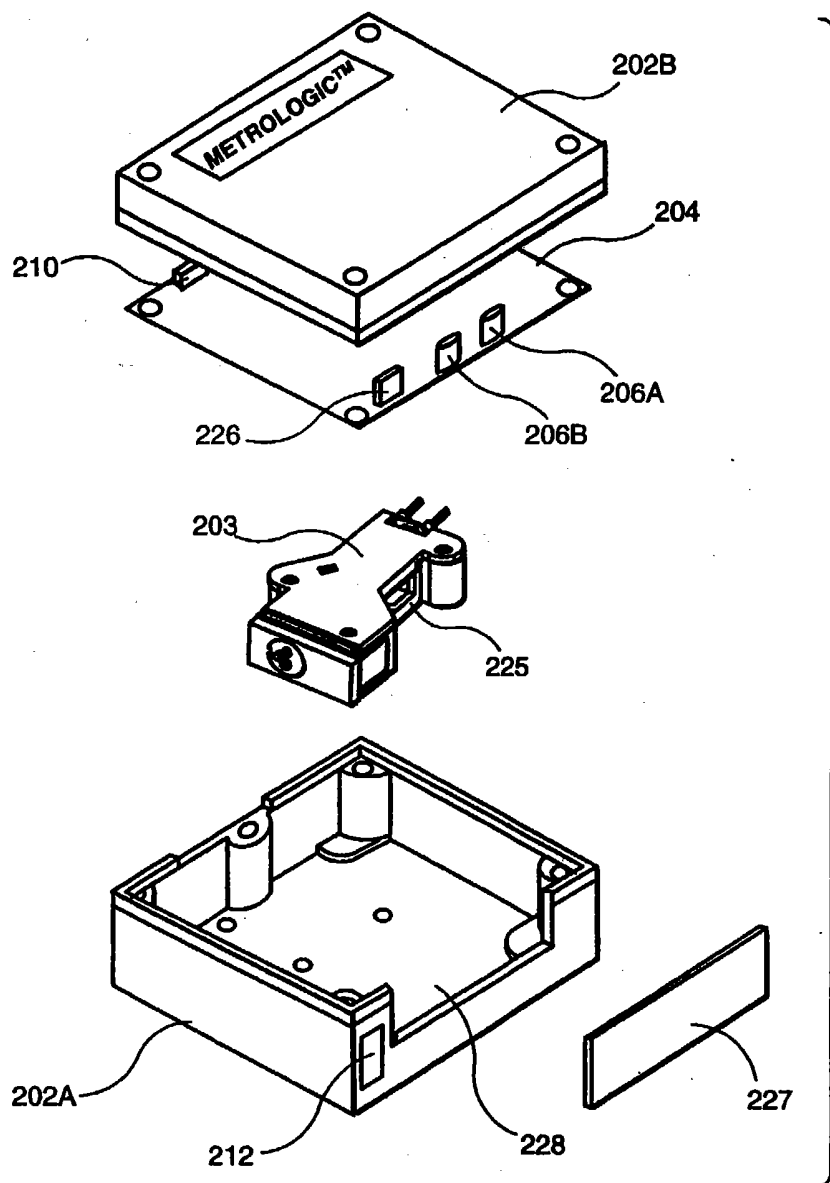


FIG. 9B

32/161

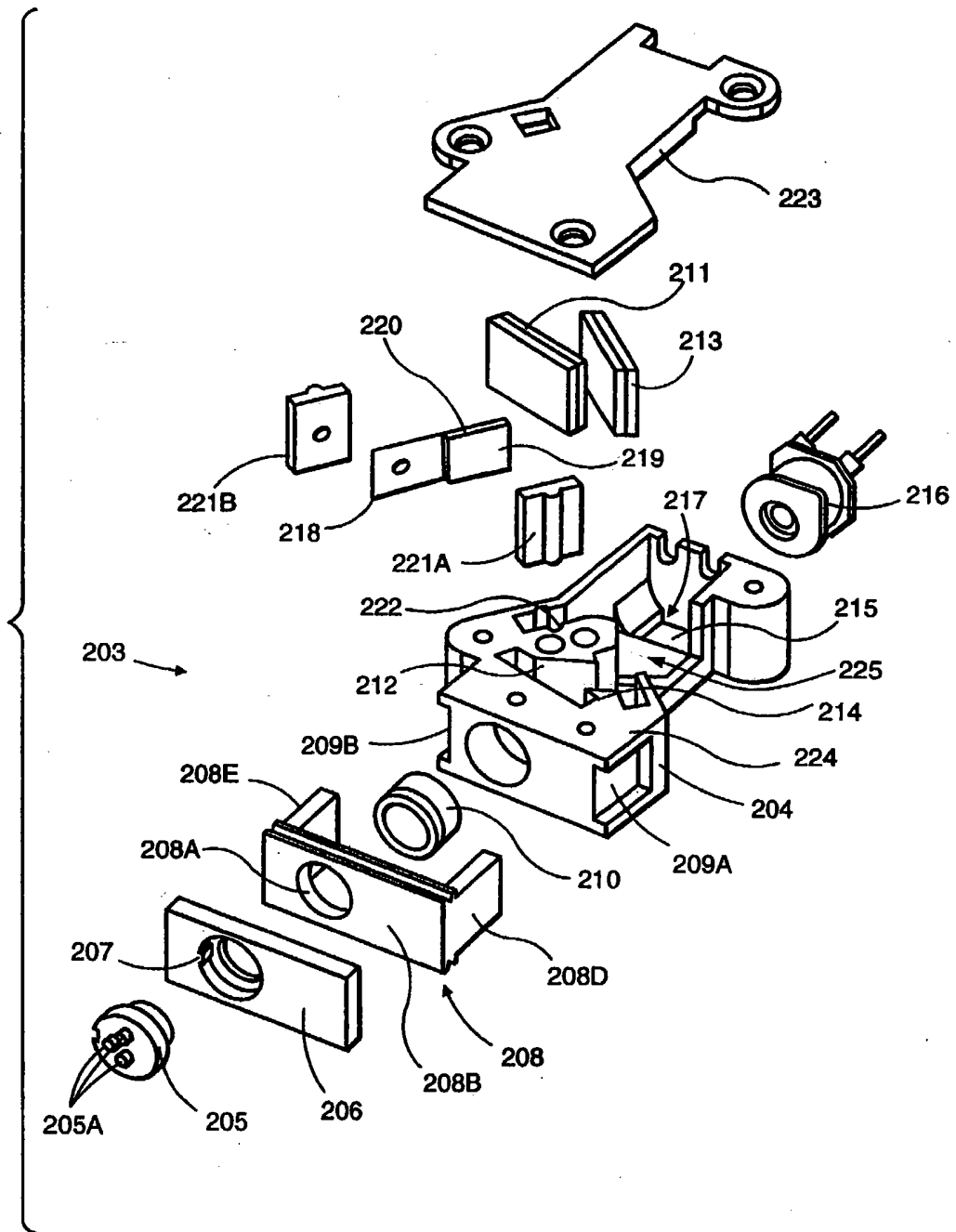


FIG. 9C

33/161

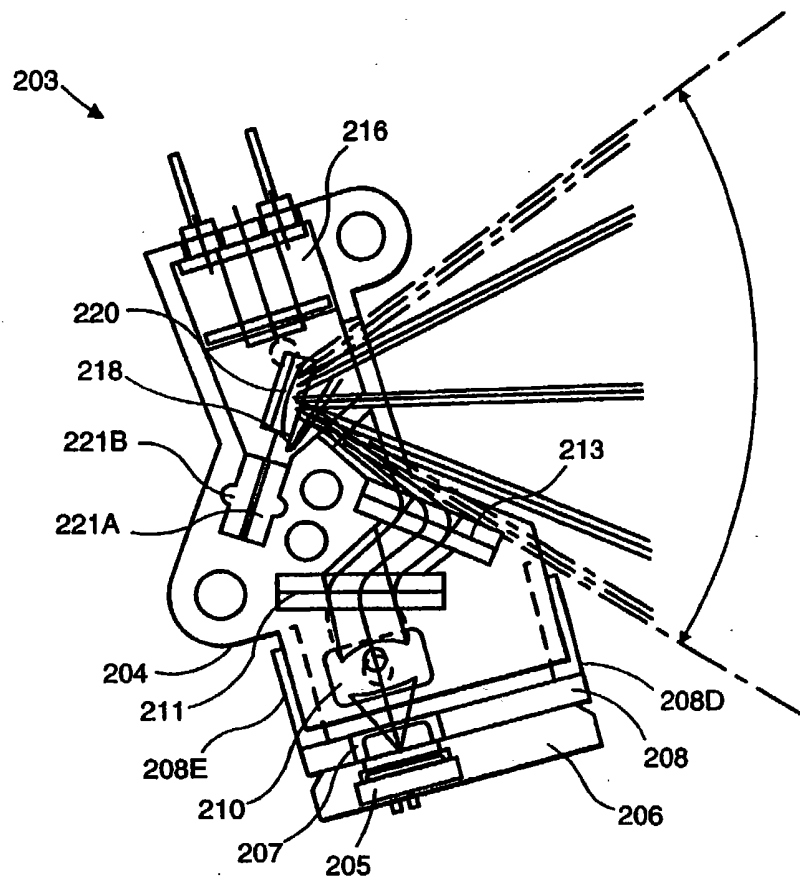


FIG. 9D

34/101

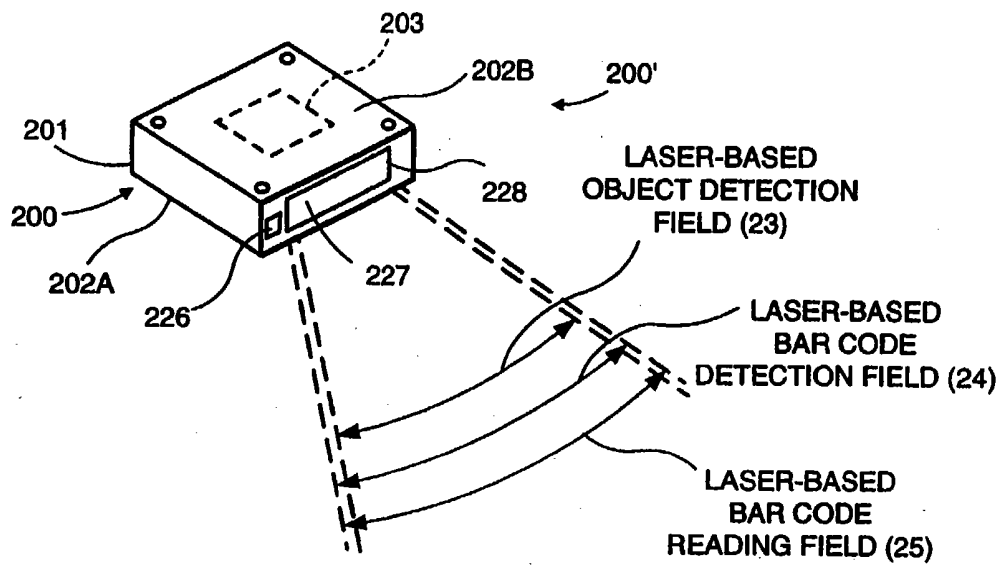


FIG. 9E

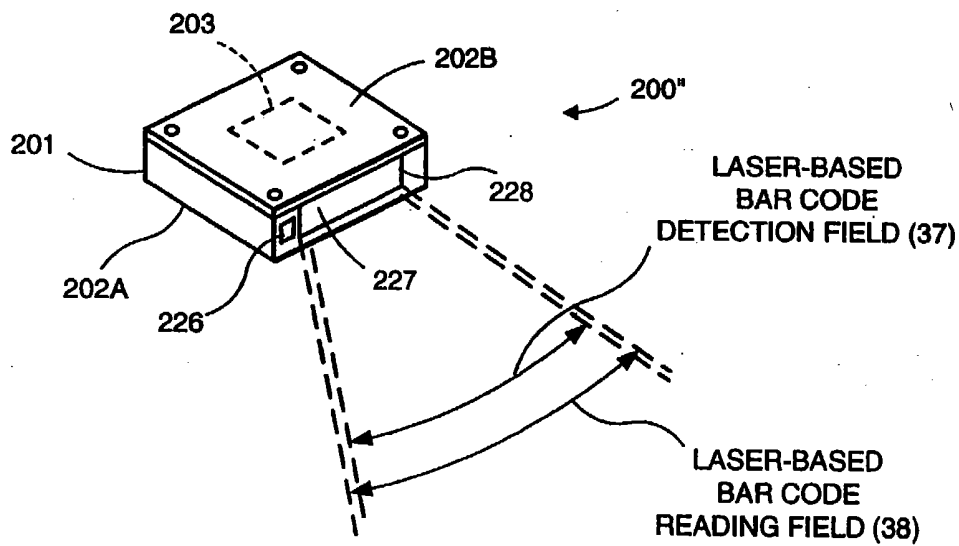


FIG. 9F

FIG. 10A

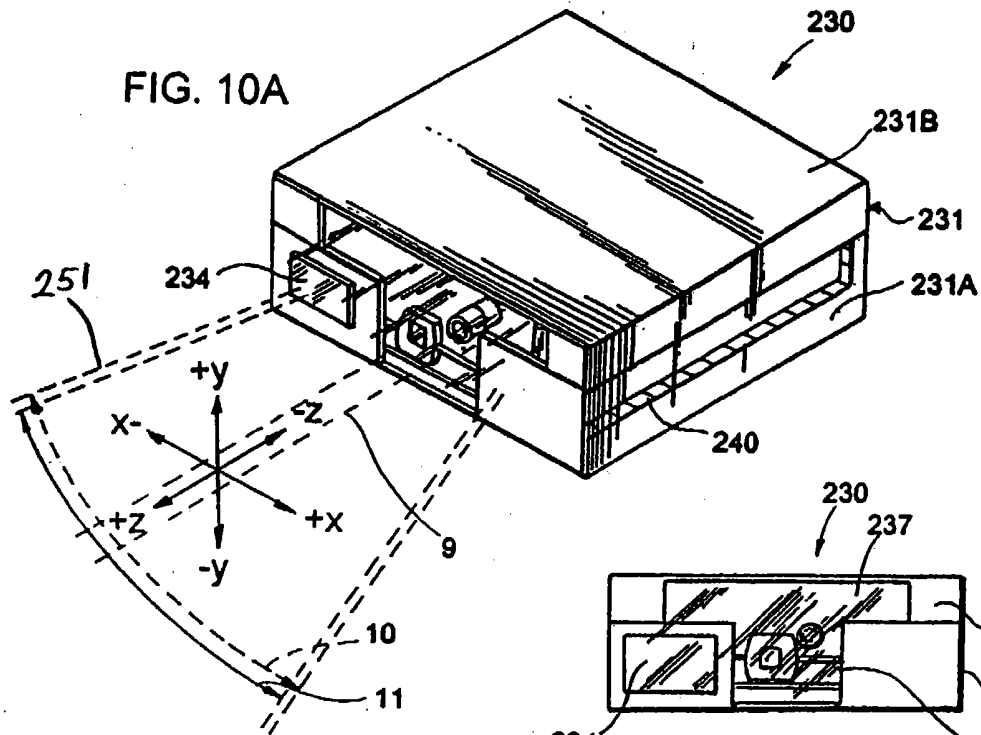


FIG. 10B

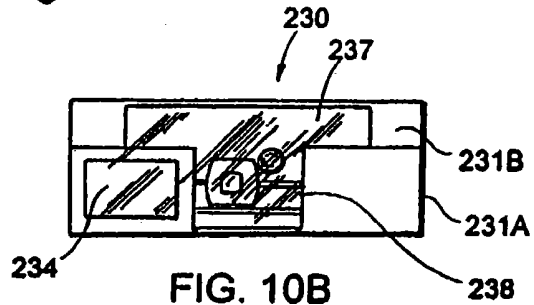


FIG. 10C

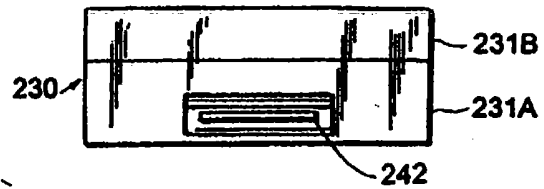
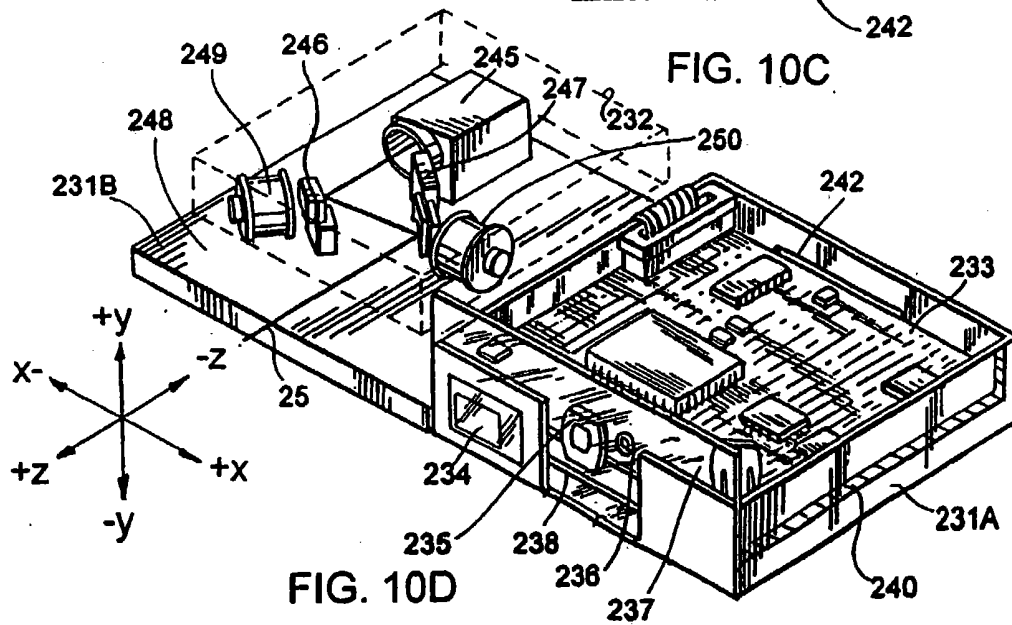


FIG. 10D



36/161

FIG. 10E

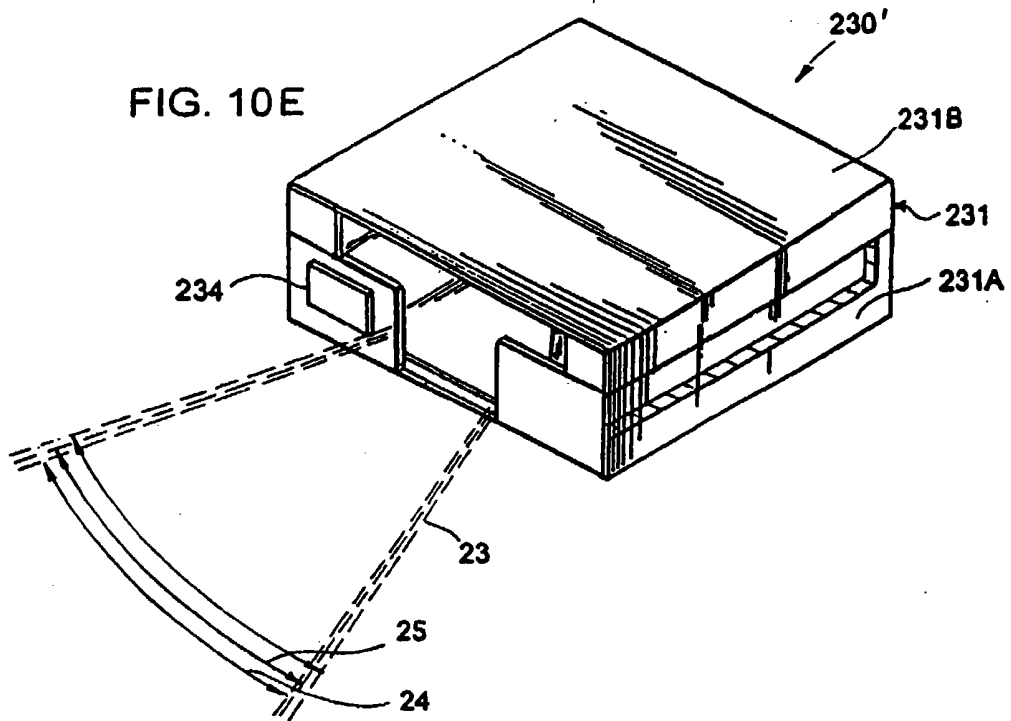
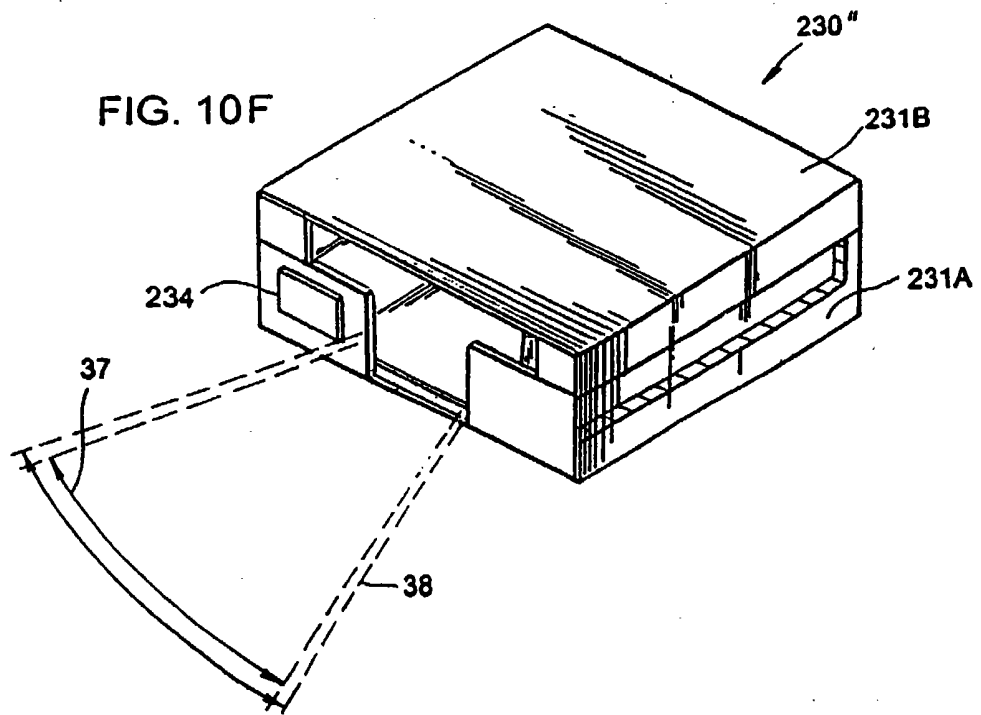


FIG. 10F



37/161

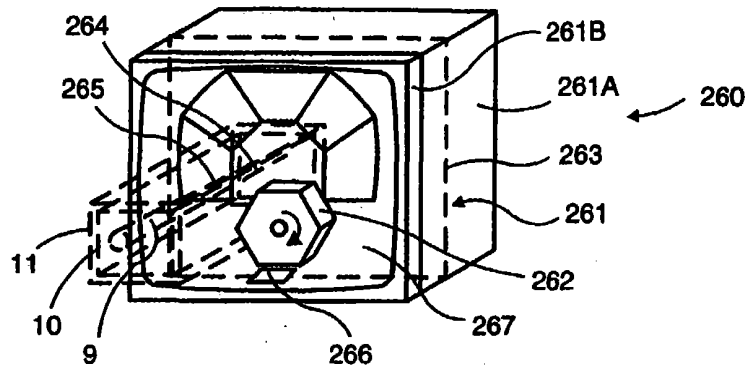


FIG. 11A

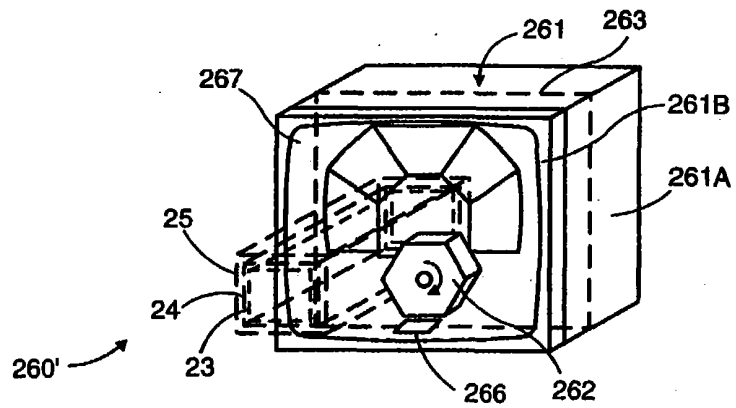


FIG. 11B

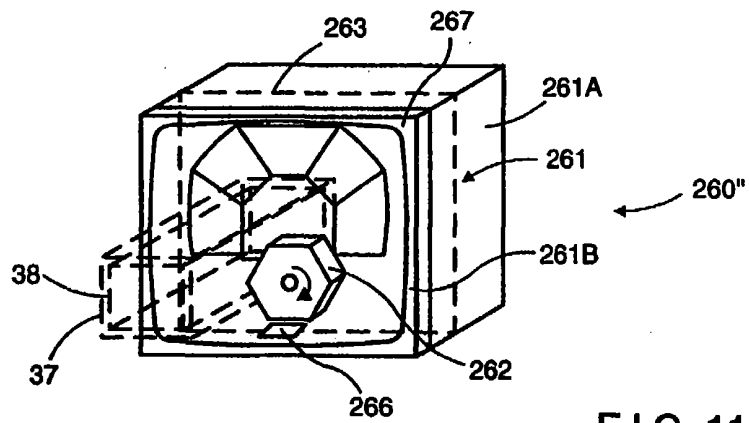


FIG. 11C

380/161

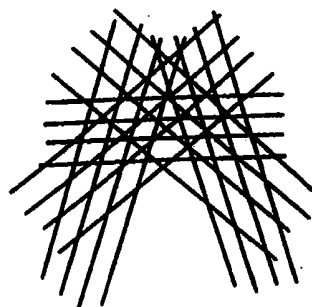


FIG. 12A

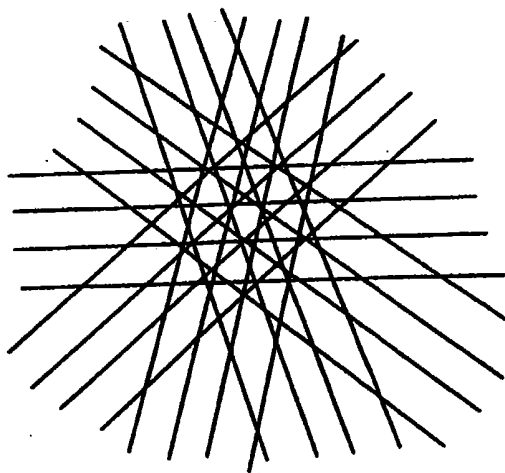
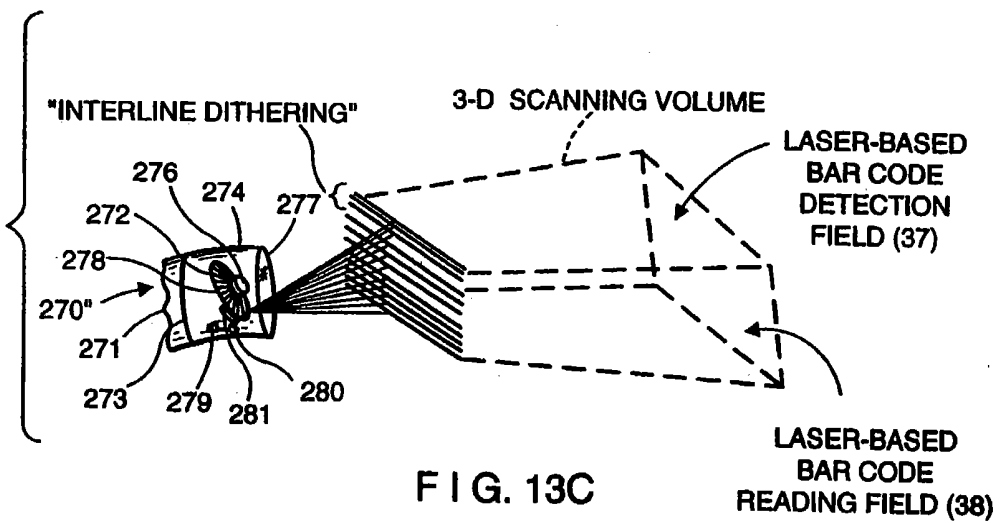
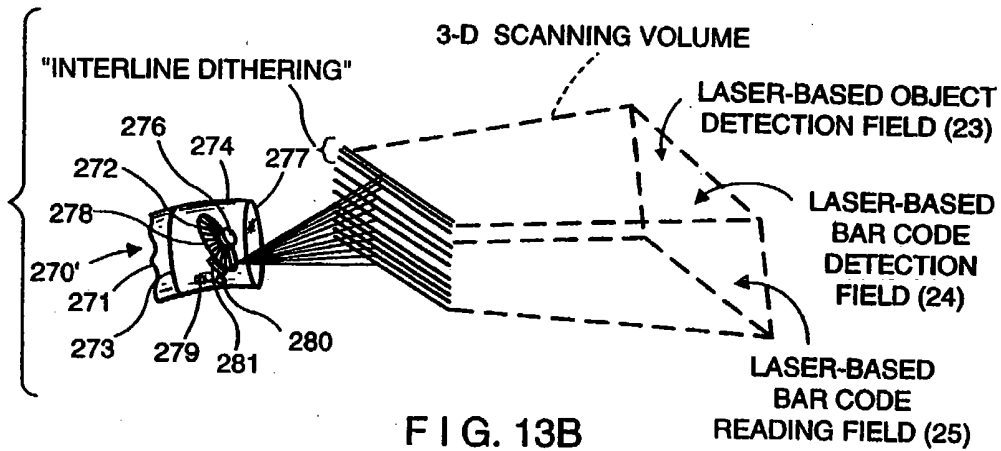
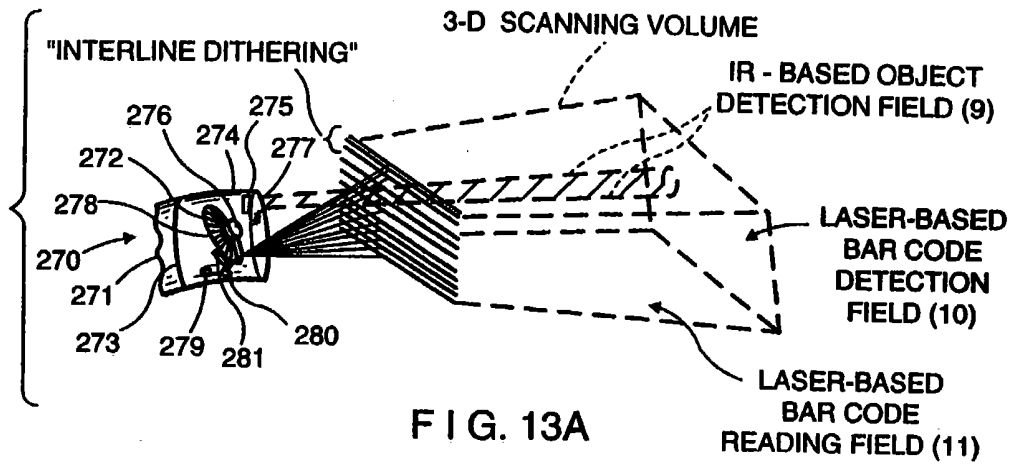


FIG. 12B

29/161



40 / 161

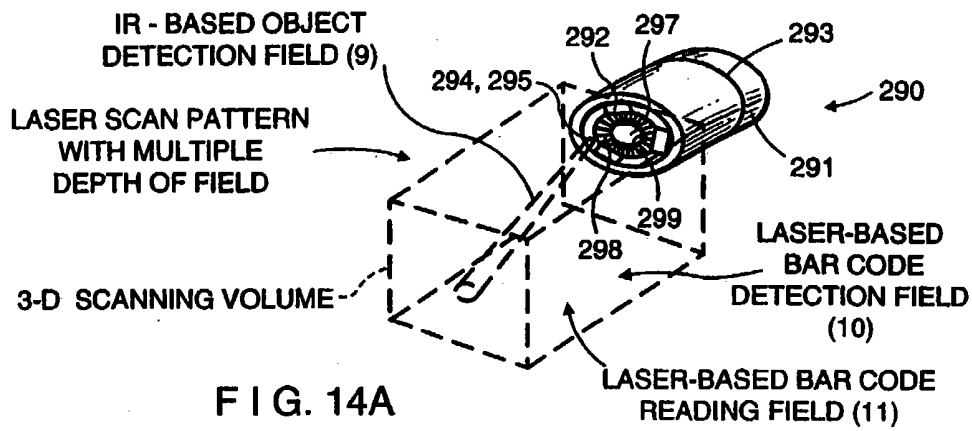


FIG. 14A

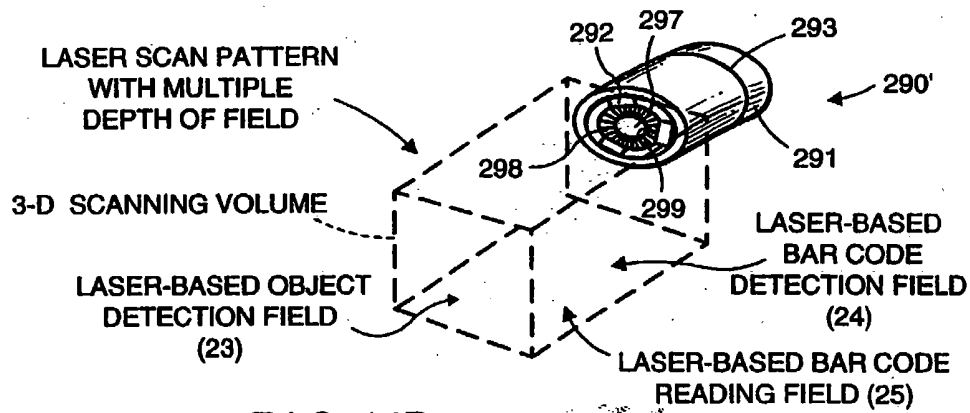


FIG. 14B

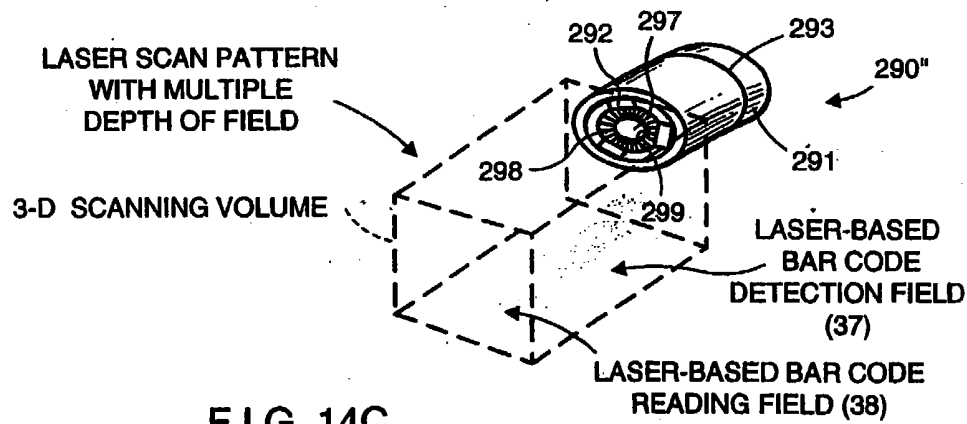


FIG. 14C

41/161

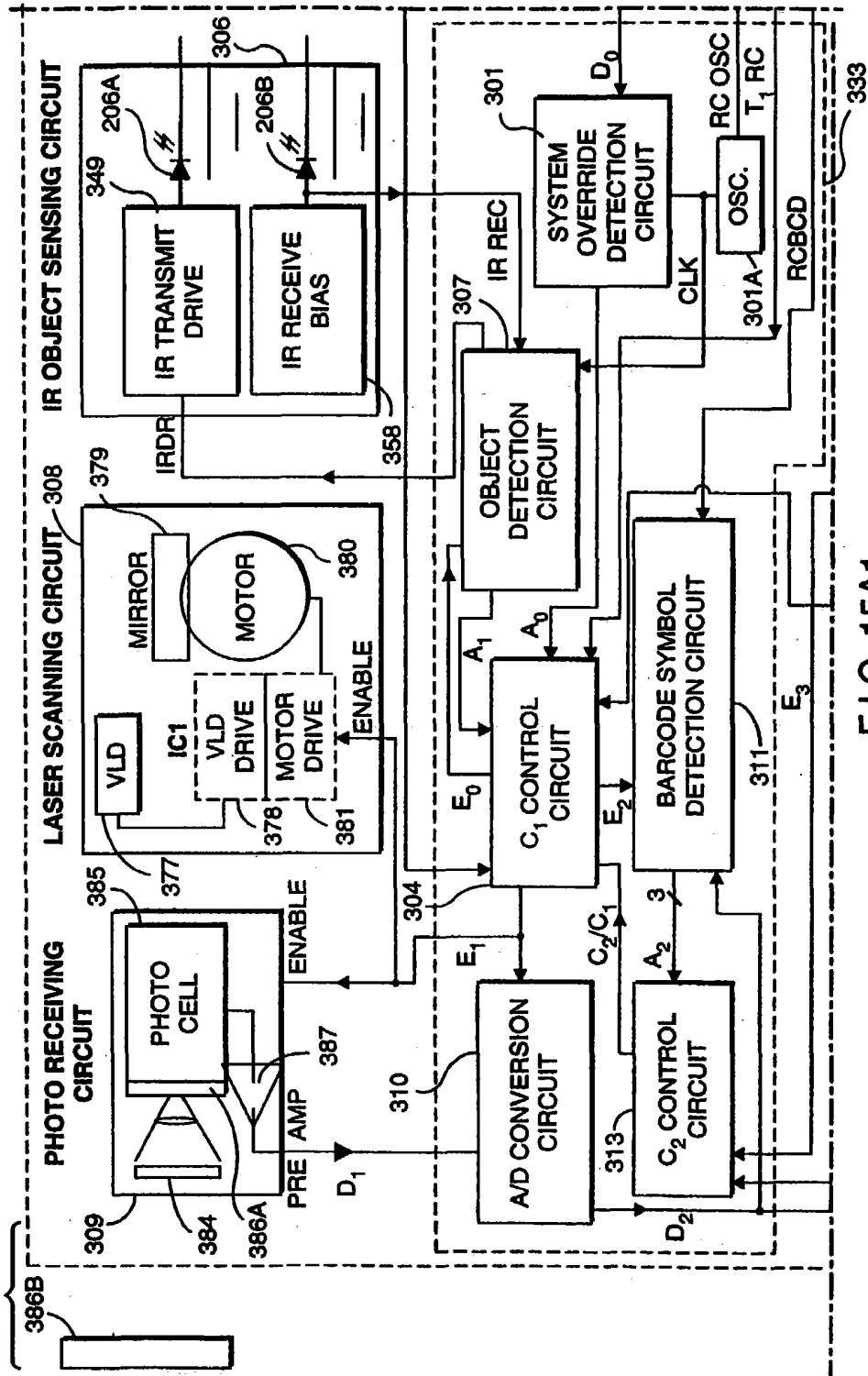


FIG. 15A1

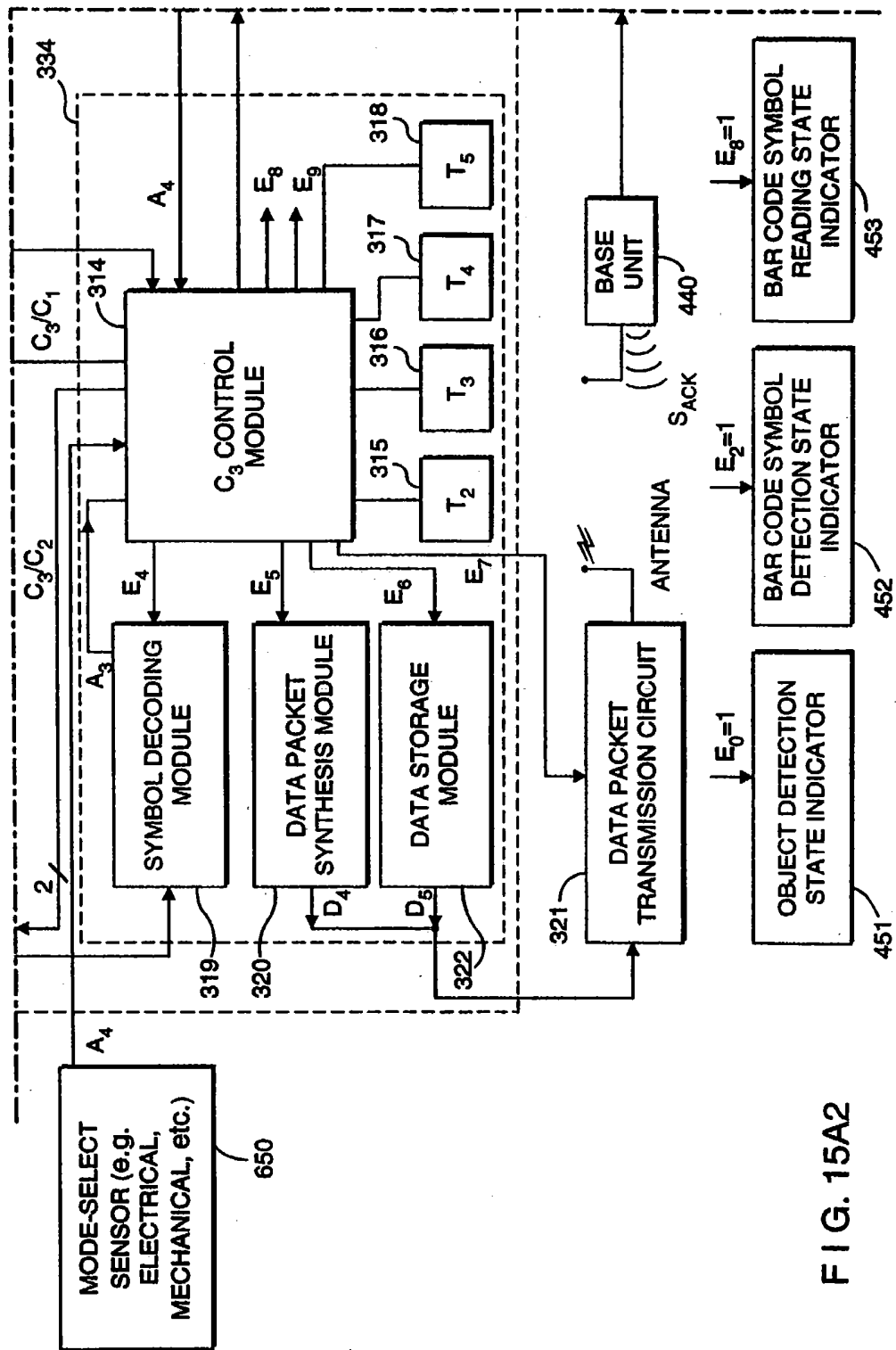


FIG. 15A2

13/161

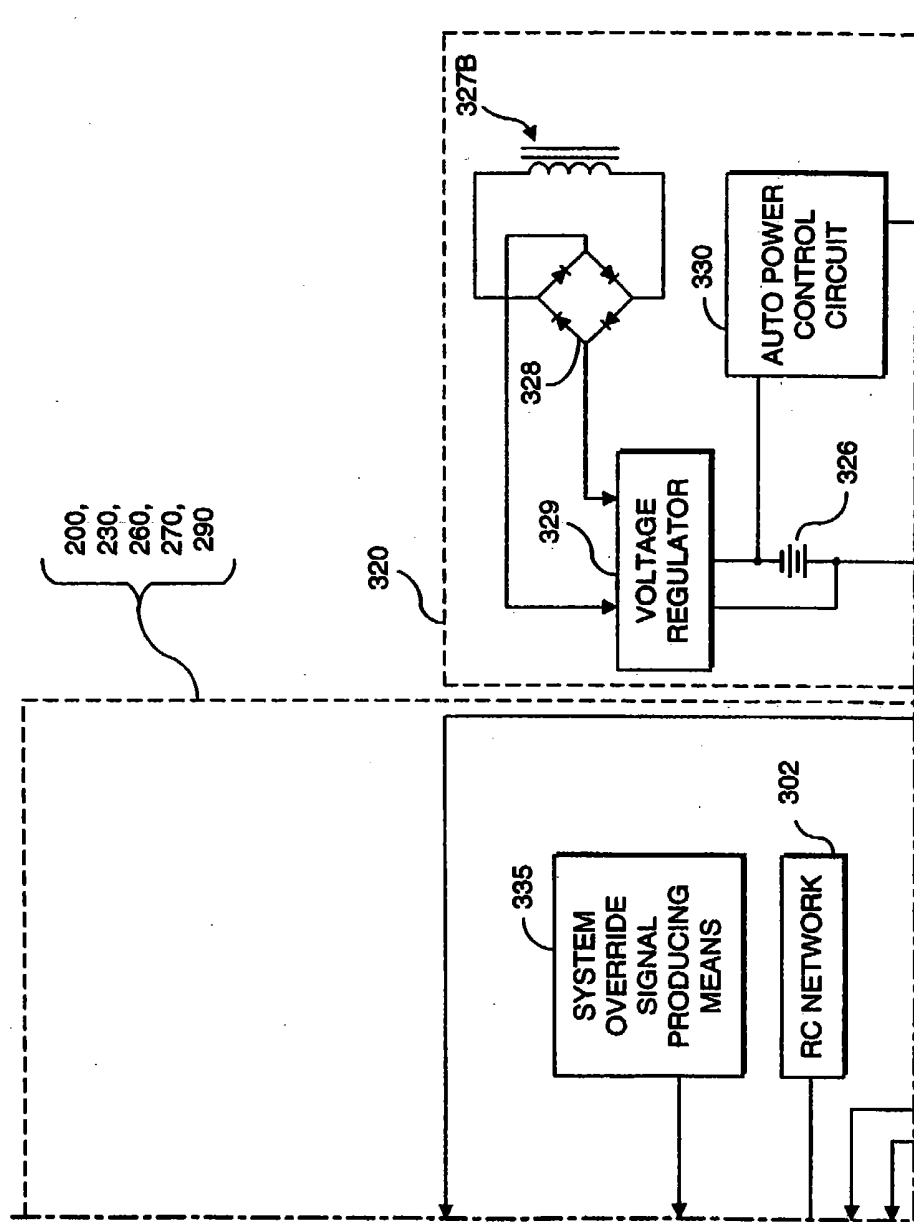


FIG. 15A3

44/161

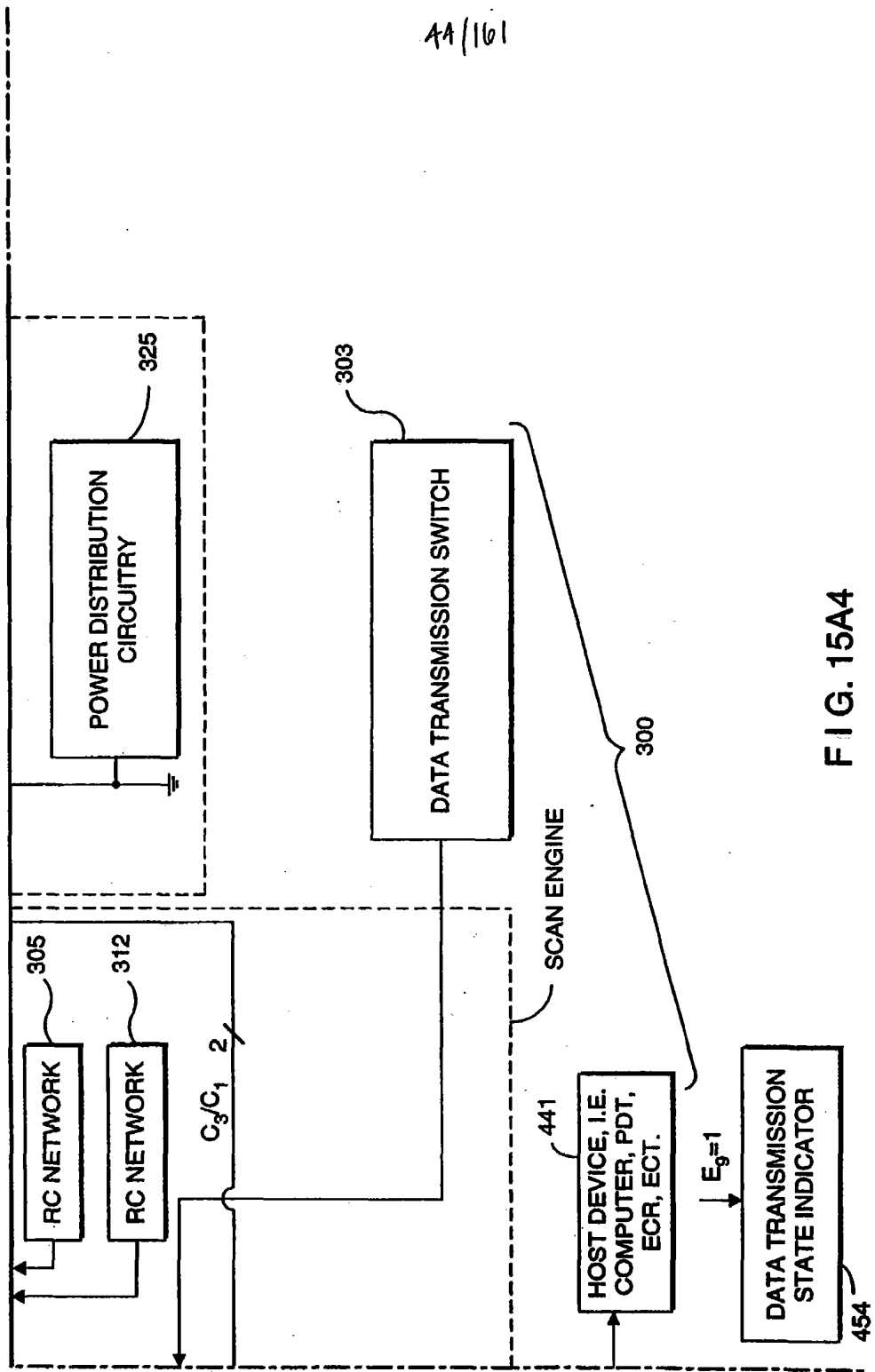


FIG. 15A4

45/161

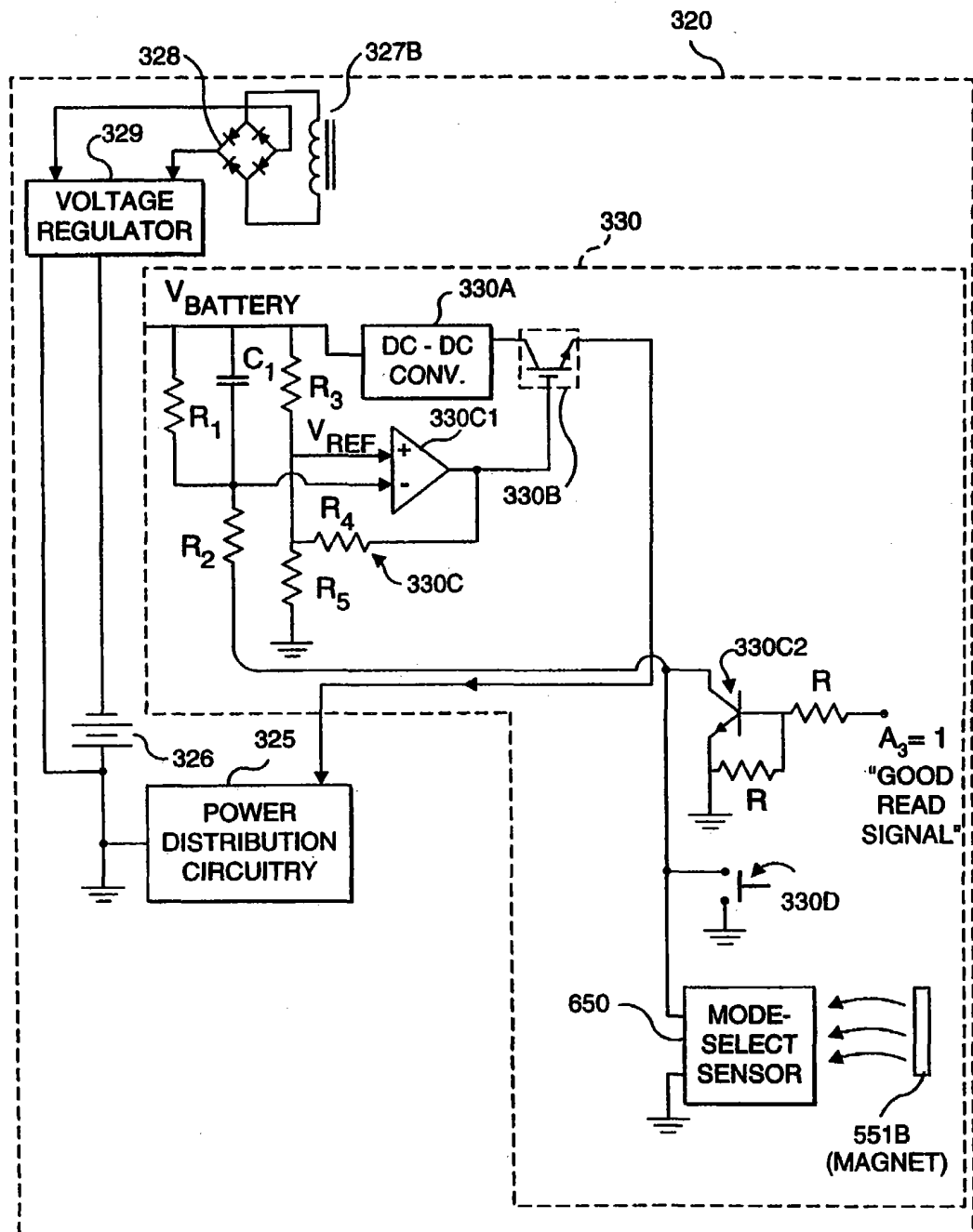


FIG. 15B1

46/161

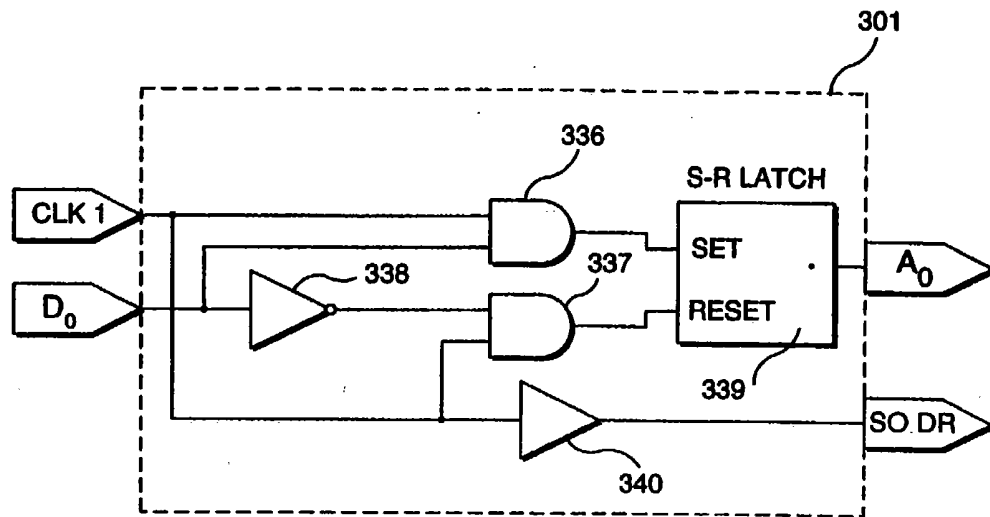


FIG. 15B2

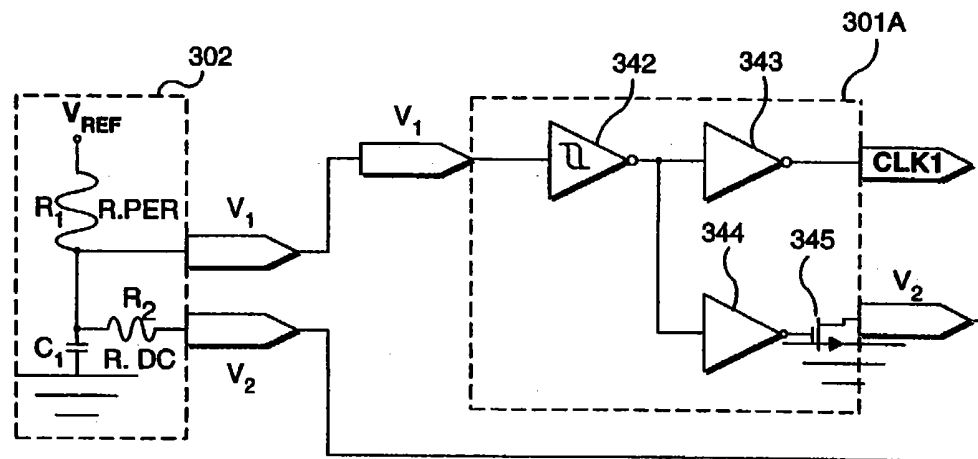


FIG. 15C

47/161

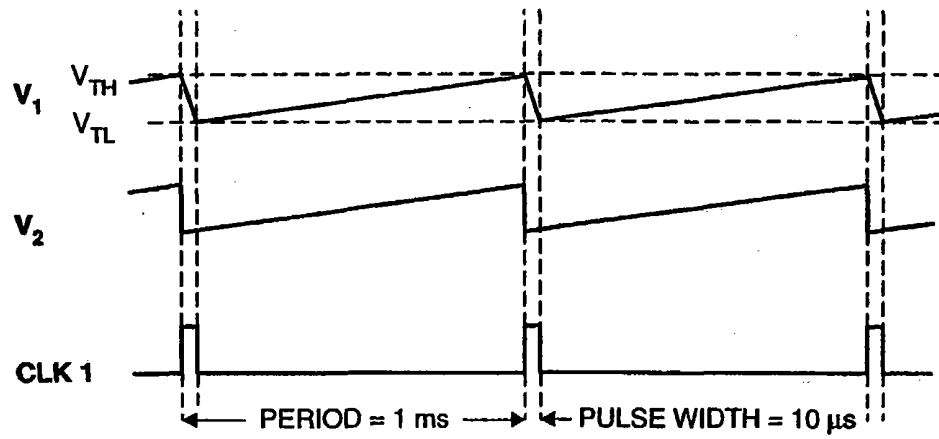


FIG. 15D

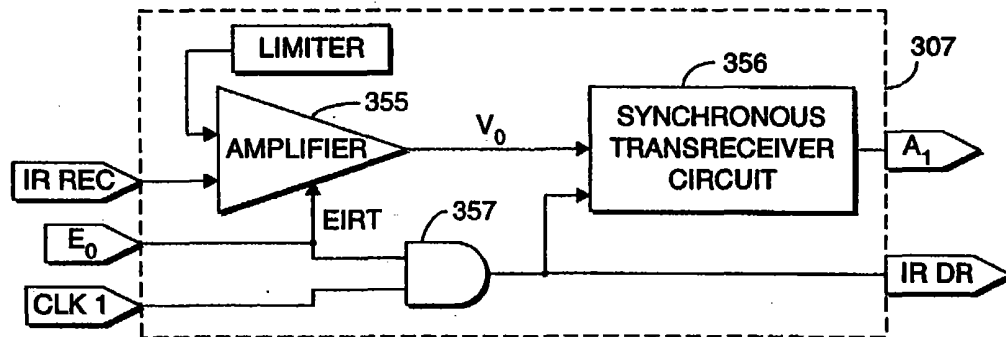


FIG. 15E

48/161

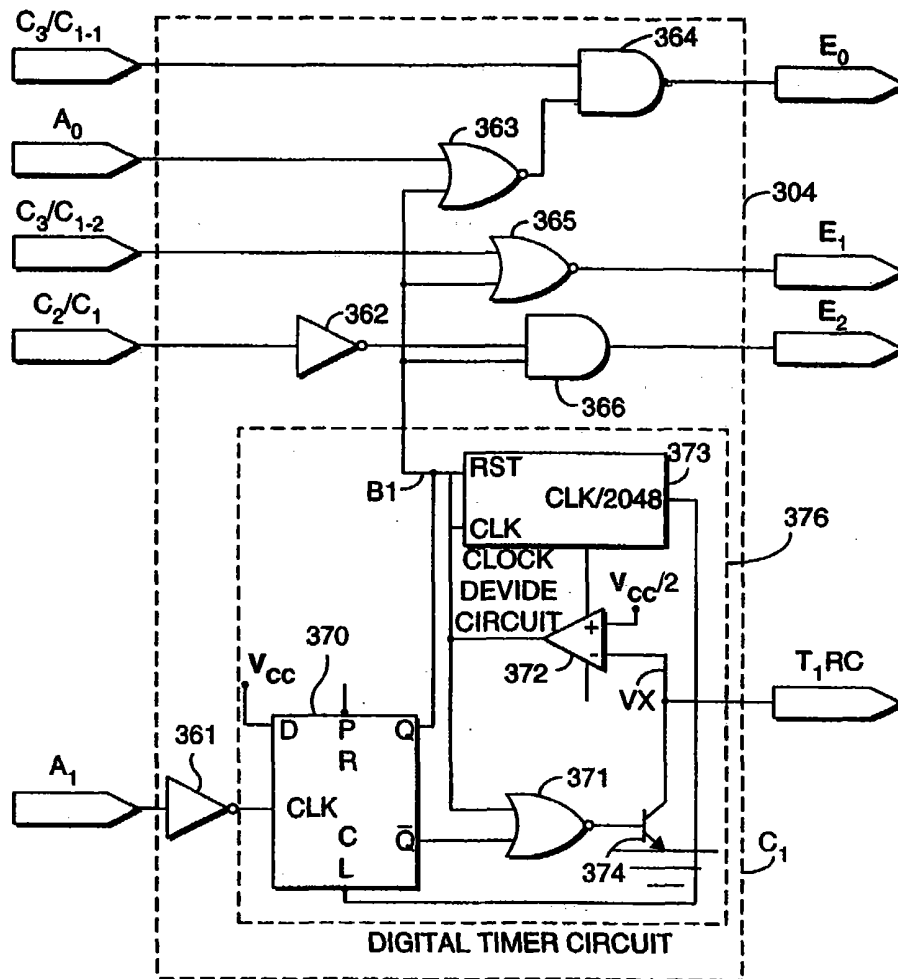


FIG. 15F

49/161

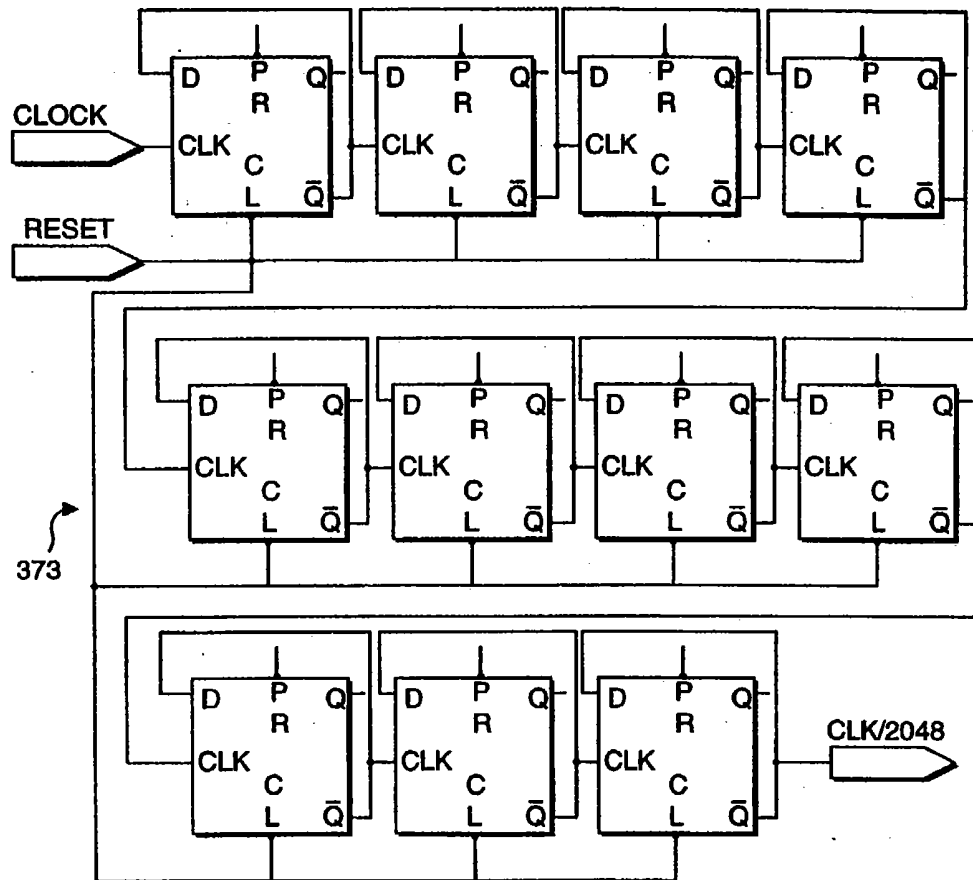


FIG. 15G

50/161

$$\left. \begin{aligned} E_0 &= (B1 + A_0) (C_3 / C_{1-1}) \\ E_1 &= (C_3 / C_{1-2}) + B1 \\ E_2 &= (C_2 / C_1) (T_1) \end{aligned} \right\}$$

FIG. 15H

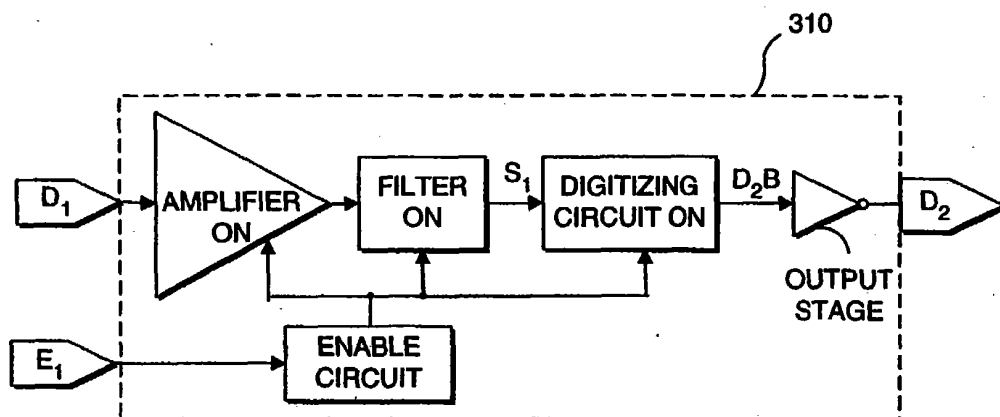


FIG. 15I

51/161.

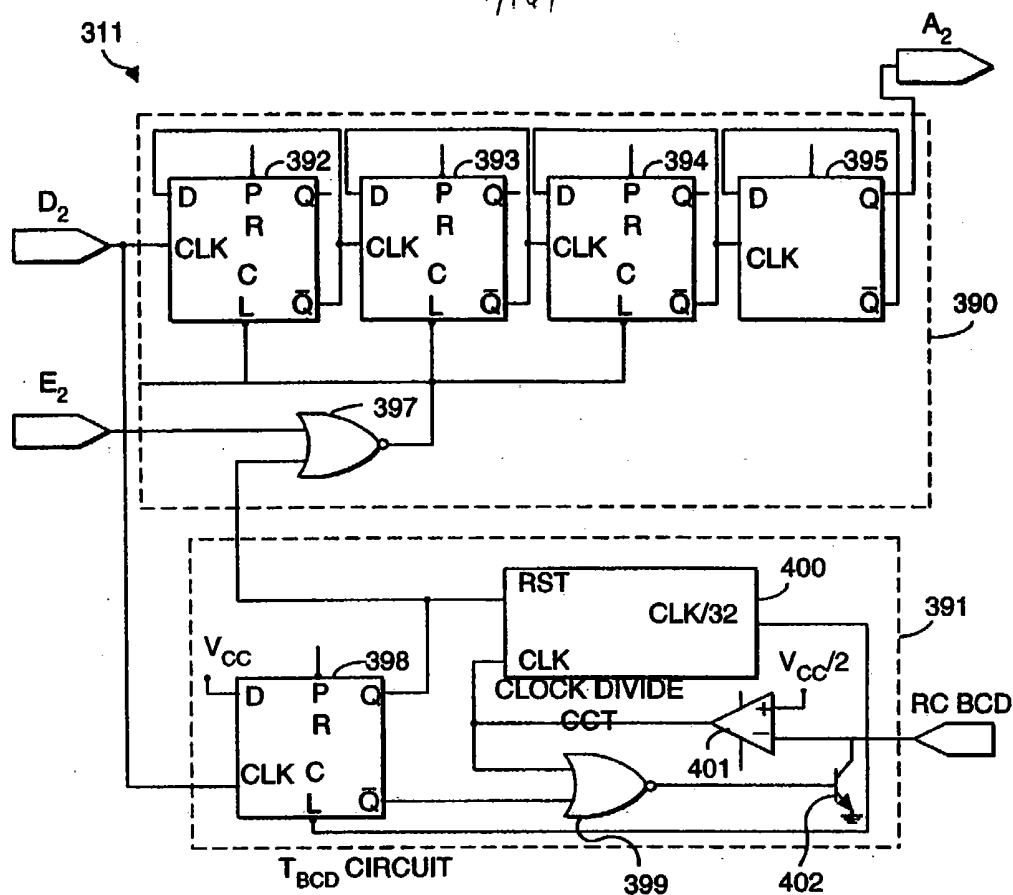


FIG. 15J

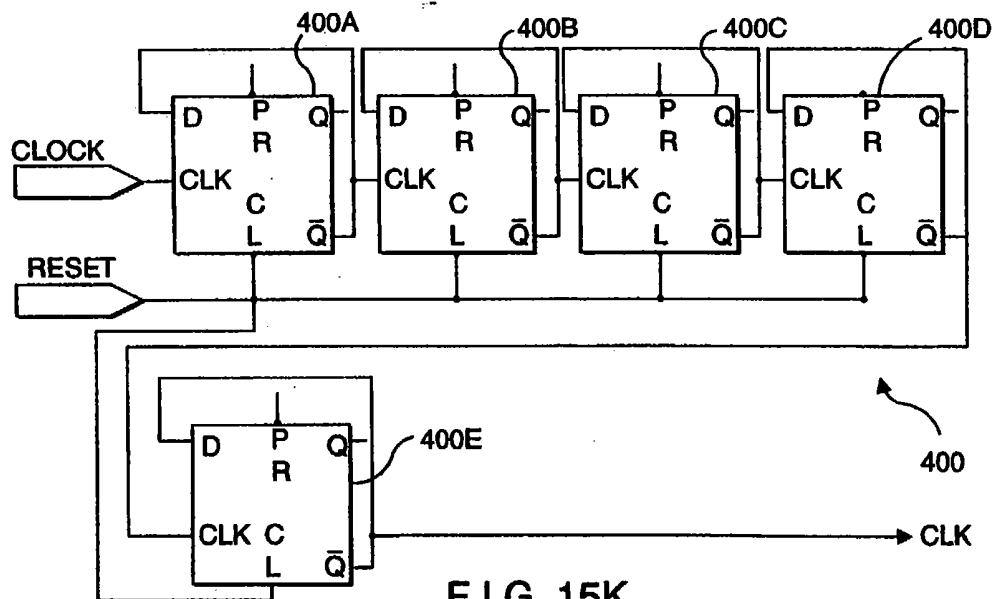


FIG. 15K

52/161

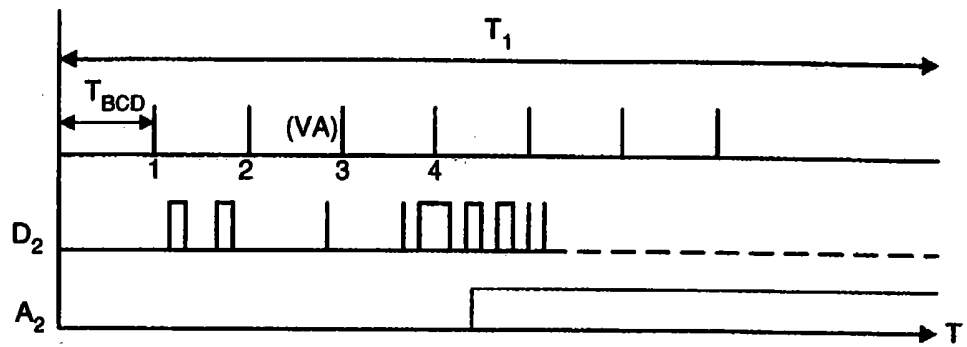


FIG. 15L

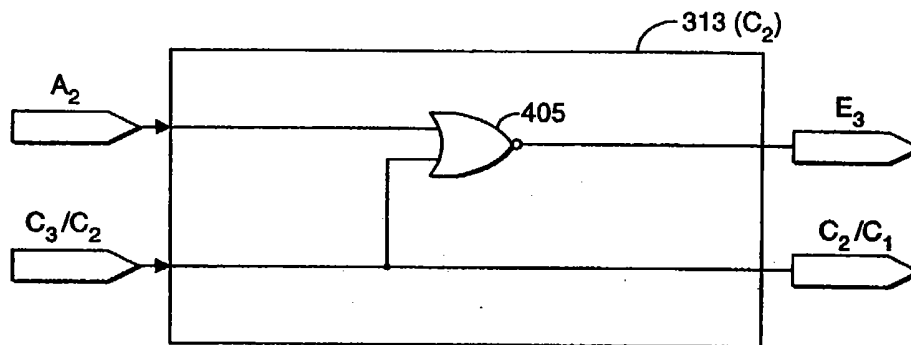


FIG. 15M

C_3/C_2	A_2	E_3	C_2/C_1
0	0	0	0
0	1	1	0
1	X	1	1

X: DON'T CARE (I.E. C_3/C_2 OVERRIDES A_2)

FIG. 15N

53/161

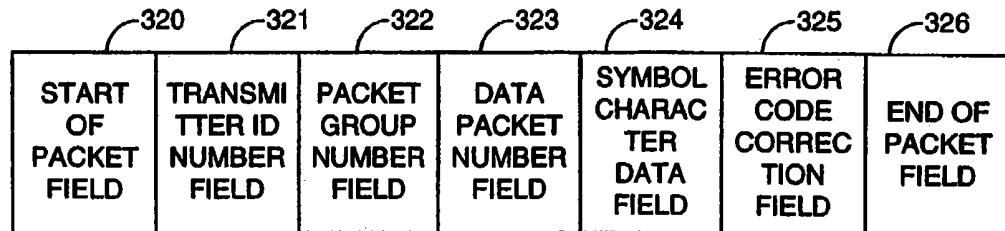


FIG. 150

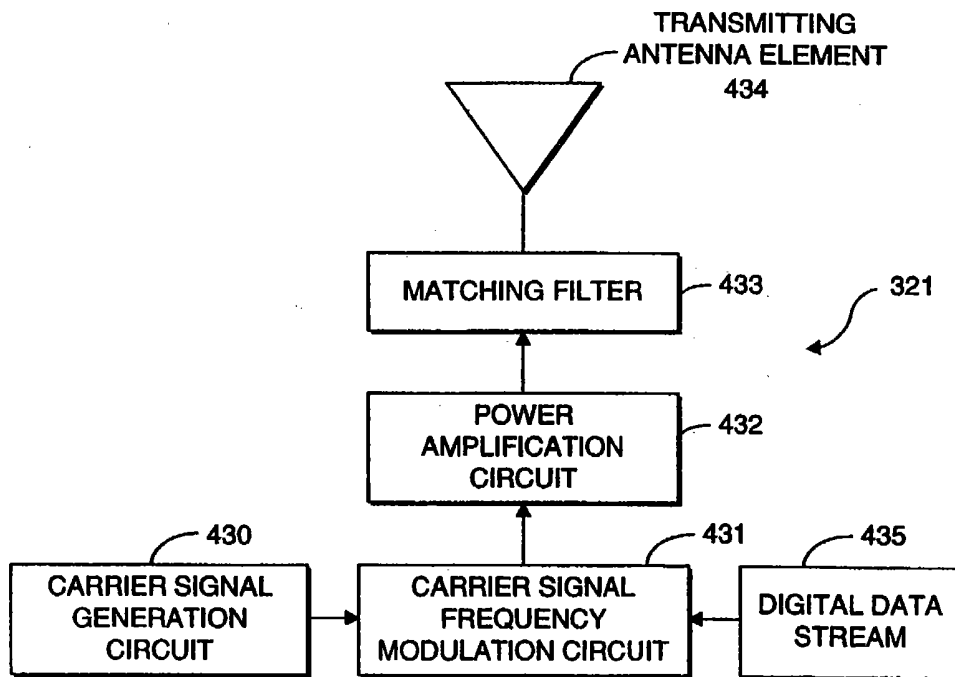


FIG. 16

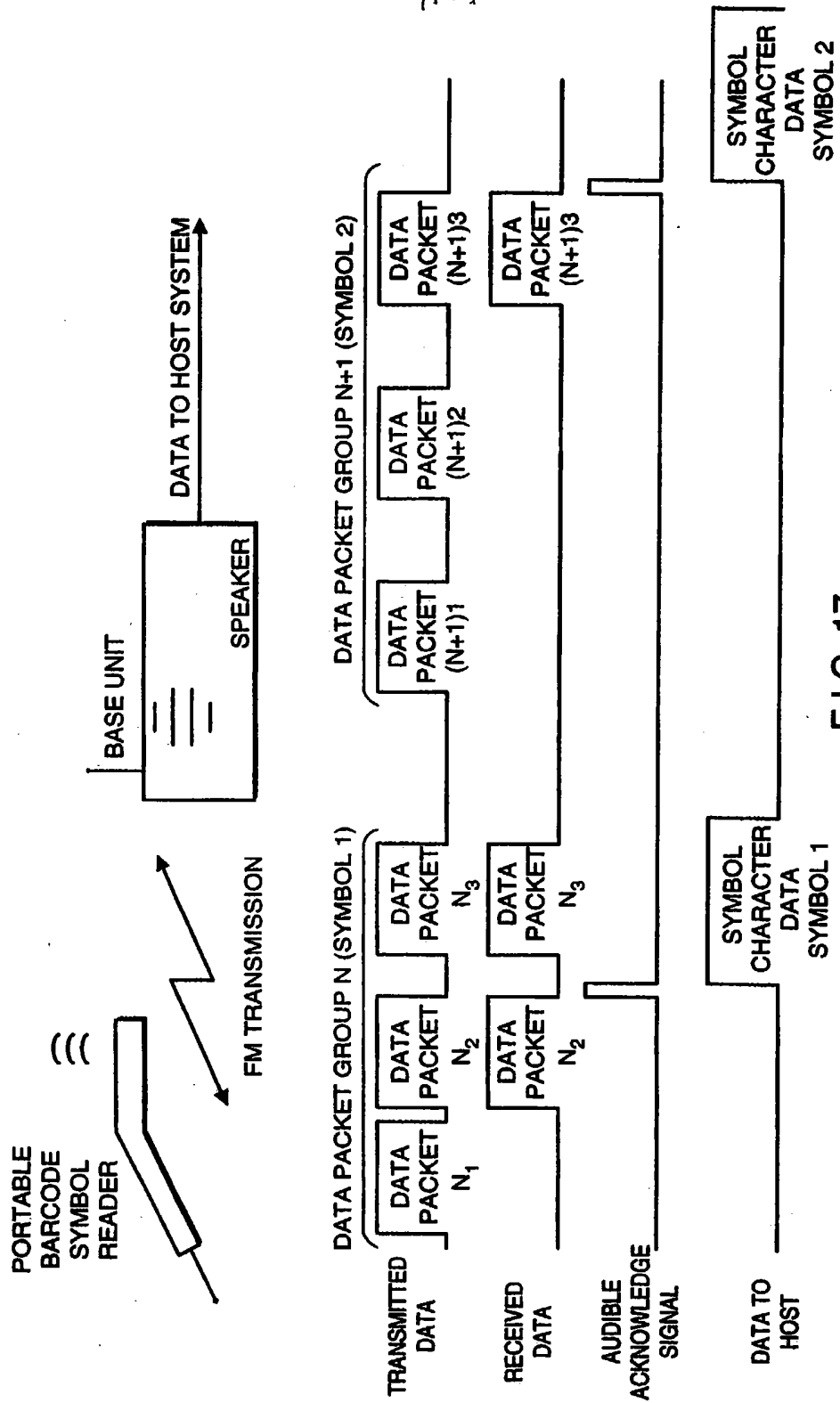


FIG. 17

55/161

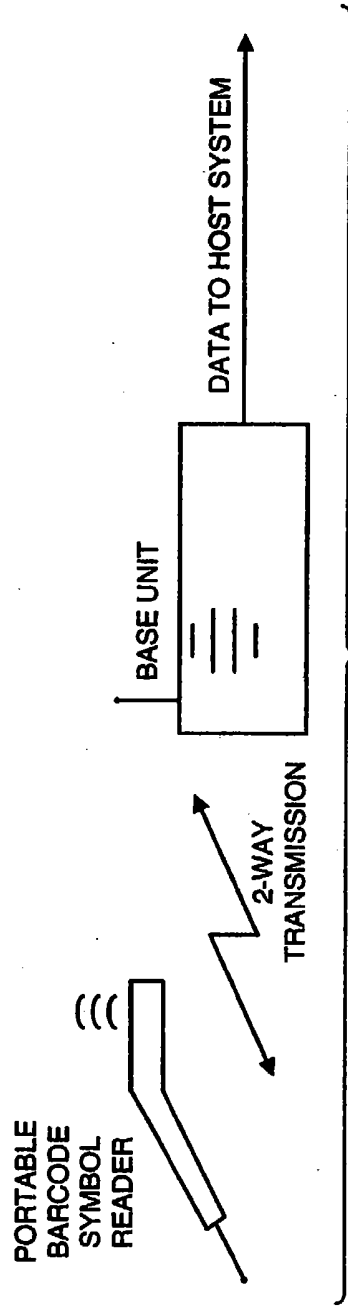


FIG. 18



FIG. 19

56/161

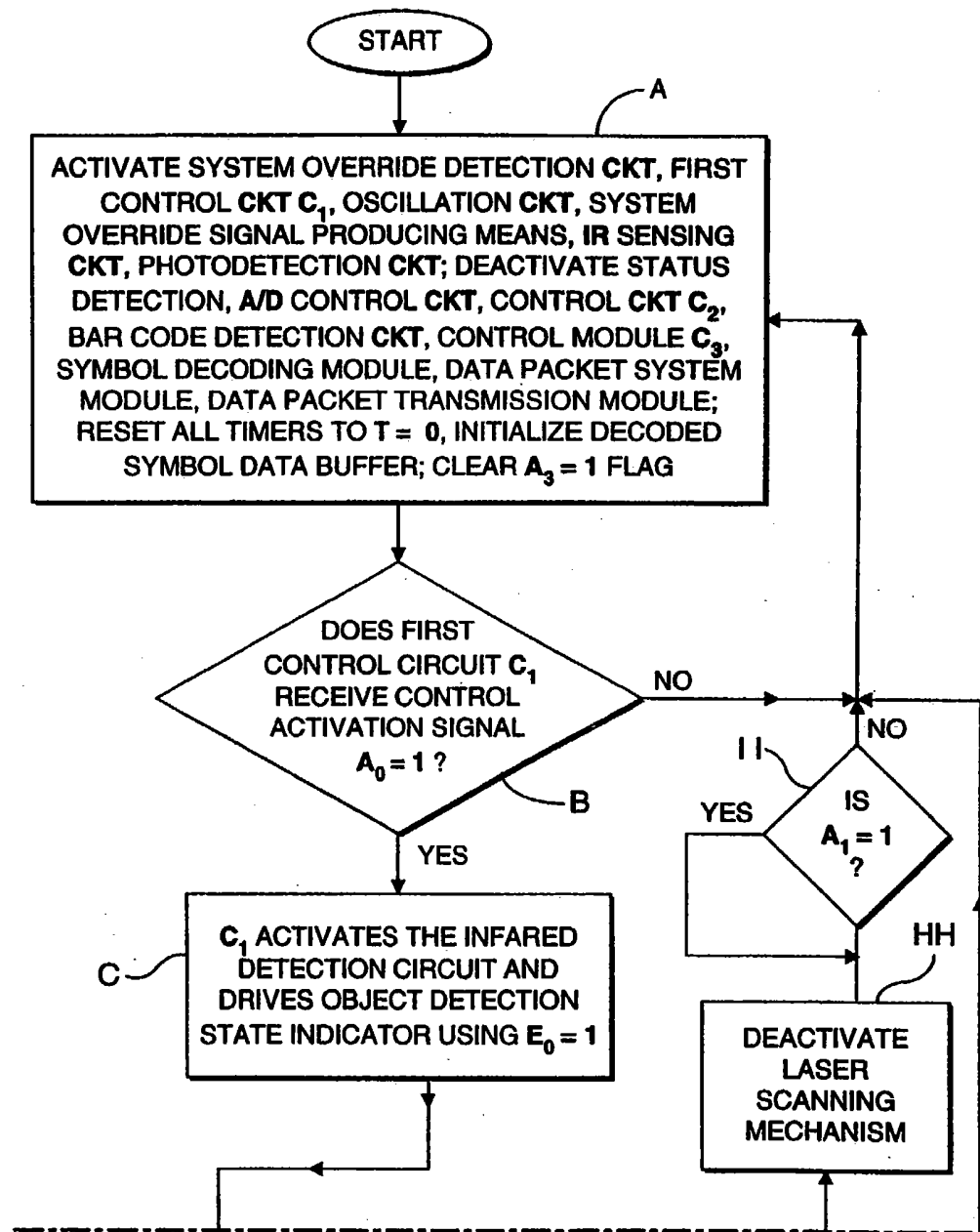


FIG. 20A1

57/161

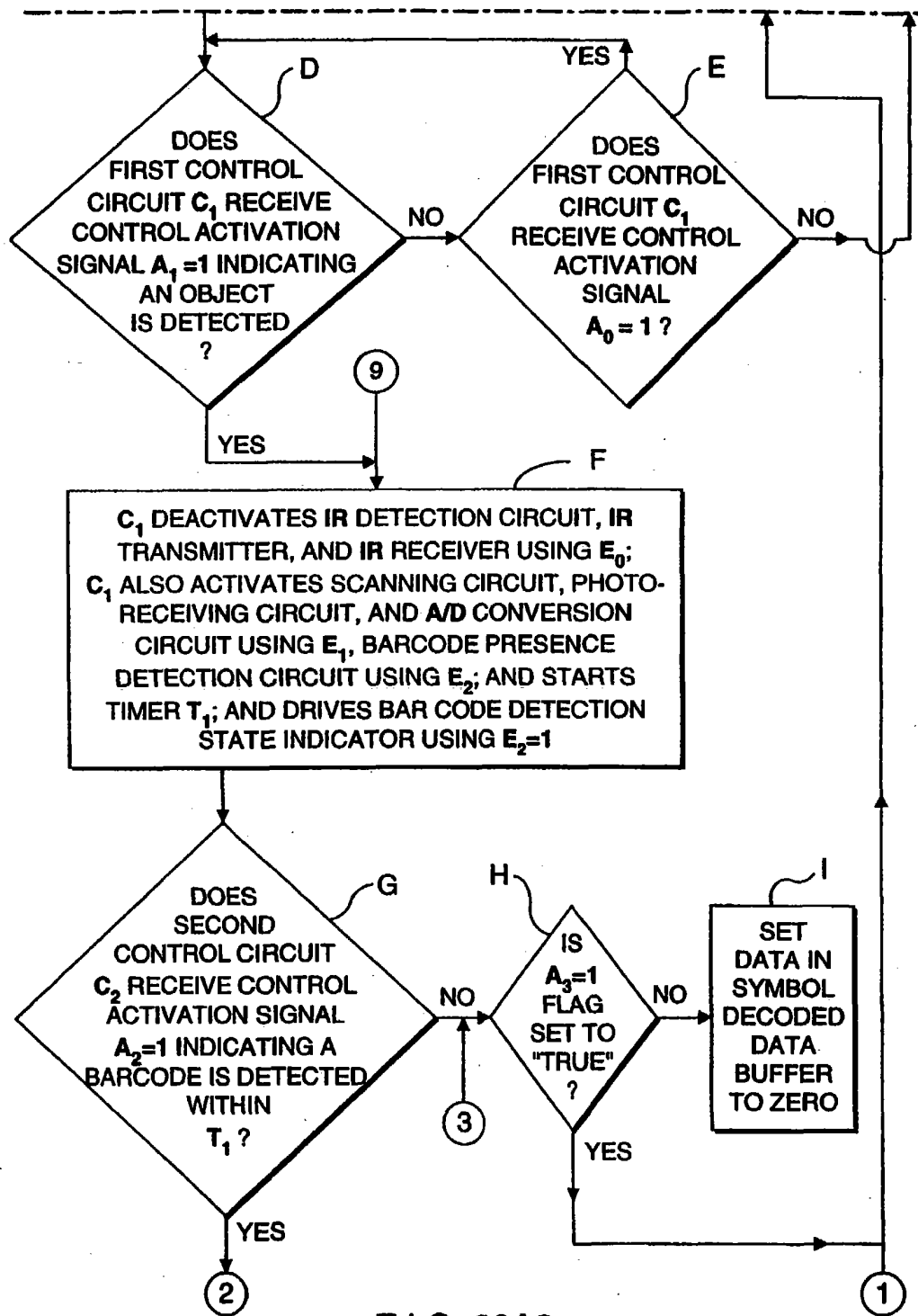


FIG. 20A2

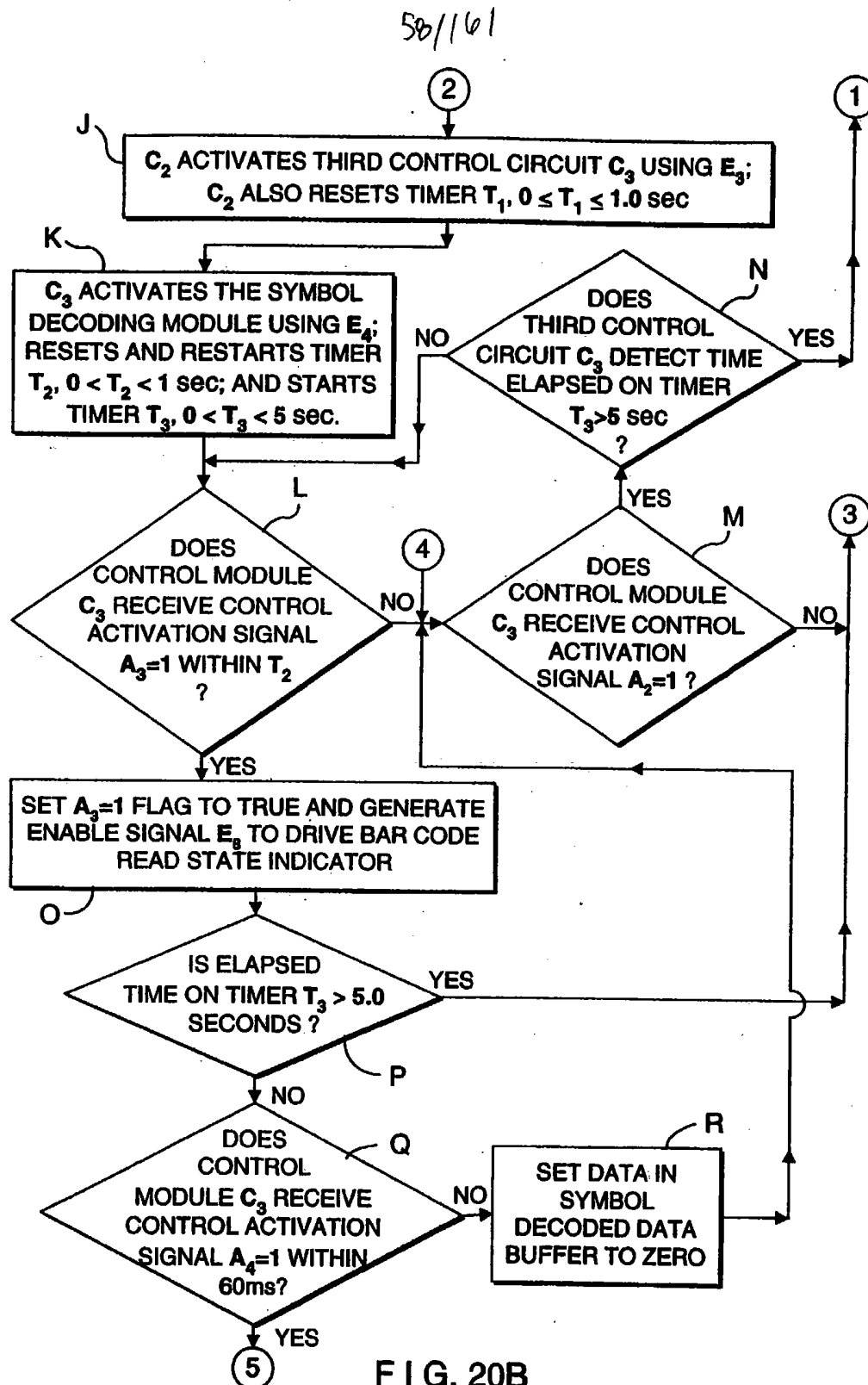


FIG. 20B

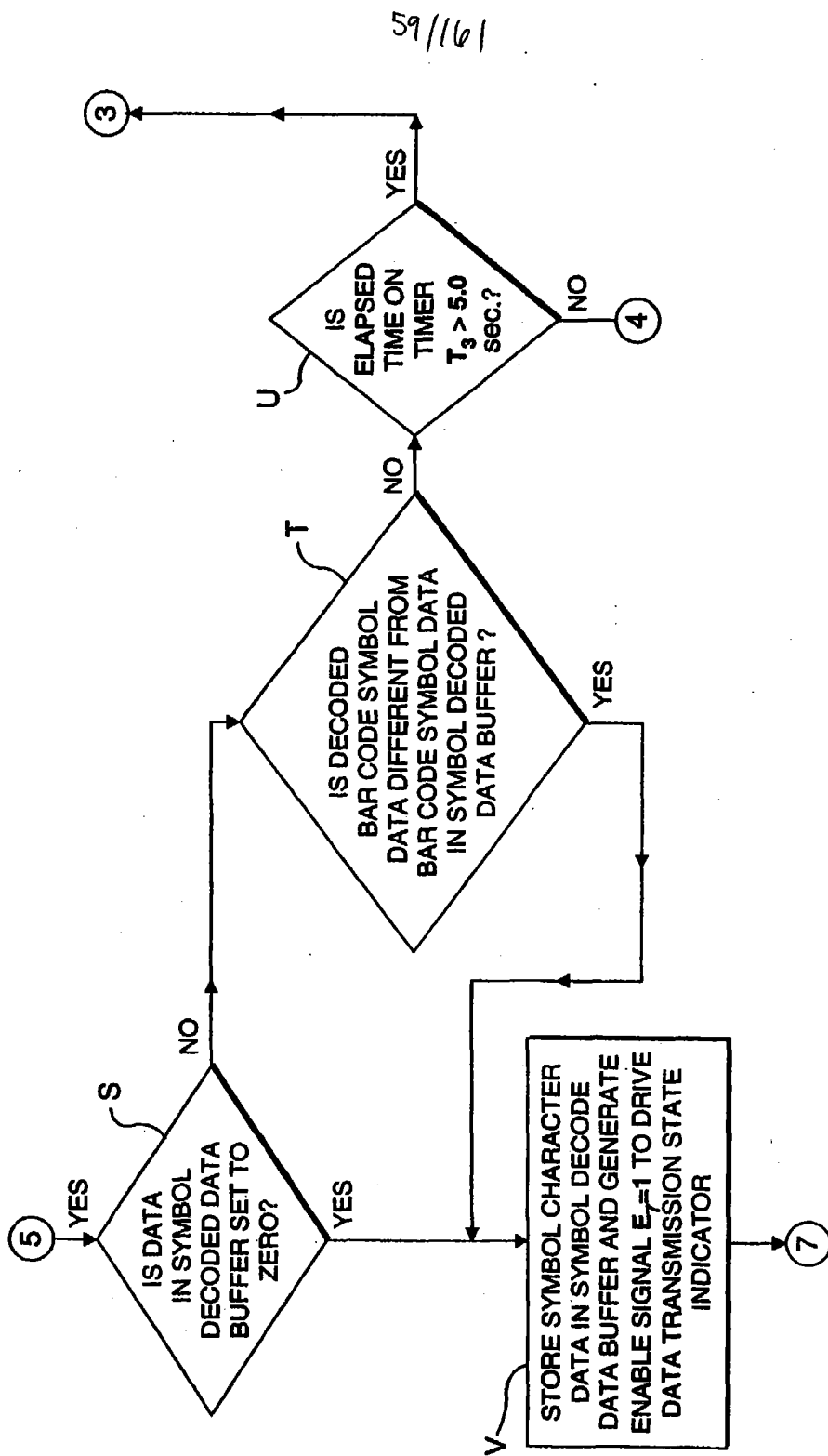


FIG. 20C

601171

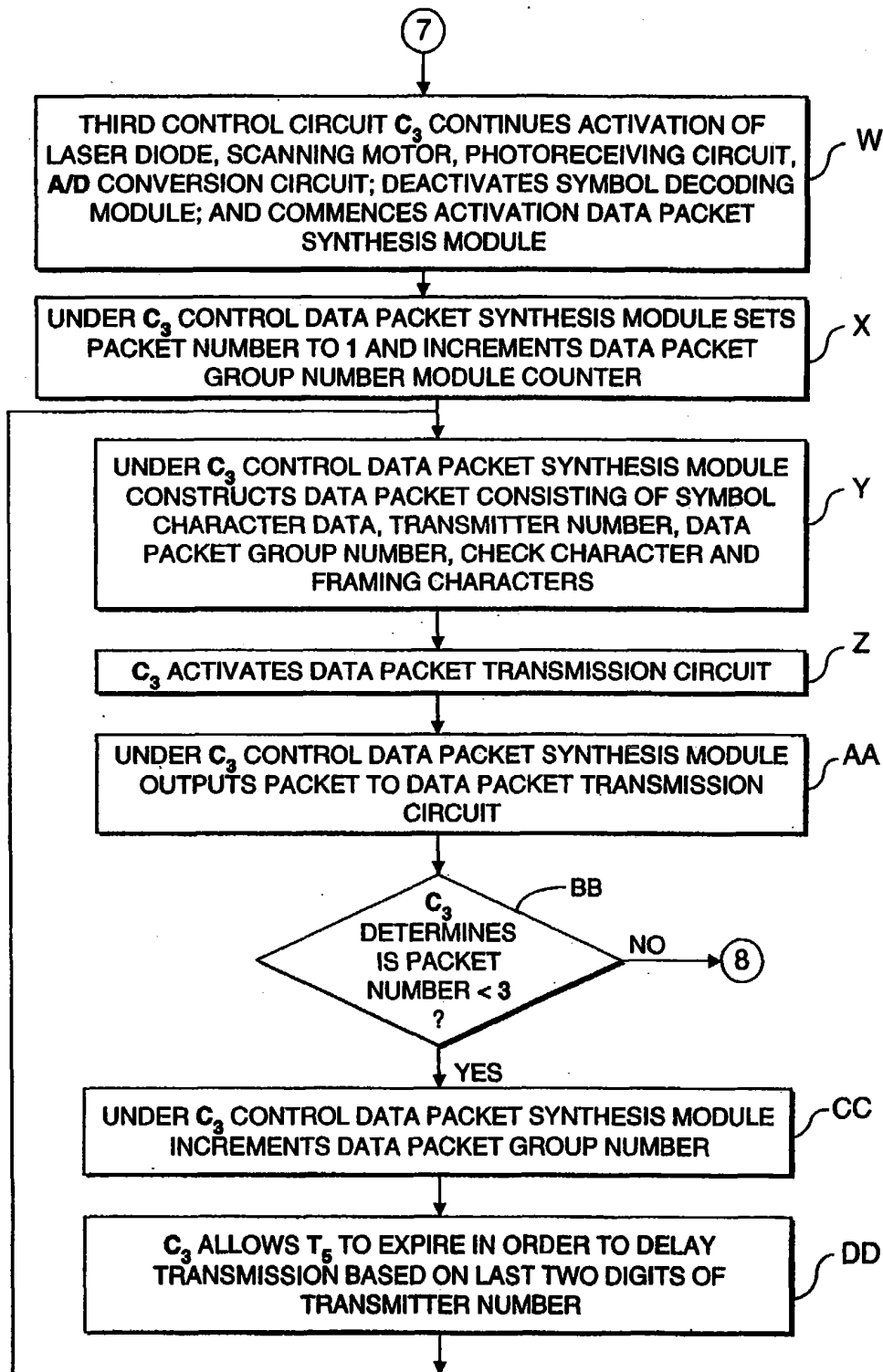


FIG. 20D

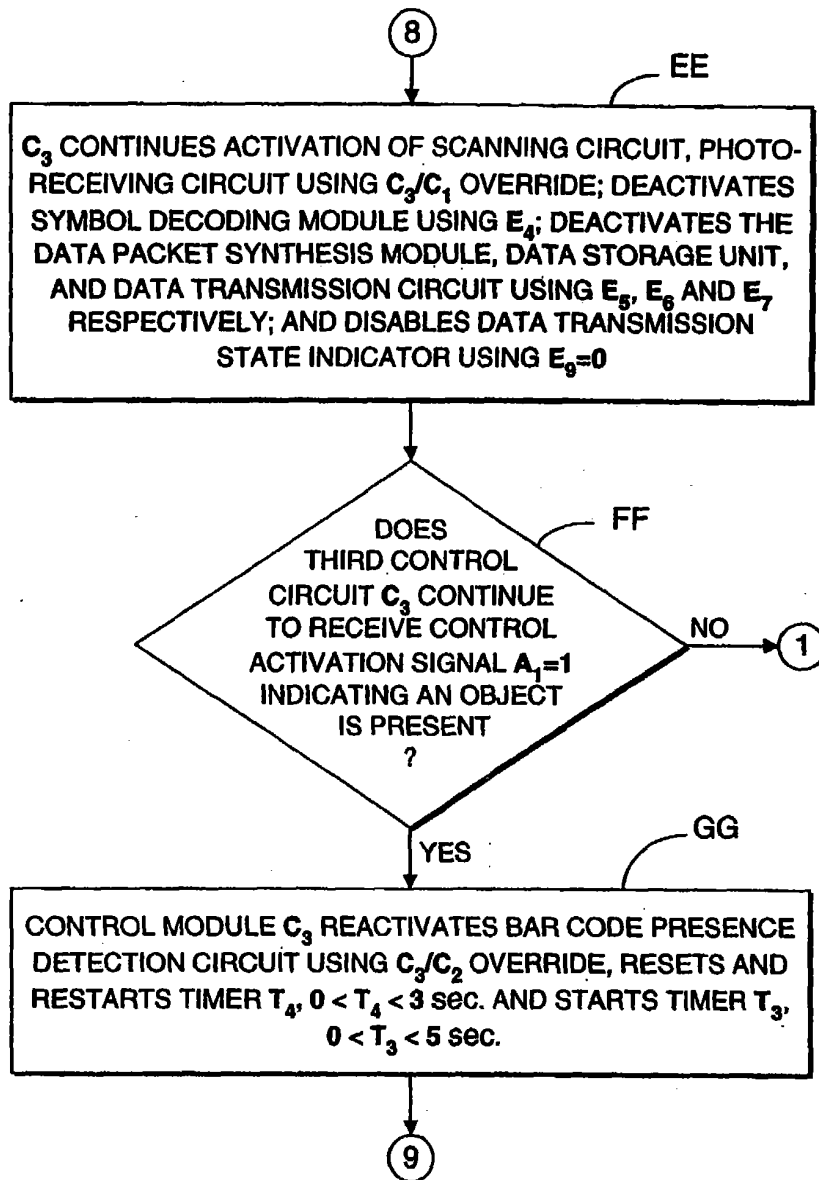


FIG. 20E

62/161

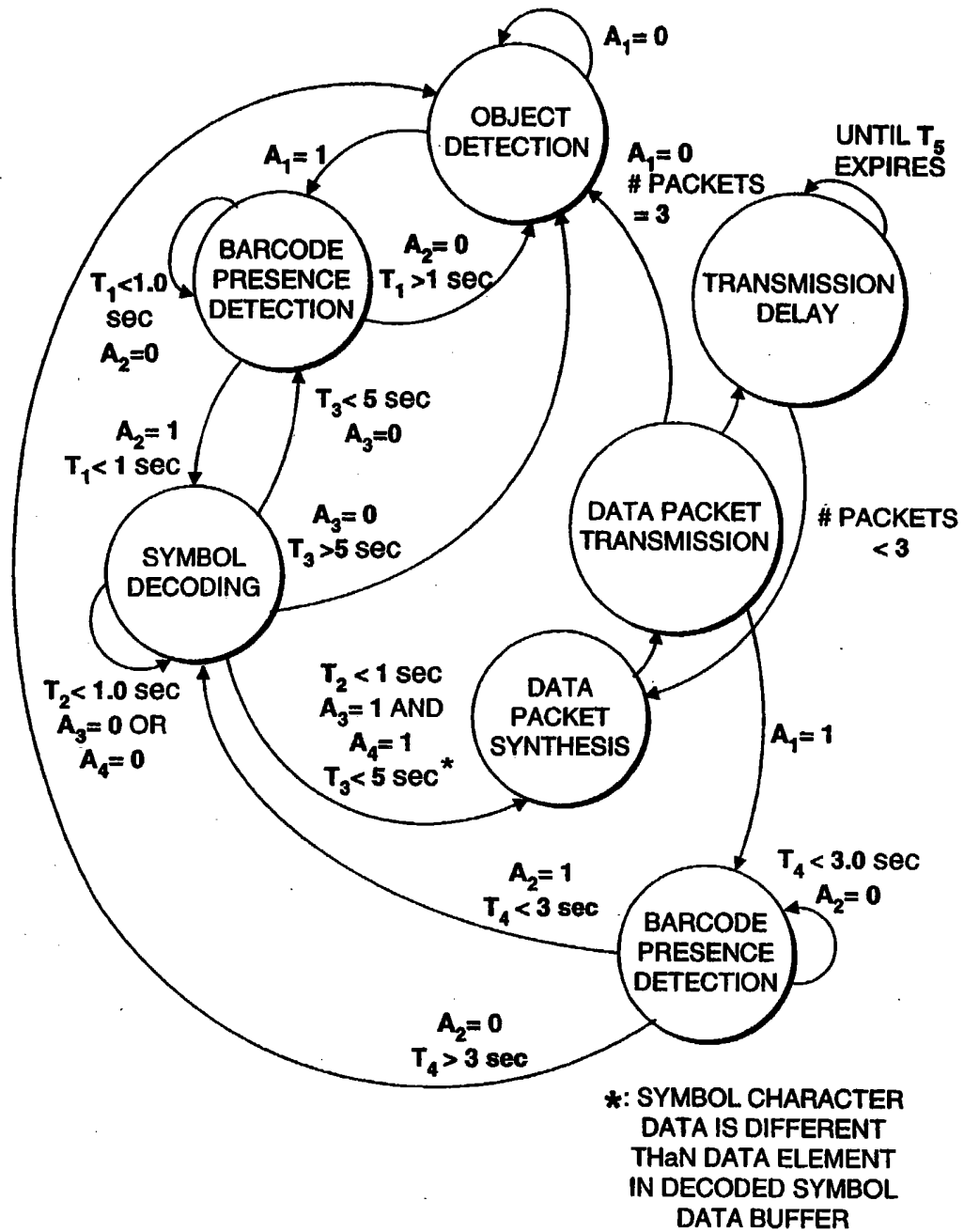


FIG. 21

63/161

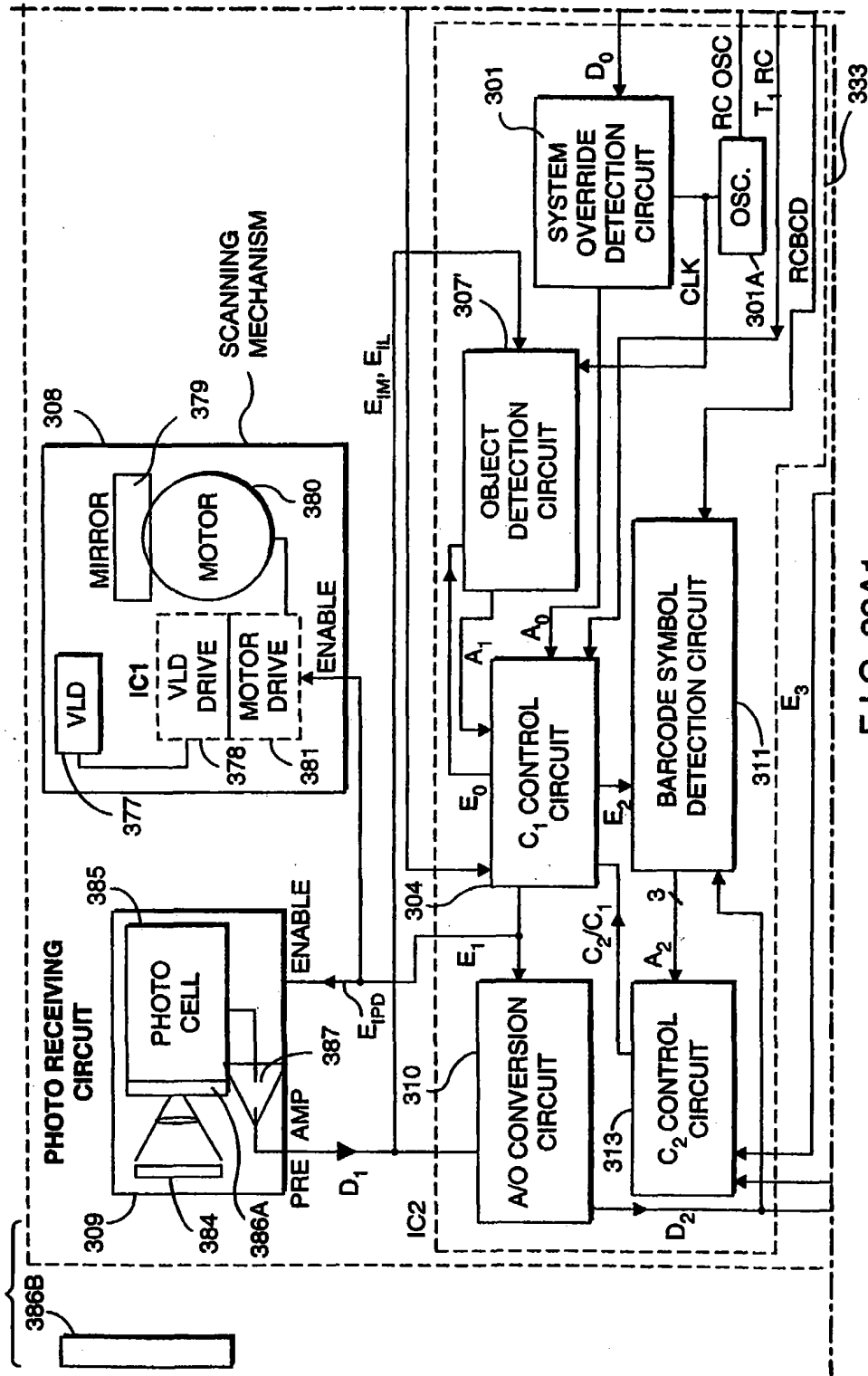


FIG. 22A1

64/161

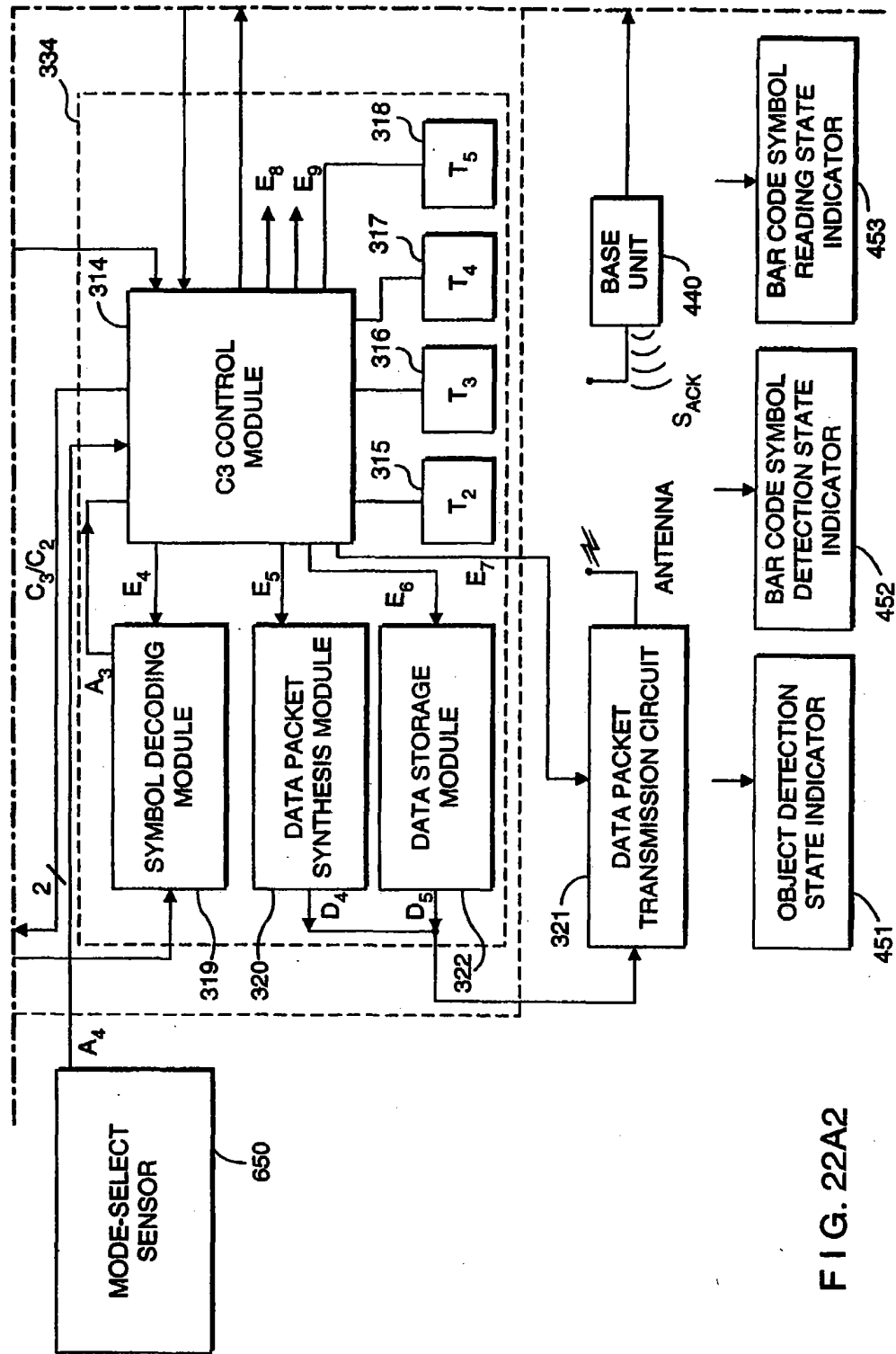


FIG. 22A2

45/161

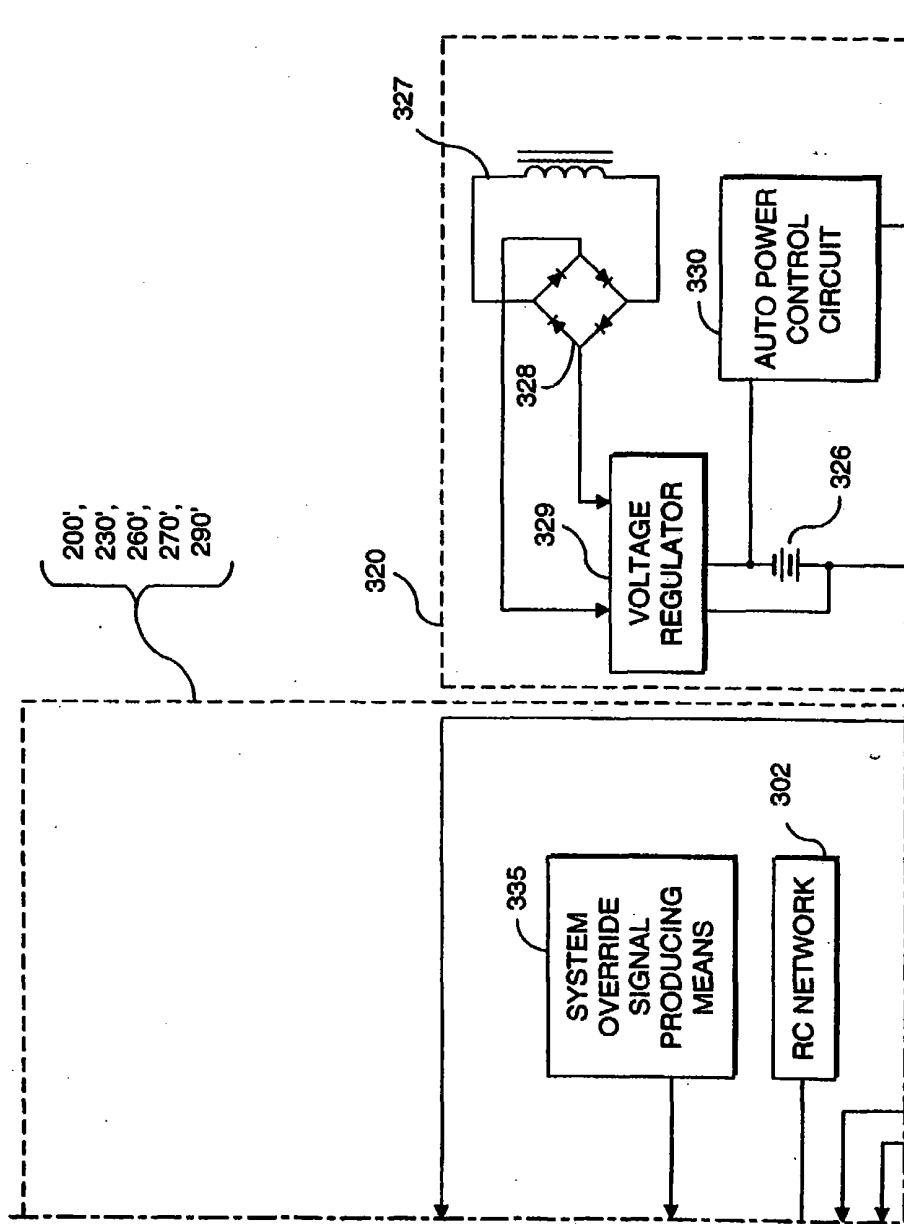


FIG. 22A3

66/161

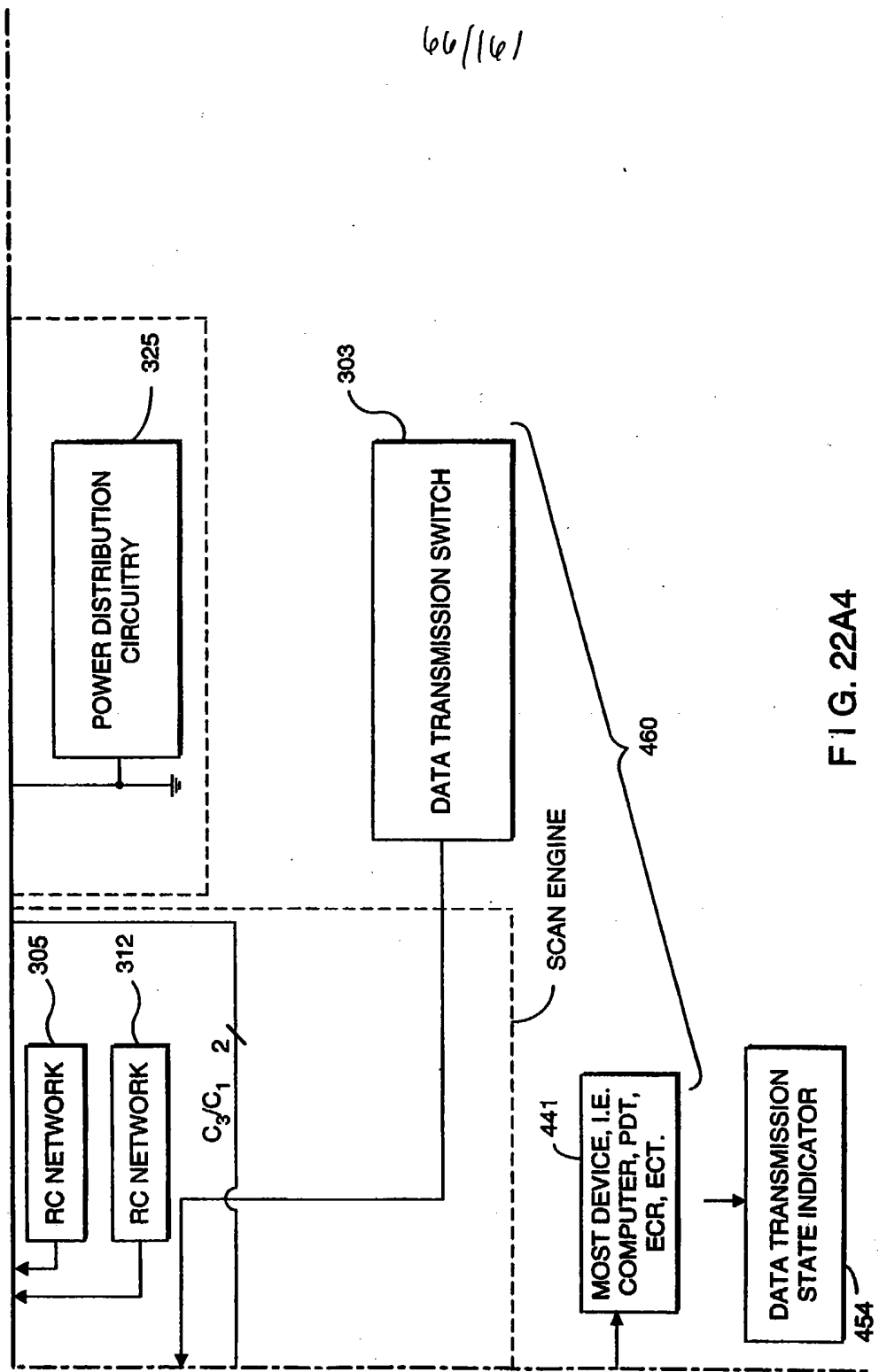


FIG. 22A4

67/161

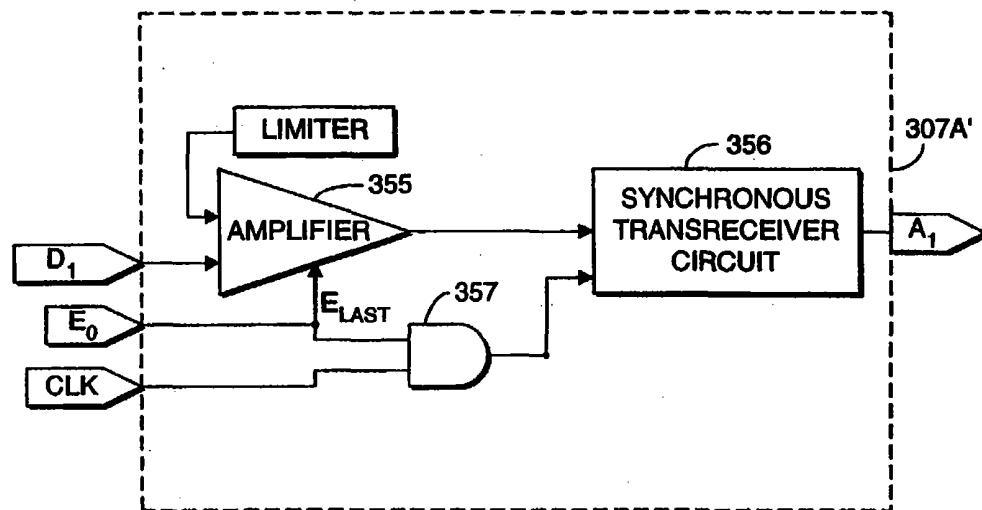


FIG. 22B

68/161

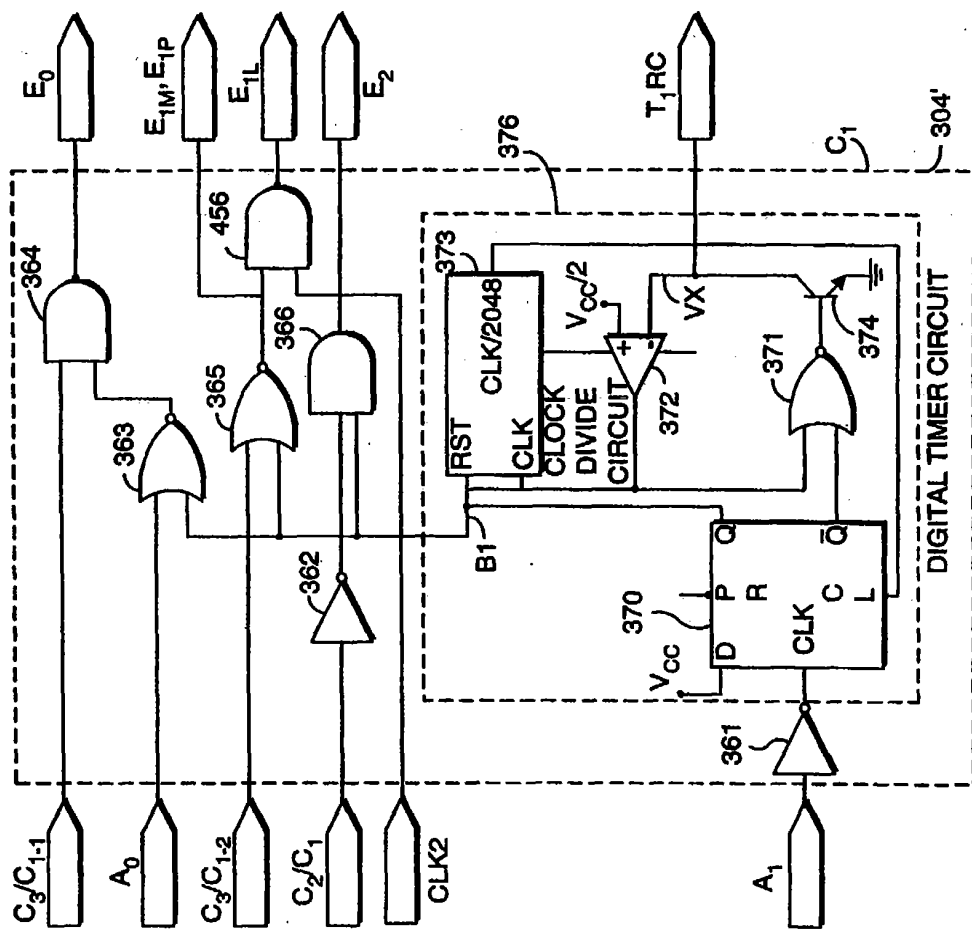


FIG. 22C

69/161

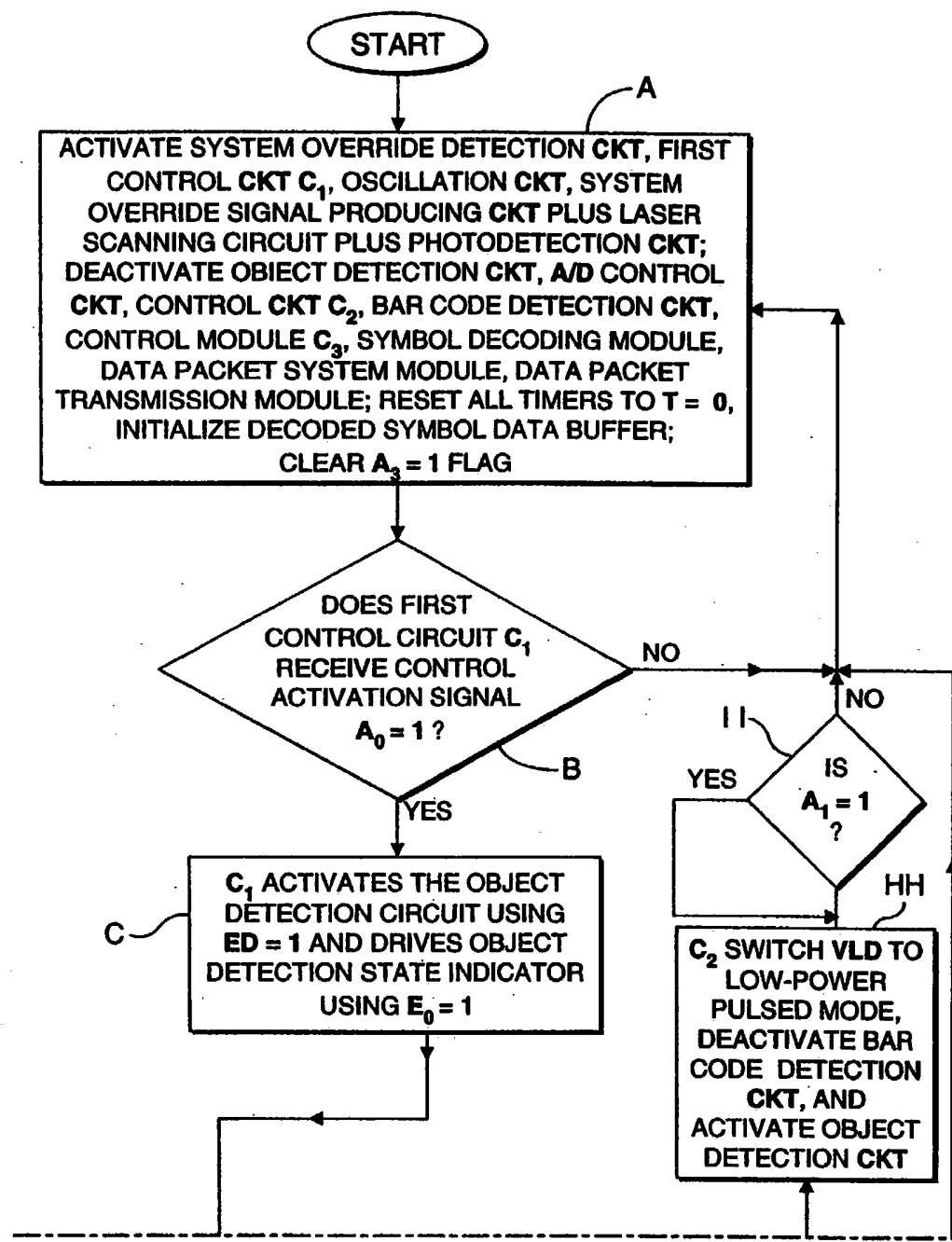


FIG. 23A1

70/161

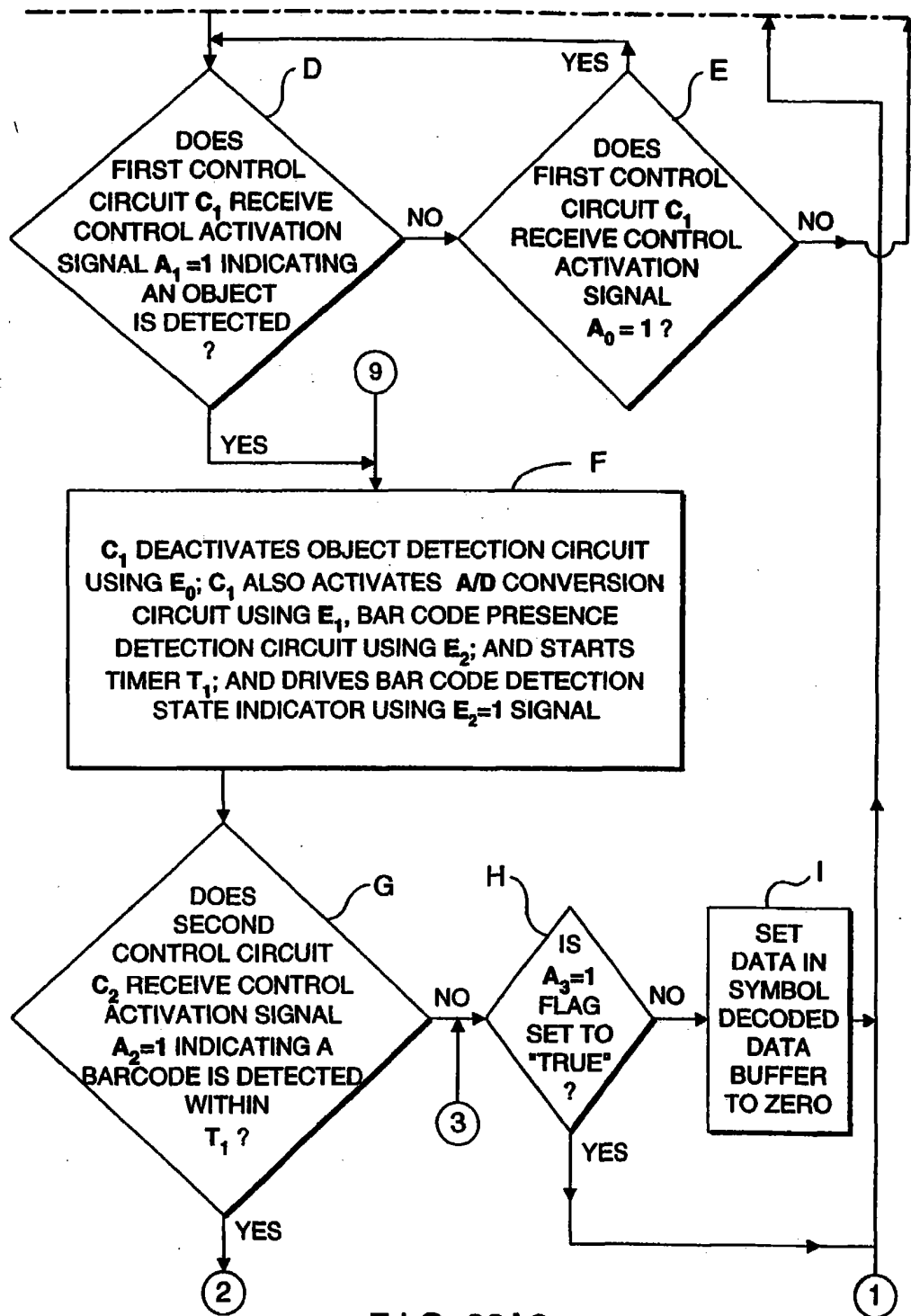
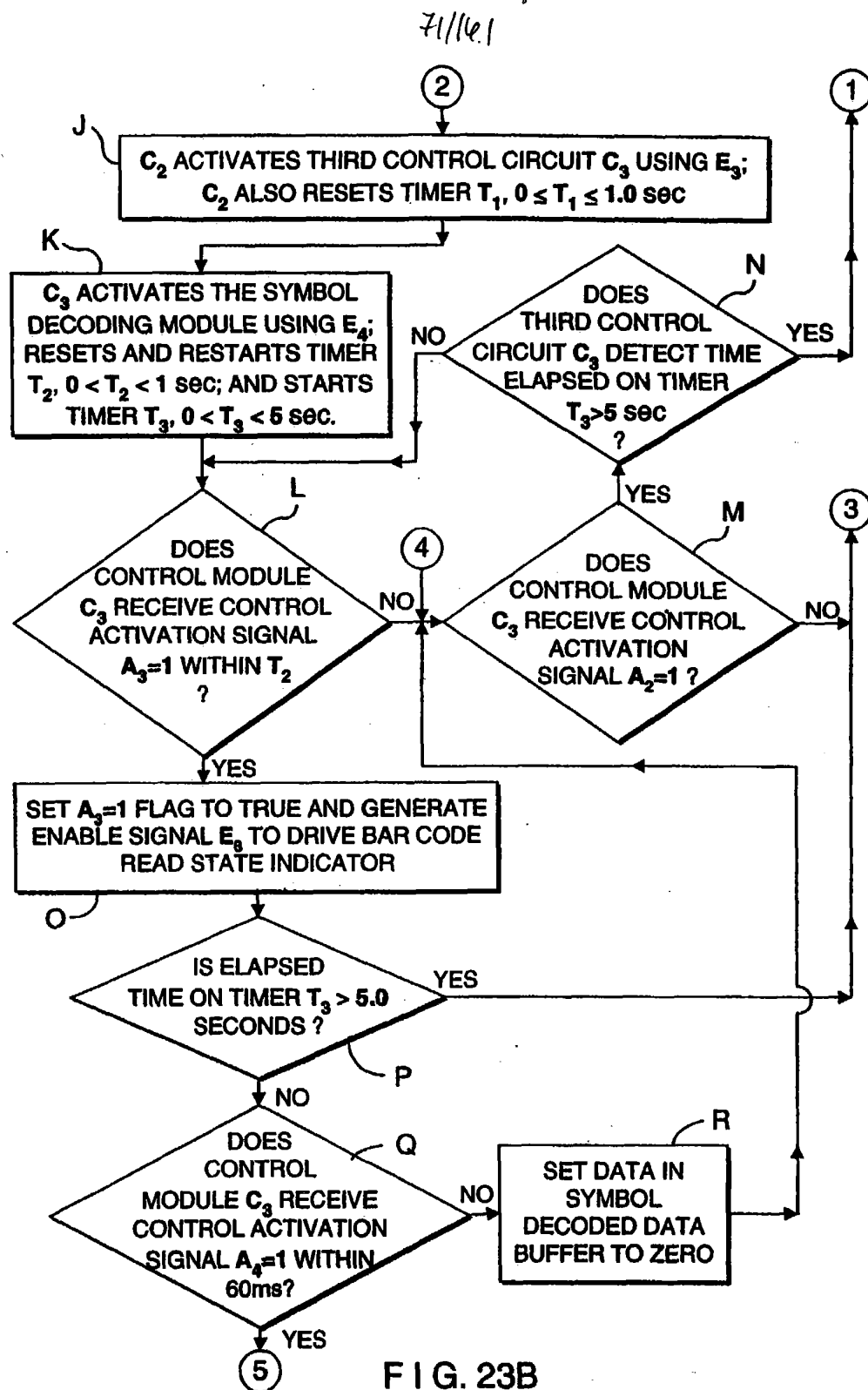


FIG. 23A2



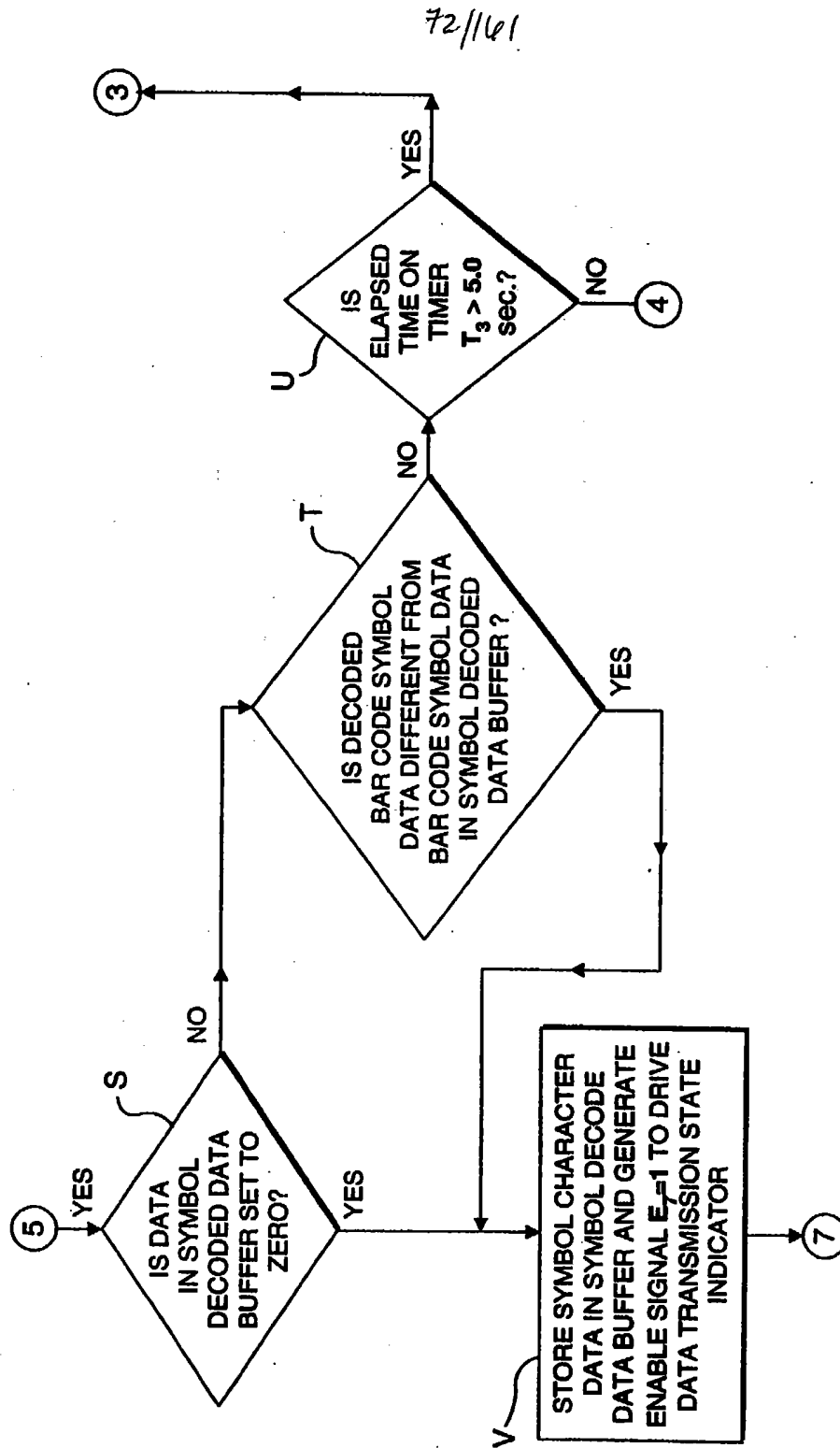


FIG. 23C

73/161

7

THIRD CONTROL CIRCUIT C_3 CONTINUES ACTIVATION OF LASER DIODE, SCANNING MOTOR, PHOTORECEIVING CIRCUIT, A/D CONVERSION CIRCUIT; DEACTIVATES SYMBOL DECODING MODULE; AND COMMENCES ACTIVATION DATA PACKET SYNTHESIS MODULE

W

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE SETS PACKET NUMBER TO 1 AND INCREMENTS DATA PACKET GROUP NUMBER MODULE COUNTER

X

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE CONSTRUCTS DATA PACKET CONSISTING OF SYMBOL CHARACTER DATA, TRANSMITTER NUMBER, DATA PACKET GROUP NUMBER, CHECK CHARACTER AND FRAMING CHARACTERS

Y

C_3 ACTIVATES DATA PACKET TRANSMISSION CIRCUIT

Z

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE OUTPUTS PACKET TO DATA PACKET TRANSMISSION CIRCUIT

AA

C_3 DETERMINES IS PACKET NUMBER < 3 ?

BB

NO → 8

YES

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE INCREMENTS DATA PACKET GROUP NUMBER

CC

C_3 ALLOWS T_5 TO EXPIRE IN ORDER TO DELAY TRANSMISSION BASED ON LAST TWO DIGITS OF TRANSMITTER NUMBER

DD

FIG. 23D

74/161

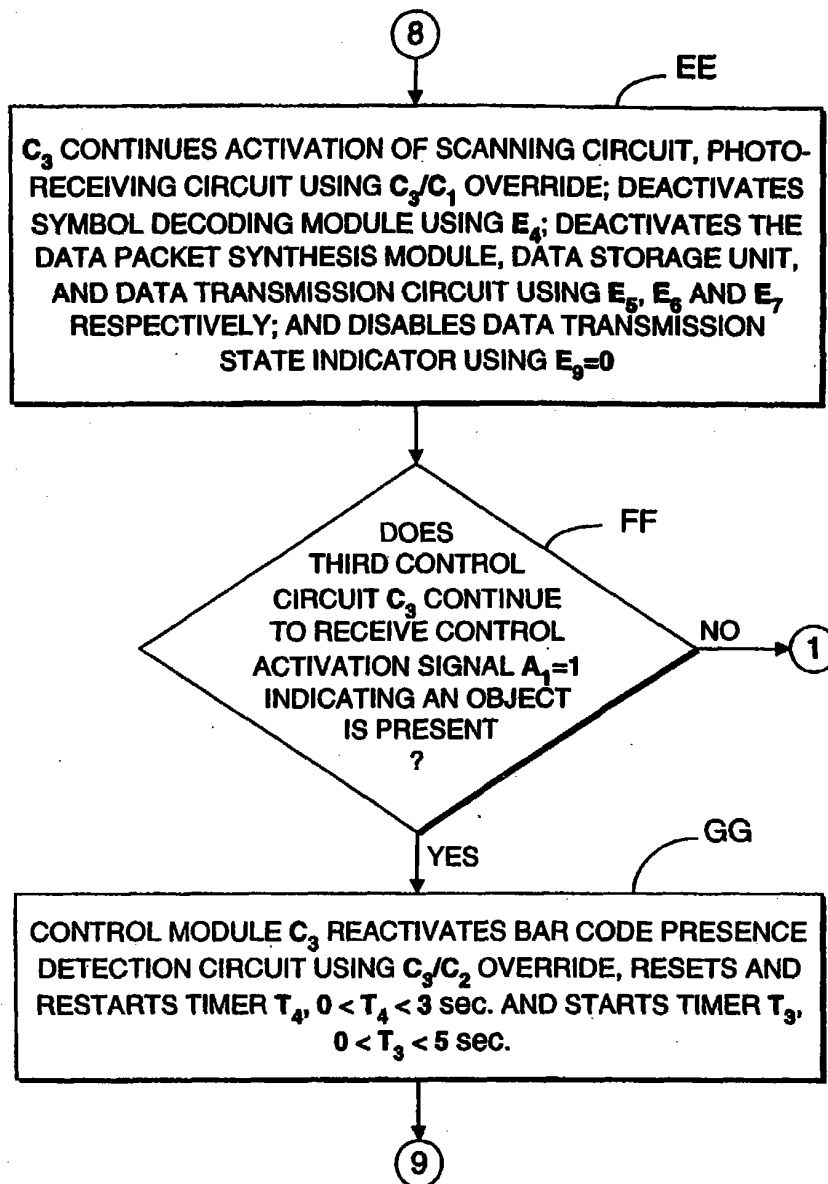


FIG. 23E

75/161.

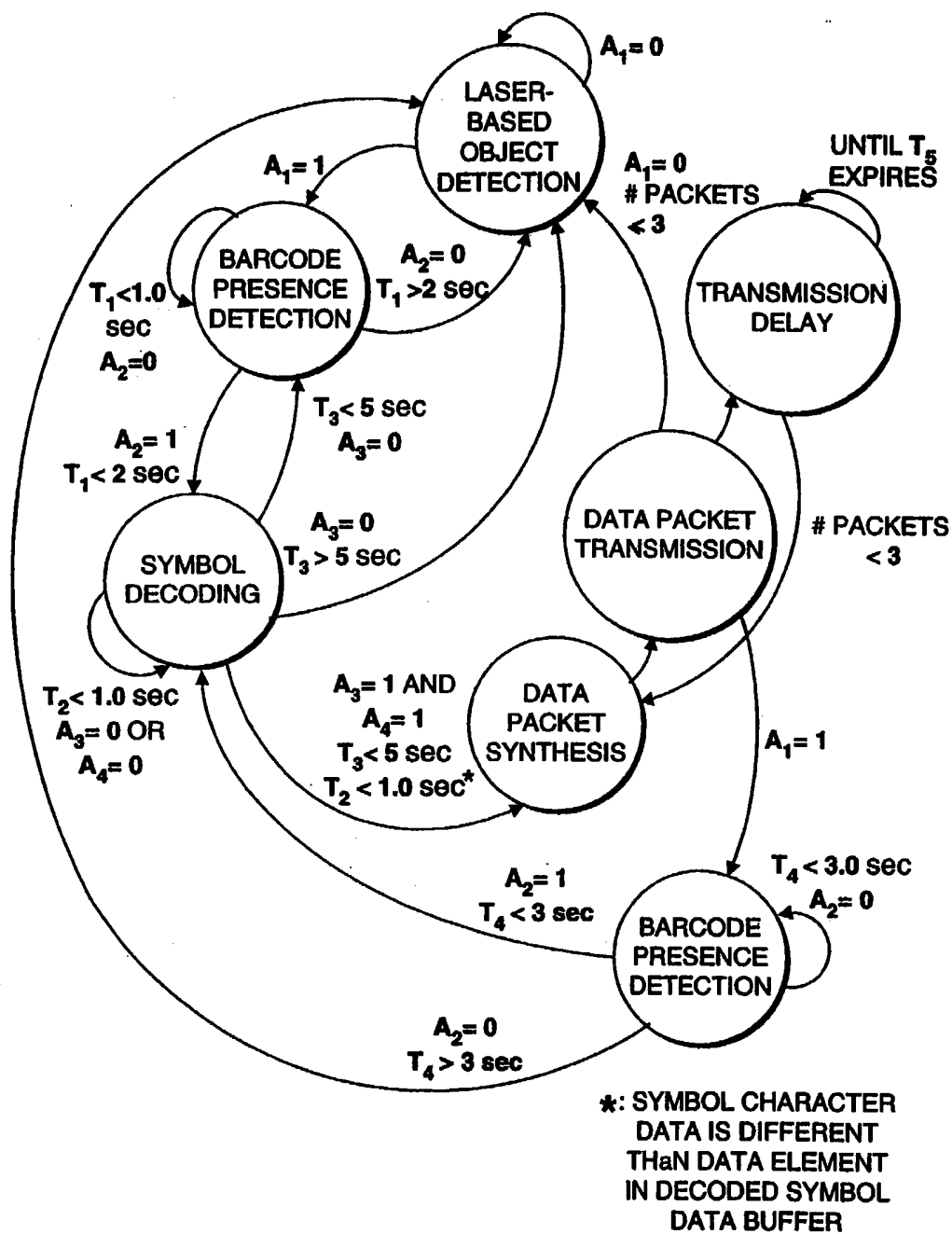


FIG. 24

77/161

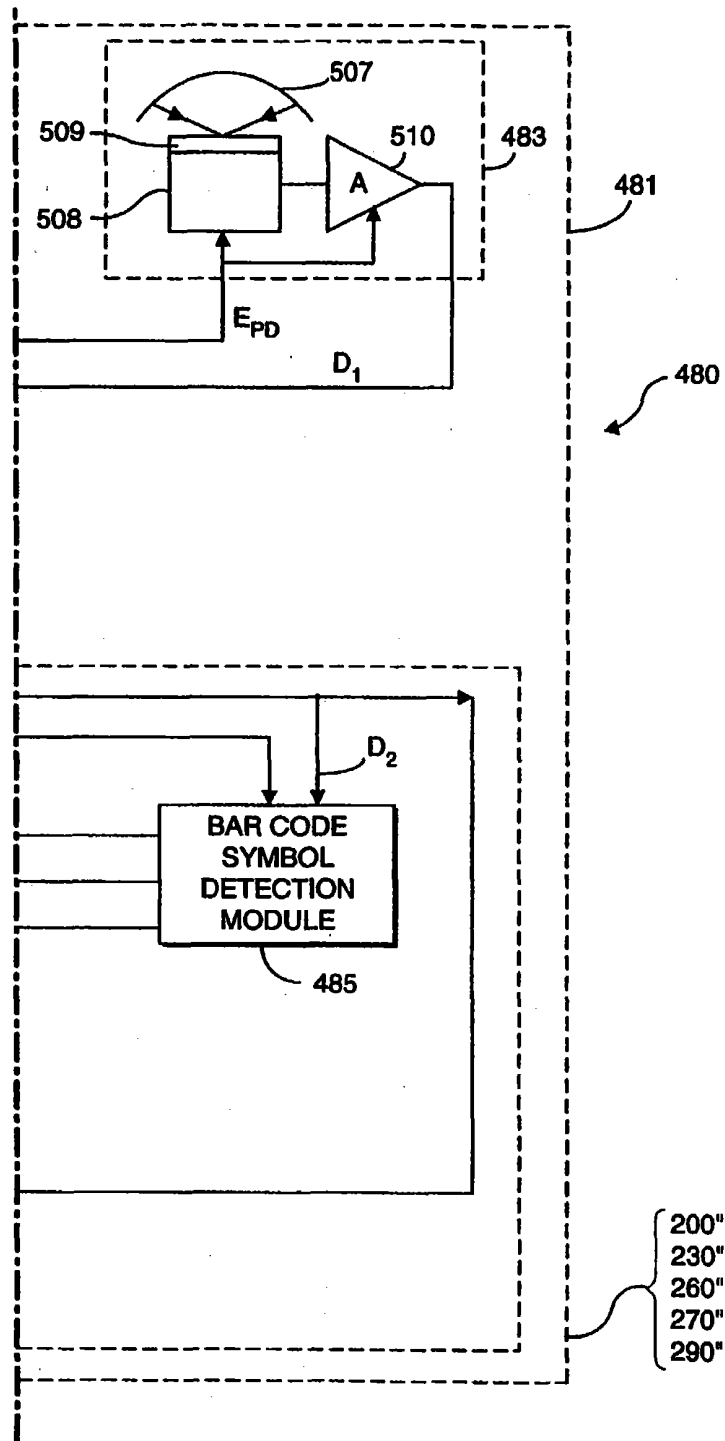


FIG. 25B

78/161

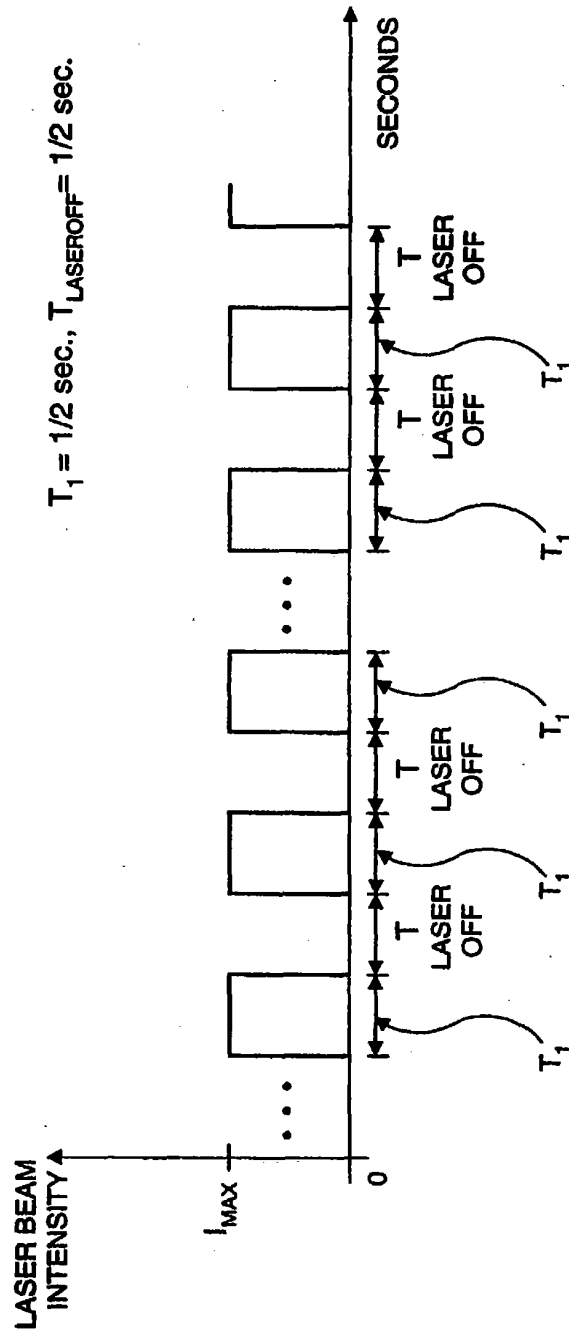


FIG. 26

79/161

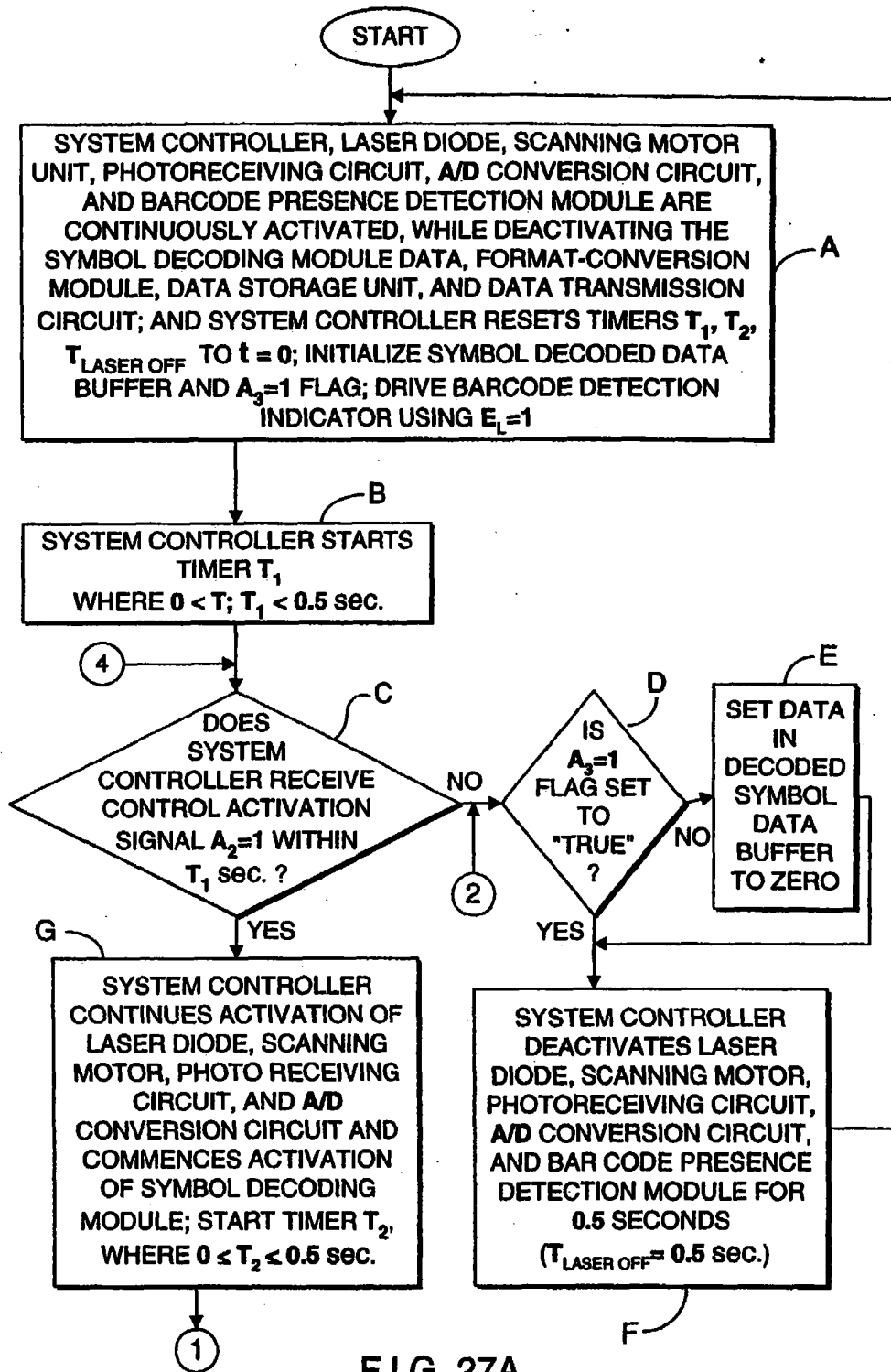


FIG. 27A

80/161

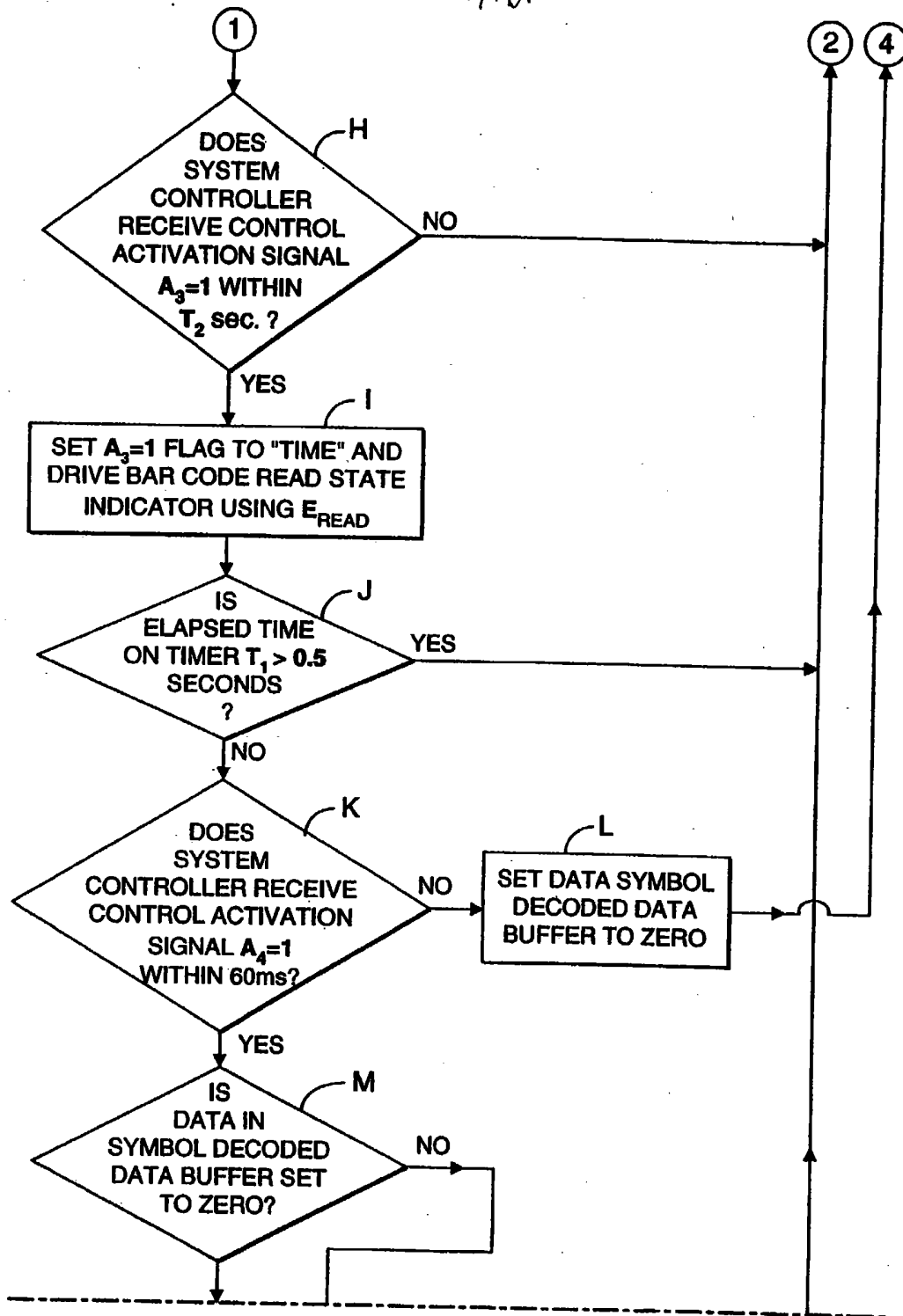


FIG. 27B1

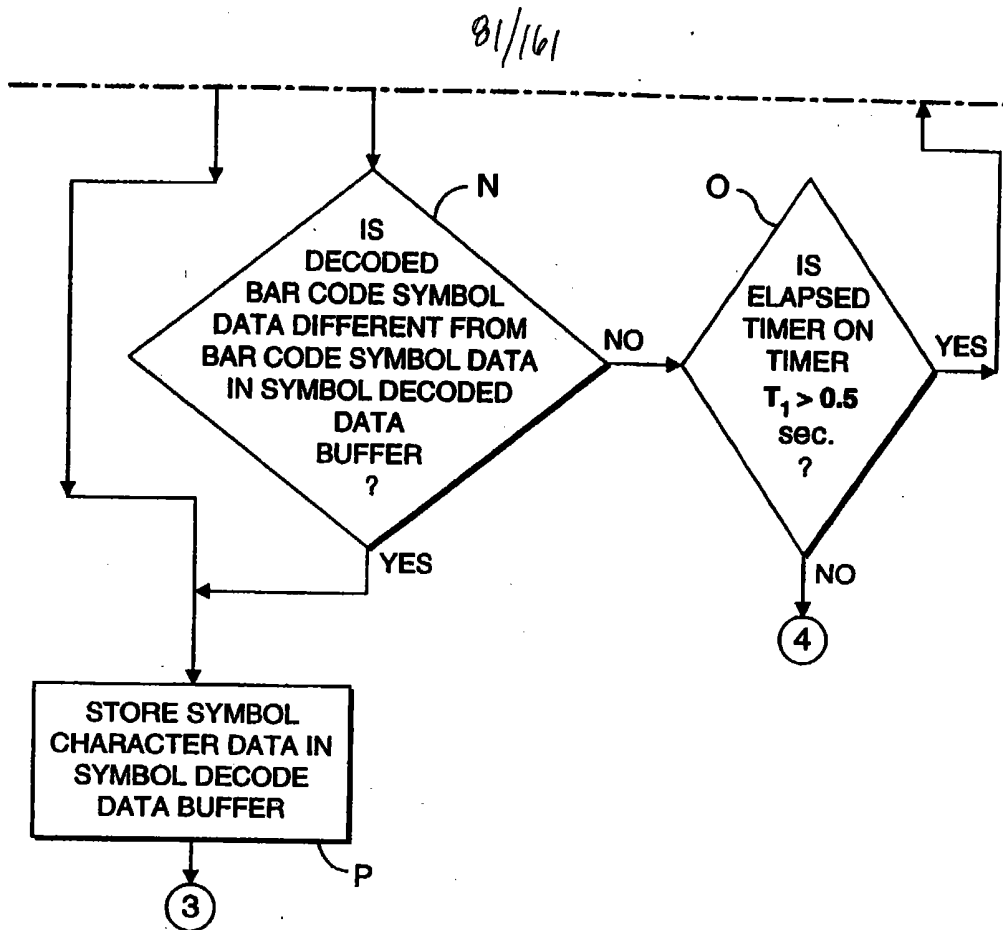


FIG. 27B2

82/101

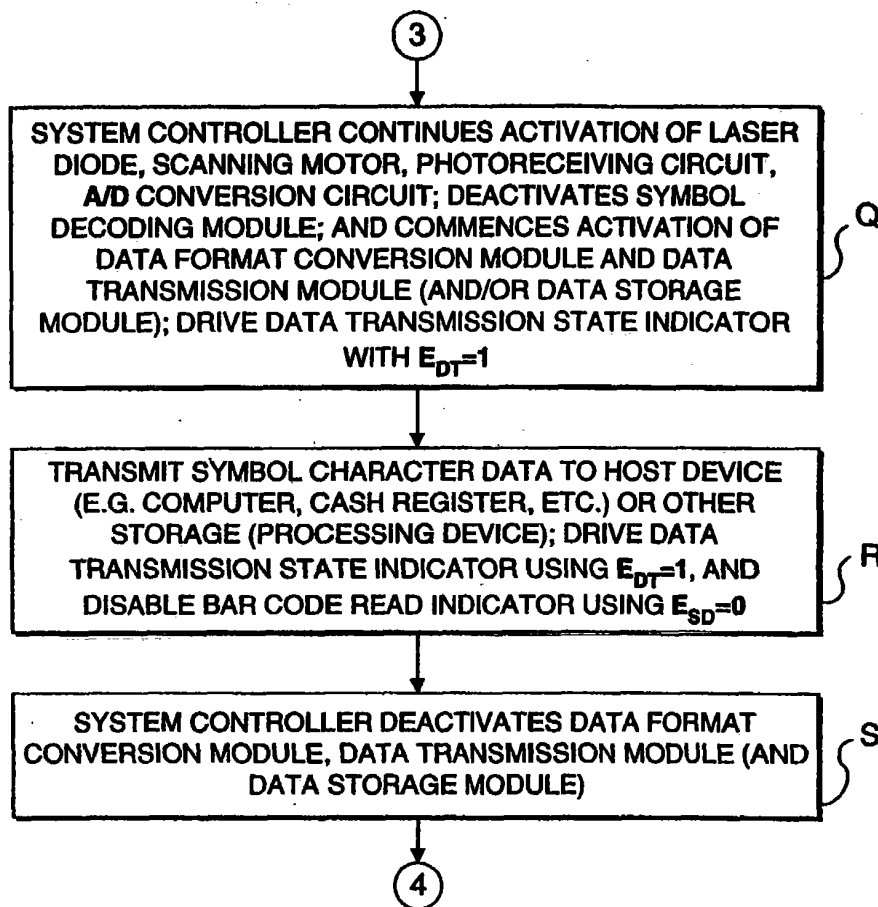


FIG. 27C

83/161

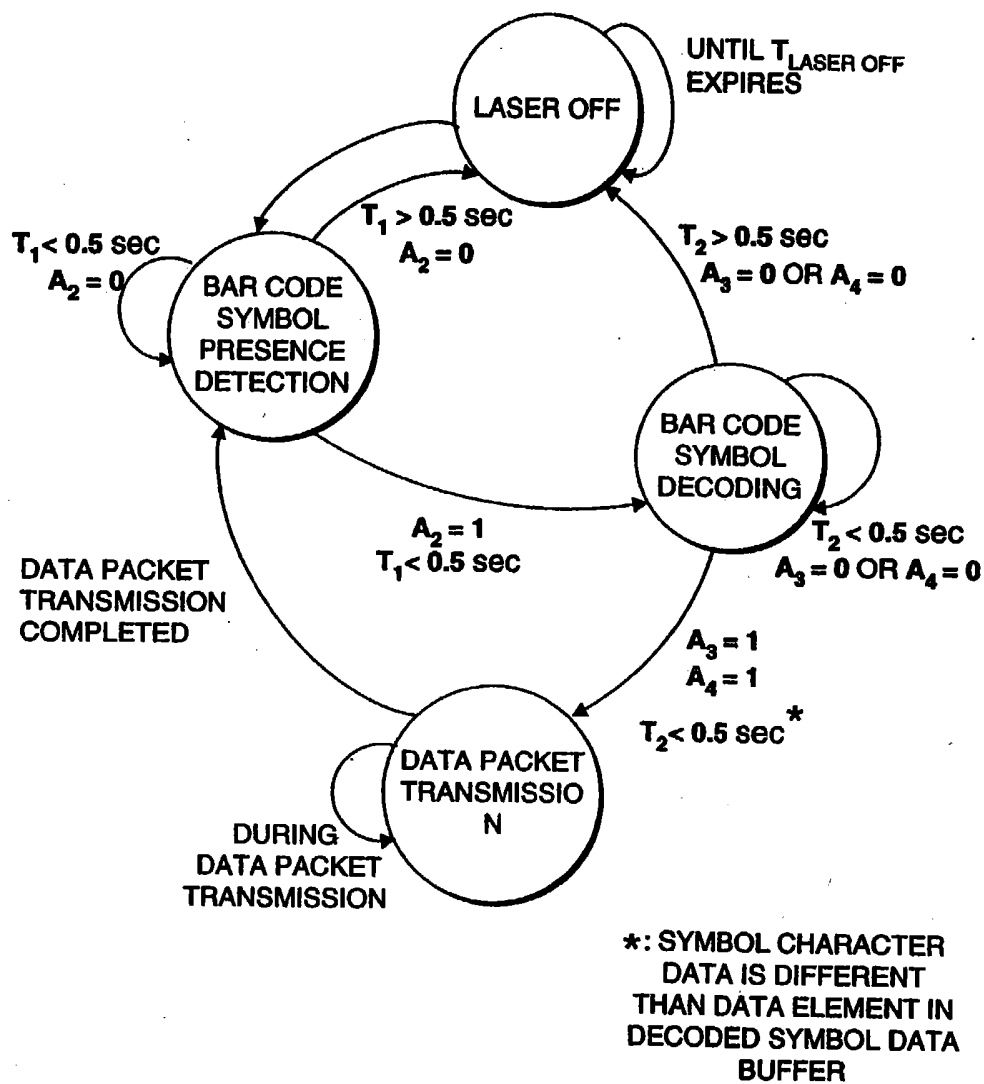


FIG. 28

The diagram illustrates a laser scanning system 308, which is divided into two main functional areas: a photo receiving circuit 309 and an IR object sensing circuit 306.

Photo Receiving Circuit 309: This section includes a photo cell 385, a pre-amplifier (PRE AMP) 387, and a motor 380. The photo cell 385 is connected to the pre-amp 387, which outputs a signal D_1 . The motor 380 is driven by a motor drive circuit 381, which is also connected to a VLD drive circuit 381 and a VLD circuit 385. The motor 380 is connected to a mirror 380, which reflects light from the photo cell 385. The motor 380 is also connected to a motor drive circuit 381, which is controlled by an enable signal E_{IM} . The motor 380 is also connected to a motor drive circuit 381, which is controlled by an enable signal E_{IL} . The motor 380 is also connected to a motor drive circuit 381, which is controlled by an enable signal E_{IPD} .

IR Object Sensing Circuit 306: This section includes an IR transmit drive 349 and an IR receive bias 358. The IR transmit drive 349 is connected to an IR receiver 307, which is connected to an object detection circuit 301. The IR receive bias 358 is connected to the IR receiver 307. The object detection circuit 301 is connected to a system override detection circuit 301, which is connected to an oscillator (OSC.) 301A and a resistor-capacitor network (RC OSC) 301B. The system override detection circuit 301 is also connected to a clock signal $CLK1$ and a resistor-capacitor network (RCBCD) 301B.

Control and Processing Circuits: The system includes a C_1 control circuit 304, which is connected to the object detection circuit 301 and the system override detection circuit 301. The C_1 control circuit 304 is also connected to a barcode presence detection circuit 301, which is connected to a C_2 control circuit 313. The C_2 control circuit 313 is connected to an A/D conversion circuit 310, which is connected to a photo cell 385. The A/D conversion circuit 310 is also connected to a pre-amp 387, which is connected to a photo cell 385. The A/D conversion circuit 310 is also connected to a pre-amp 387, which is connected to a photo cell 385.

Signal Flow and Connections: The diagram shows various signal lines connecting the different components. These include data lines D_0 , D_1 , D_2 , D_3 , D_4 , D_5 , D_6 , D_7 , D_8 , D_9 , D_{10} , D_{11} , D_{12} , D_{13} , D_{14} , D_{15} , D_{16} , D_{17} , D_{18} , D_{19} , D_{20} , D_{21} , D_{22} , D_{23} , D_{24} , D_{25} , D_{26} , D_{27} , D_{28} , D_{29} , D_{30} , D_{31} , D_{32} , D_{33} , D_{34} , D_{35} , D_{36} , D_{37} , D_{38} , D_{39} , D_{40} , D_{41} , D_{42} , D_{43} , D_{44} , D_{45} , D_{46} , D_{47} , D_{48} , D_{49} , D_{50} , D_{51} , D_{52} , D_{53} , D_{54} , D_{55} , D_{56} , D_{57} , D_{58} , D_{59} , D_{60} , D_{61} , D_{62} , D_{63} , D_{64} , D_{65} , D_{66} , D_{67} , D_{68} , D_{69} , D_{70} , D_{71} , D_{72} , D_{73} , D_{74} , D_{75} , D_{76} , D_{77} , D_{78} , D_{79} , D_{80} , D_{81} , D_{82} , D_{83} , D_{84} , D_{85} , D_{86} , D_{87} , D_{88} , D_{89} , D_{90} , D_{91} , D_{92} , D_{93} , D_{94} , D_{95} , D_{96} , D_{97} , D_{98} , D_{99} , D_{100} , D_{101} , D_{102} , D_{103} , D_{104} , D_{105} , D_{106} , D_{107} , D_{108} , D_{109} , D_{110} , D_{111} , D_{112} , D_{113} , D_{114} , D_{115} , D_{116} , D_{117} , D_{118} , D_{119} , D_{120} , D_{121} , D_{122} , D_{123} , D_{124} , D_{125} , D_{126} , D_{127} , D_{128} , D_{129} , D_{130} , D_{131} , D_{132} , D_{133} , D_{134} , D_{135} , D_{136} , D_{137} , D_{138} , D_{139} , D_{140} , D_{141} , D_{142} , D_{143} , D_{144} , D_{145} , D_{146} , D_{147} , D_{148} , D_{149} , D_{150} , D_{151} , D_{152} , D_{153} , D_{154} , D_{155} , D_{156} , D_{157} , D_{158} , D_{159} , D_{160} , D_{161} , D_{162} , D_{163} , D_{164} , D_{165} , D_{166} , D_{167} , D_{168} , D_{169} , D_{170} , D_{171} , D_{172} , D_{173} , D_{174} , D_{175} , D_{176} , D_{177} , D_{178} , D_{179} , D_{180} , D_{181} , D_{182} , D_{183} , D_{184} , D_{185} , D_{186} , D_{187} , D_{188} , D_{189} , D_{190} , D_{191} , D_{192} , D_{193} , D_{194} , D_{195} , D_{196} , D_{197} , D_{198} , D_{199} , D_{200} , D_{201} , D_{202} , D_{203} , D_{204} , D_{205} , D_{206} , D_{207} , D_{208} , D_{209} , D_{210} , D_{211} , D_{212} , D_{213} , D_{214} , D_{215} , D_{216} , D_{217} , D_{218} , D_{219} , D_{220} , D_{221} , D_{222} , D_{223} , D_{224} , D_{225} , D_{226} , D_{227} , D_{228} , D_{229} , D_{230} , D_{231} , D_{232} , D_{233} , D_{234} , D_{235} , D_{236} , D_{237} , D_{238} , D_{239} , D_{240} , D_{241} , D_{242} , D_{243} , D_{244} , D_{245} , D_{246} , D_{247} , D_{248} , D_{249} , D_{250} , D_{251} , D_{252} , D_{253} , D_{254} , D_{255} , D_{256} , D_{257} , D_{258} , D_{259} , D_{260} , D_{261} , D_{262} , D_{263} , D_{264} , D_{265} , D_{266} , D_{267} , D_{268} , D_{269} , D_{270} , D_{271} , D_{272} , D_{273} , D_{274} , D_{275} , D_{276} , D_{277} , D_{278} , D_{279} , D_{280} , D_{281} , D_{282} , D_{283} , D_{284} , D_{285} , D_{286} , D_{287} , D_{288} , D_{289} , D_{290} , D_{291} , D_{292} , D_{293} , D_{294} , D_{295} , D_{296} , D_{297} , D_{298} , D_{299} , D_{300} , D_{301} , D_{302} , D_{303} , D_{304} , D_{305} , D_{306} , D_{307} , D_{308} , D_{309} , D_{310} , D_{311} , D_{312} , D_{313} , D_{314} , D_{315} , D_{316} , D_{317} , D_{318} , D_{319} , D_{320} , D_{321} , D_{322} , D_{323} , D_{324} , D_{325} , D_{326} , D_{327} , D_{328} , D_{329} , D_{330} , D_{331} , D_{332} , D_{333} , D_{334} , D_{335} , D_{336} , D_{337} , D_{338} , D_{339} , D_{340} , D_{341} , D_{342} , D_{343} , D_{344} , D_{34

FIG. 29A1

85/161

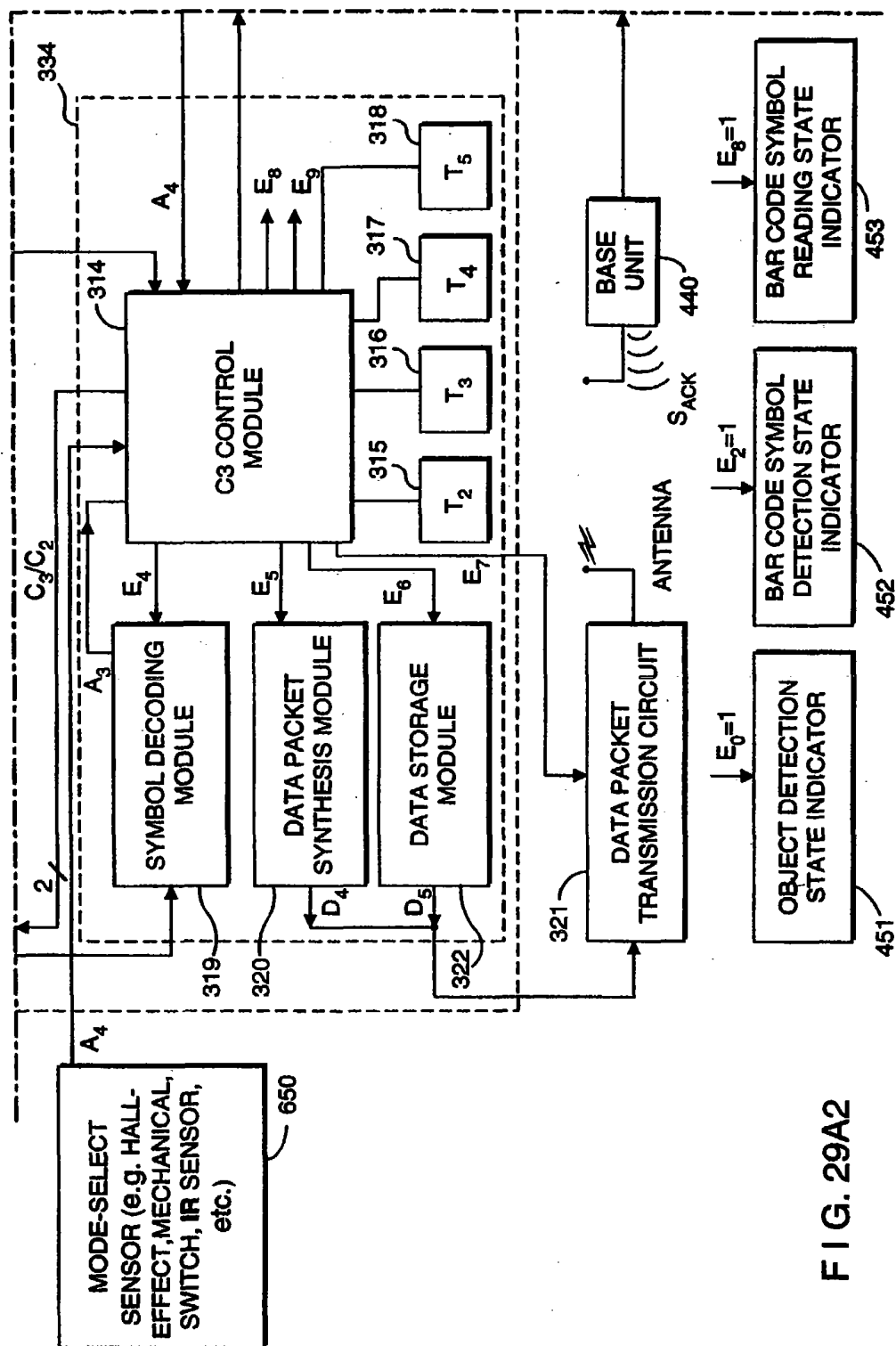


FIG. 29A2

86/1161

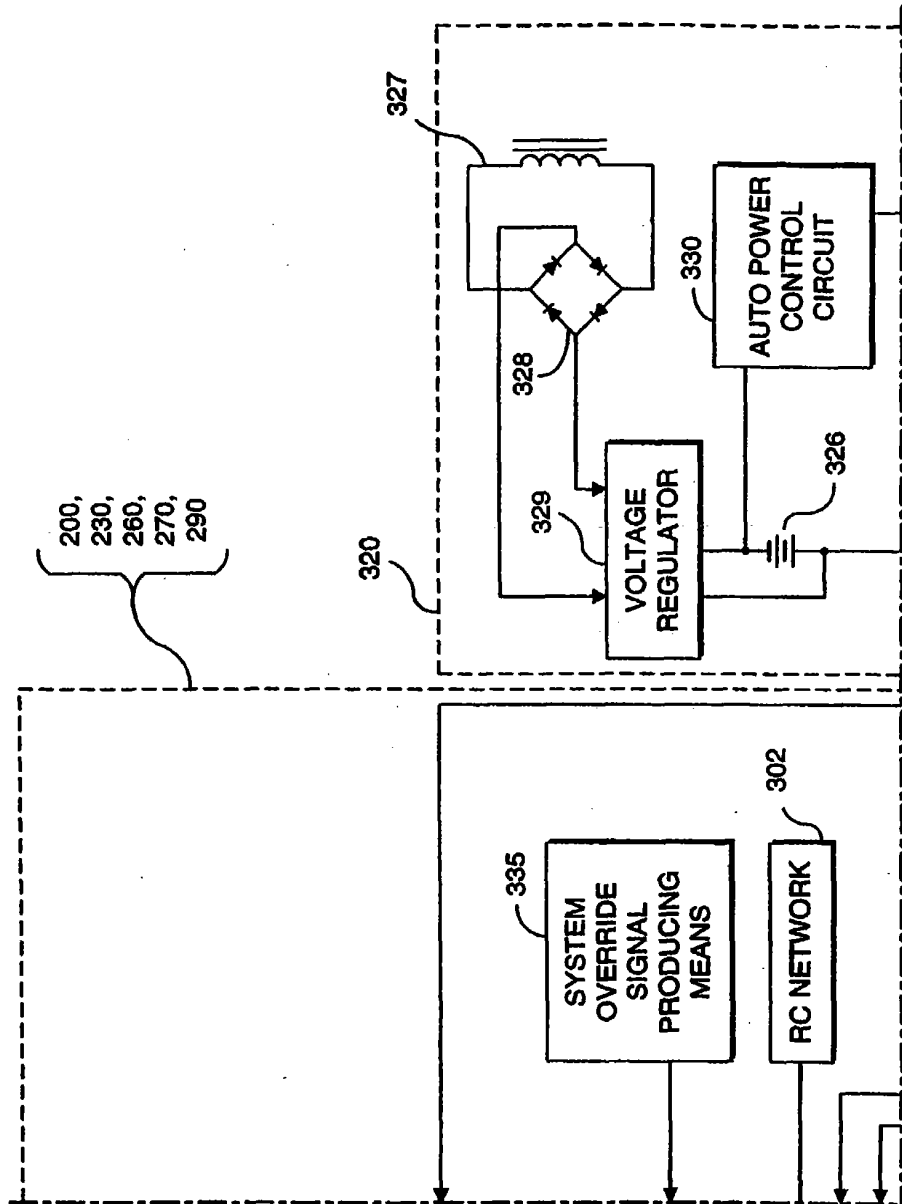
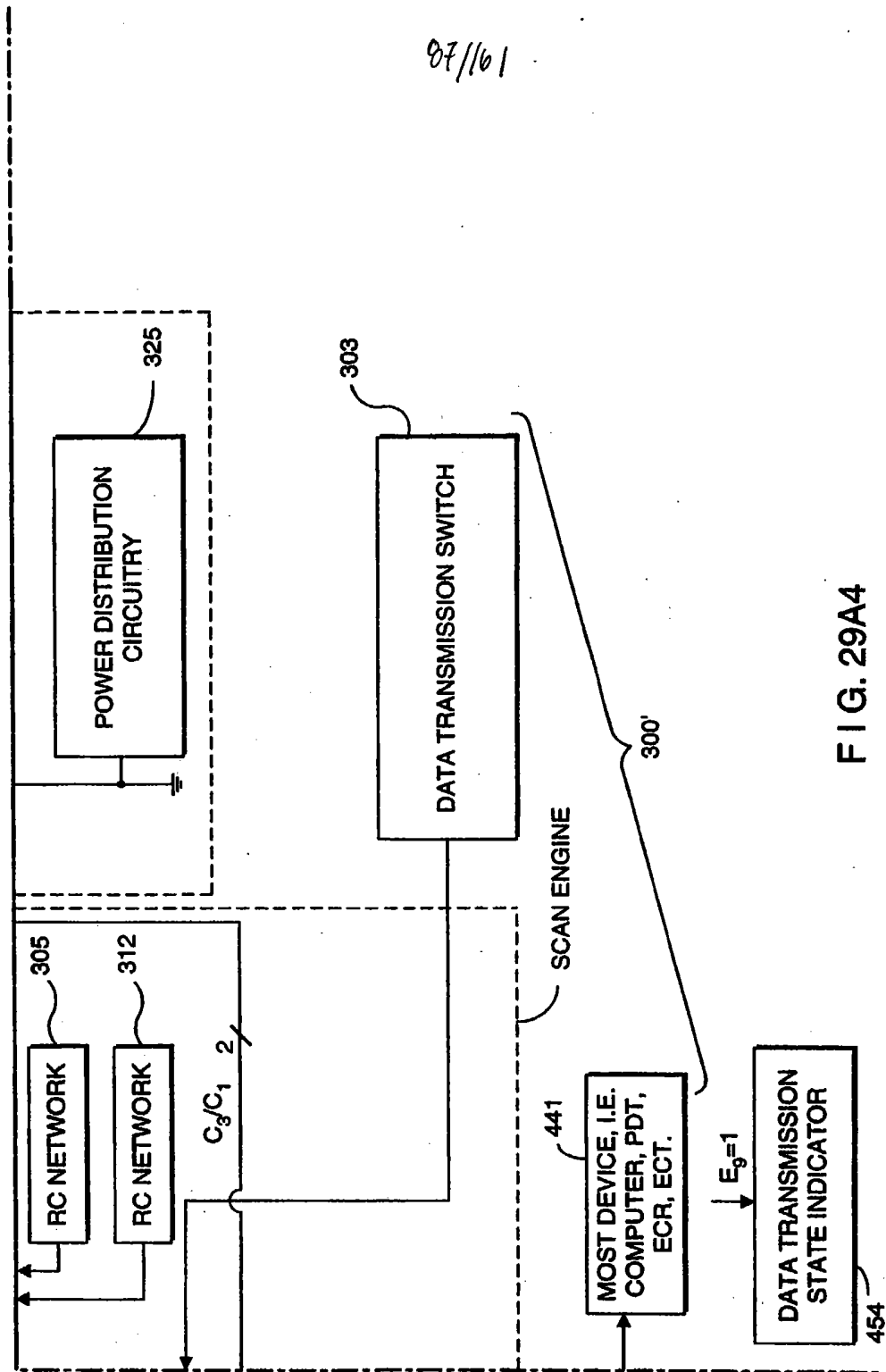


FIG. 29A3



88/161

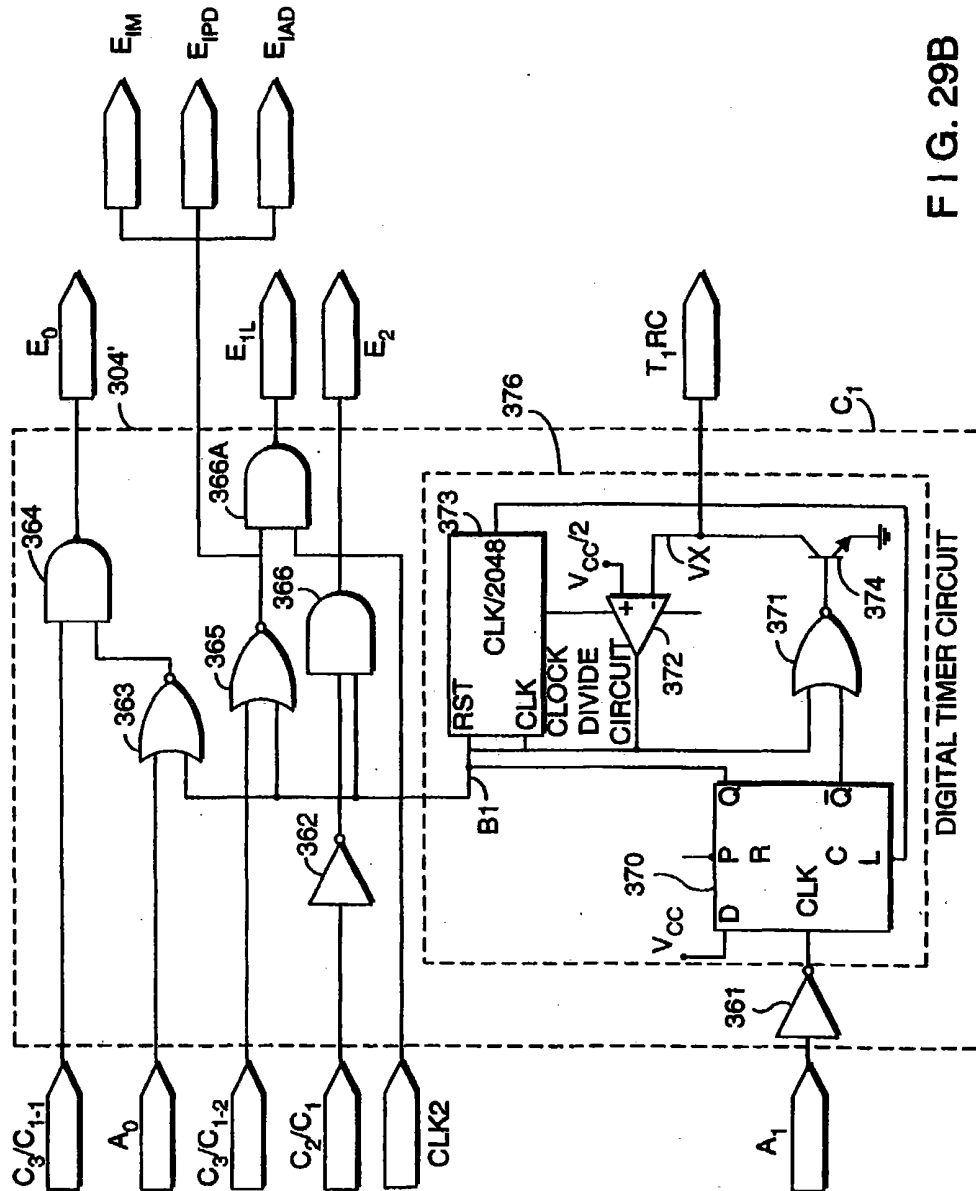


FIG. 29B

89/161

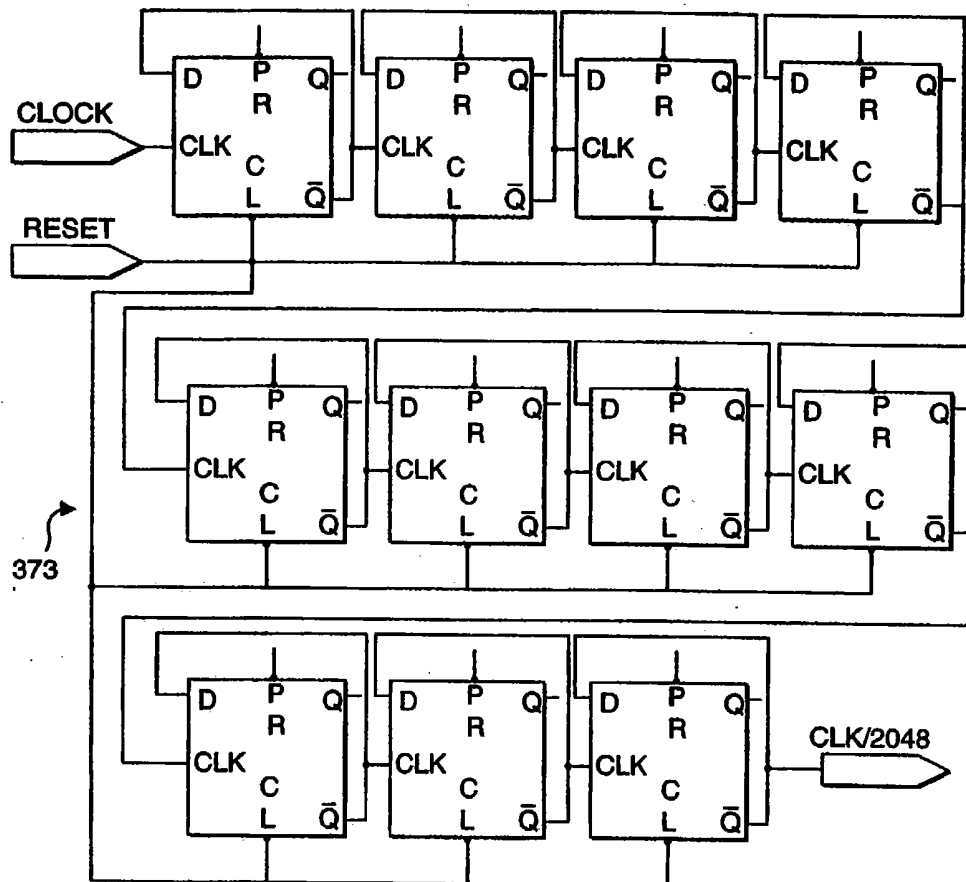


FIG. 29C

90/161

$$\left\{ \begin{array}{l} E_0 = \overline{(B1 + A_0) (C_3 / C_{1-1})} \\ E_{IM} = E_{IPD} = E_{IAD} = \overline{(C_3 / C_{1-2}) + B1} \\ E_L = \overline{[(C_3 / C_{1-1}) + B1] [B2]} \\ E_2 = \overline{(C_2 / C_1) (B1)} \end{array} \right.$$

FIG. 29D

91/161

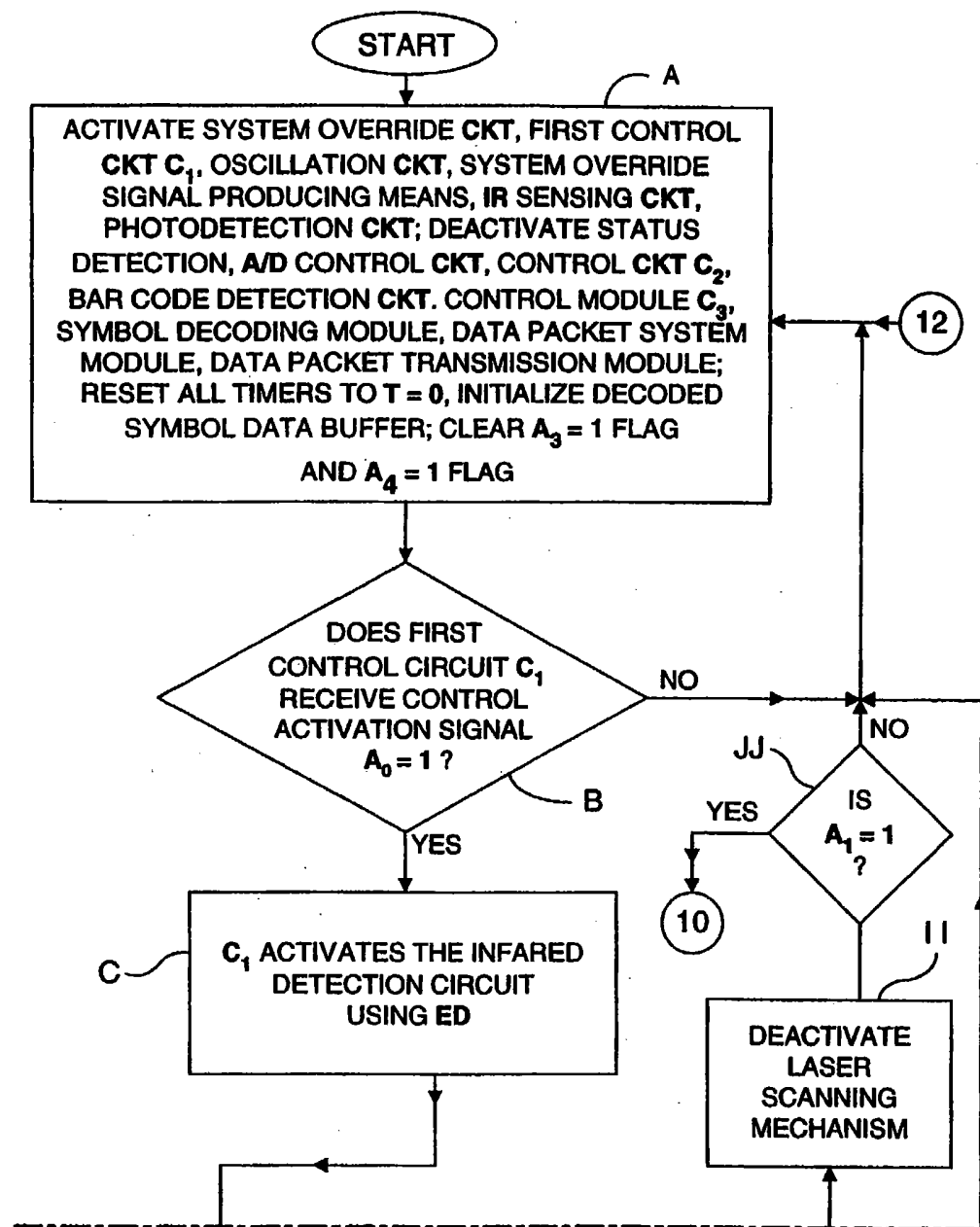


FIG. 30A1

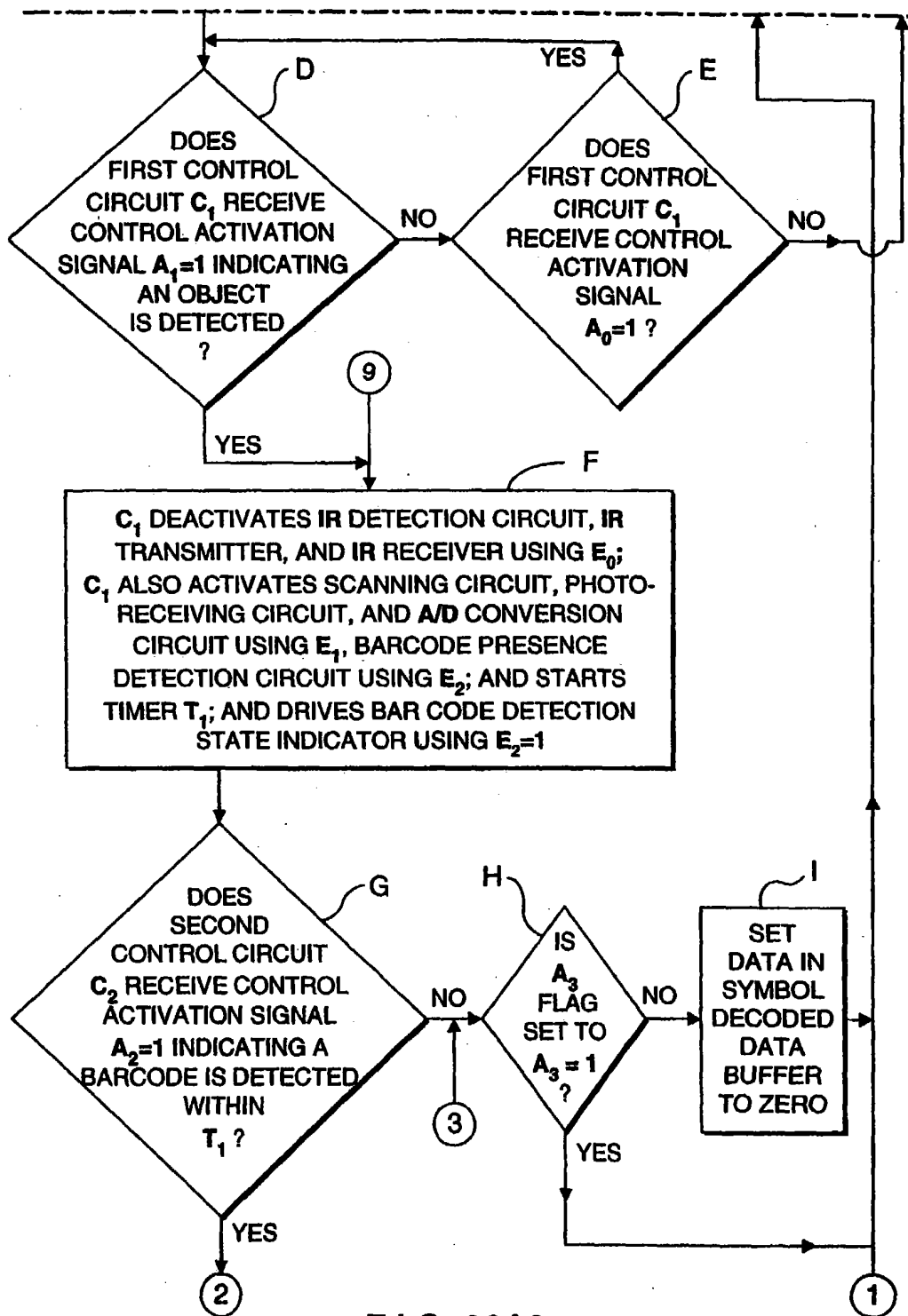


FIG. 30A2

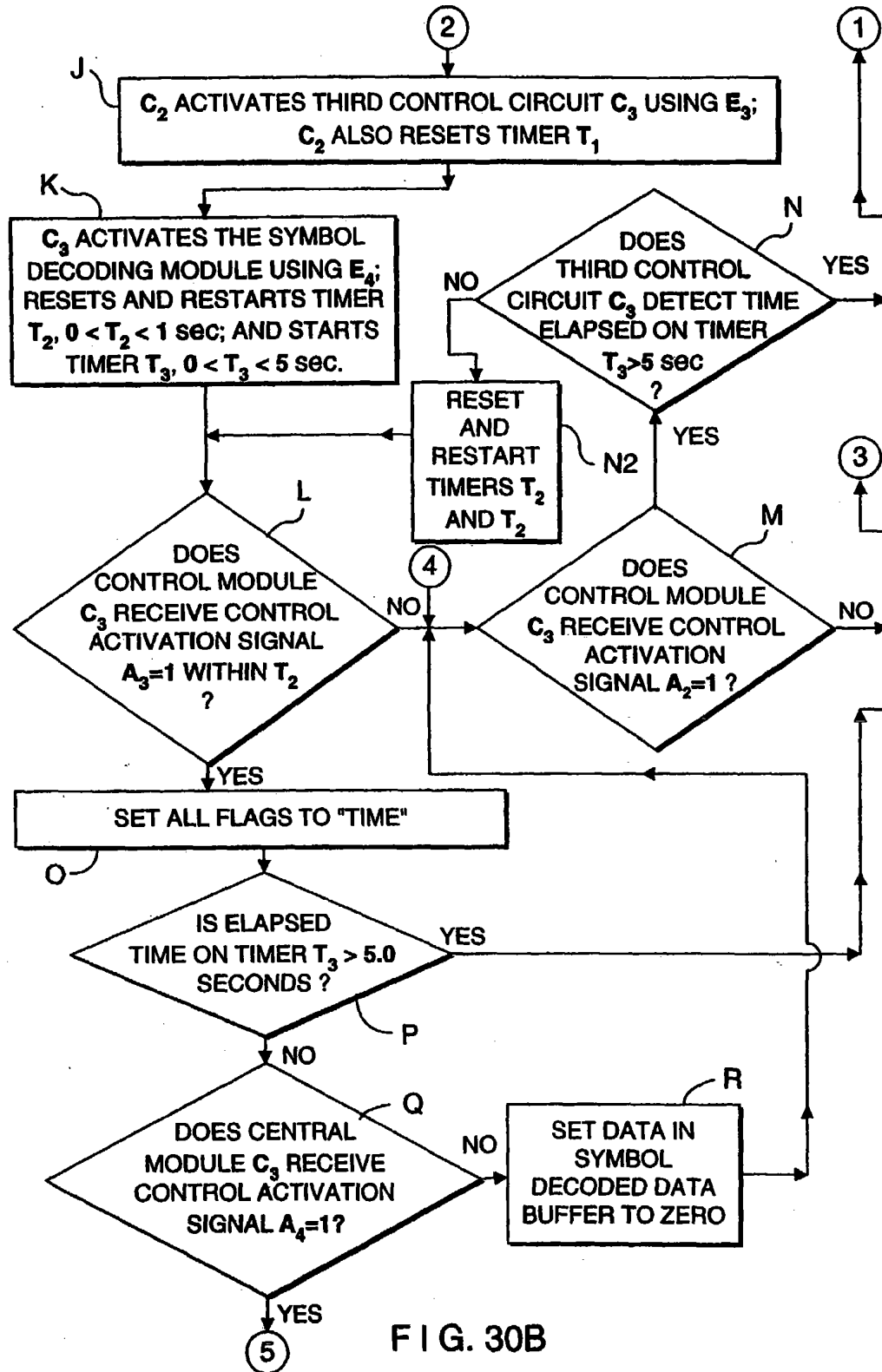


FIG. 30B

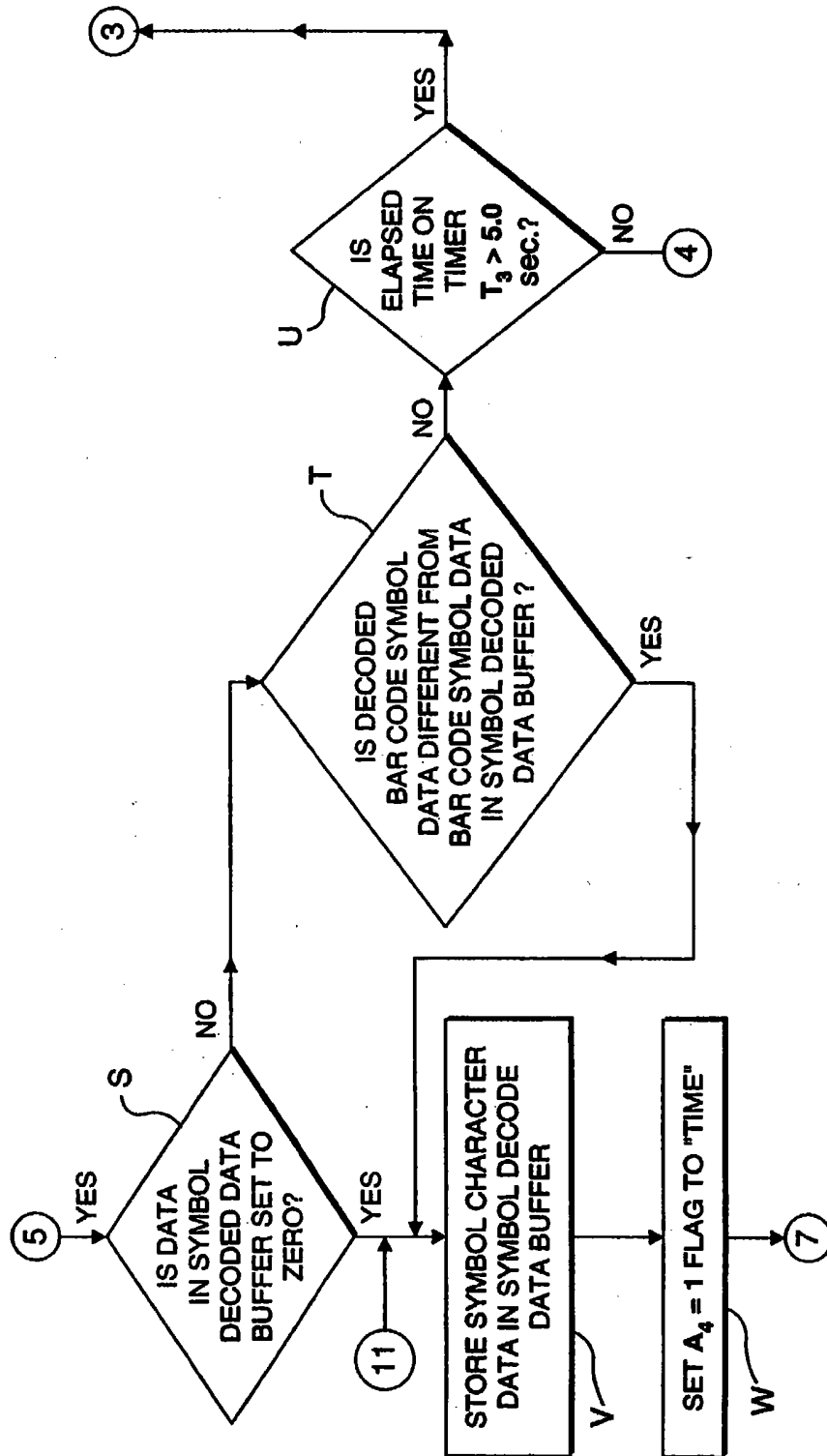


FIG. 30C

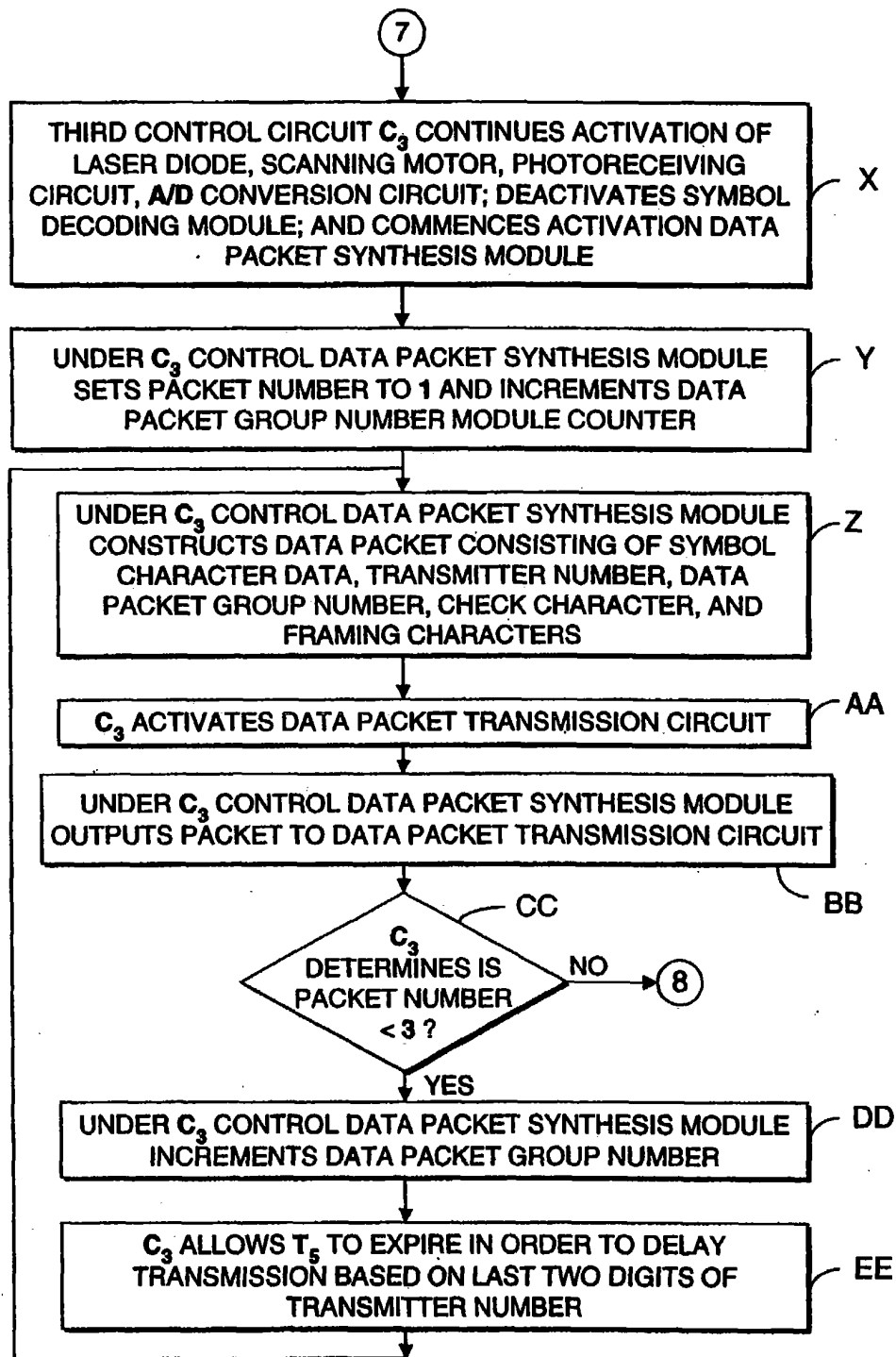


FIG. 30D

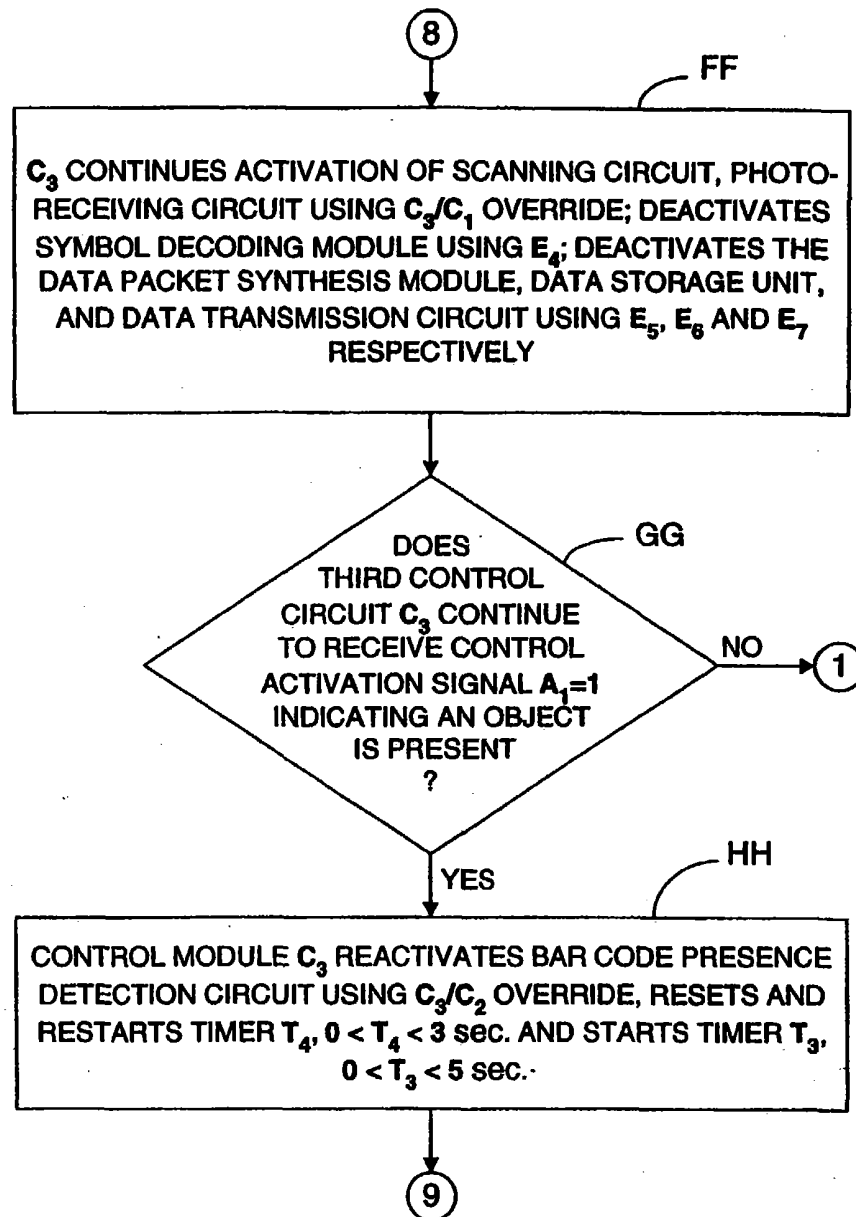


FIG. 30E

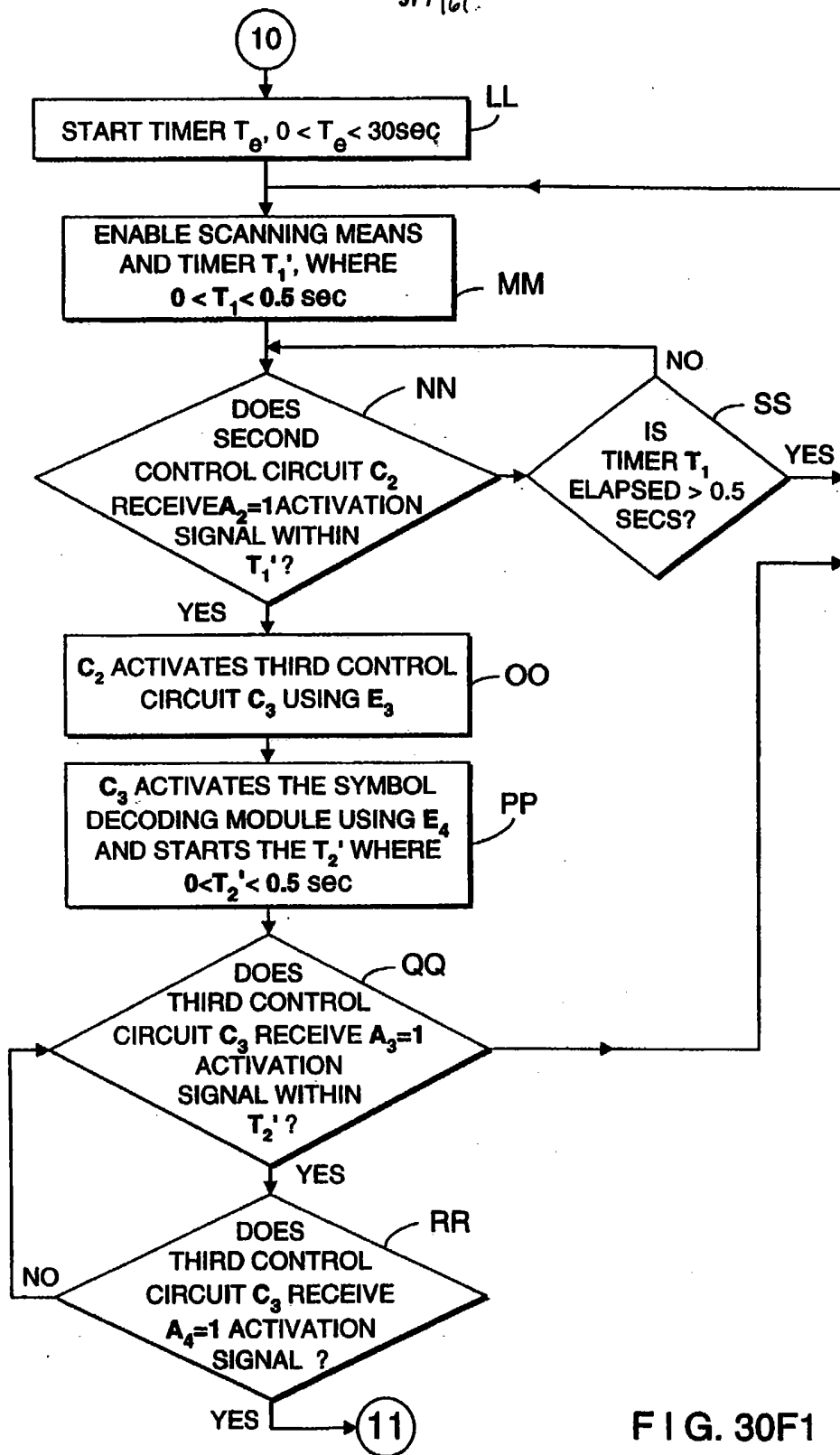
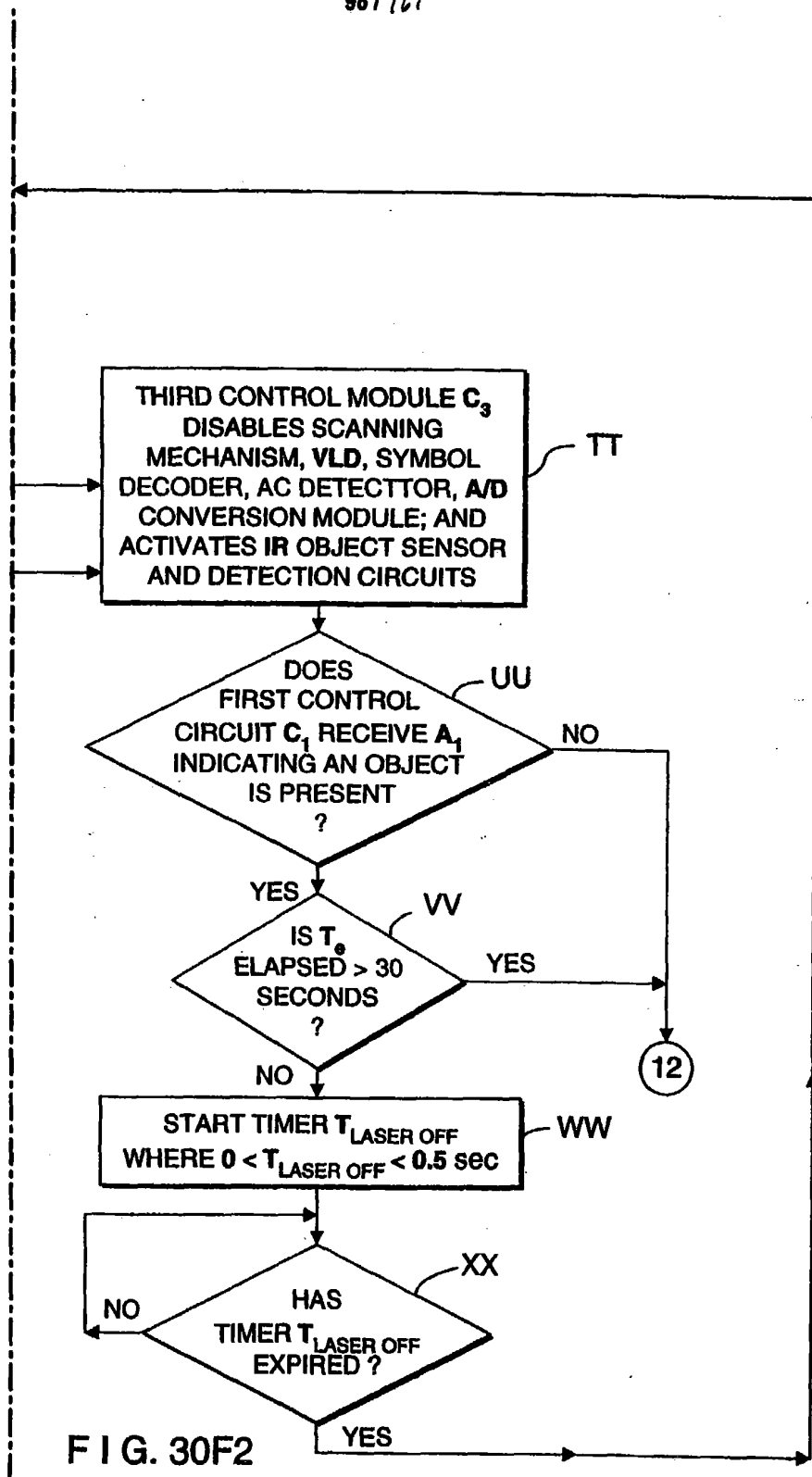


FIG. 30F1



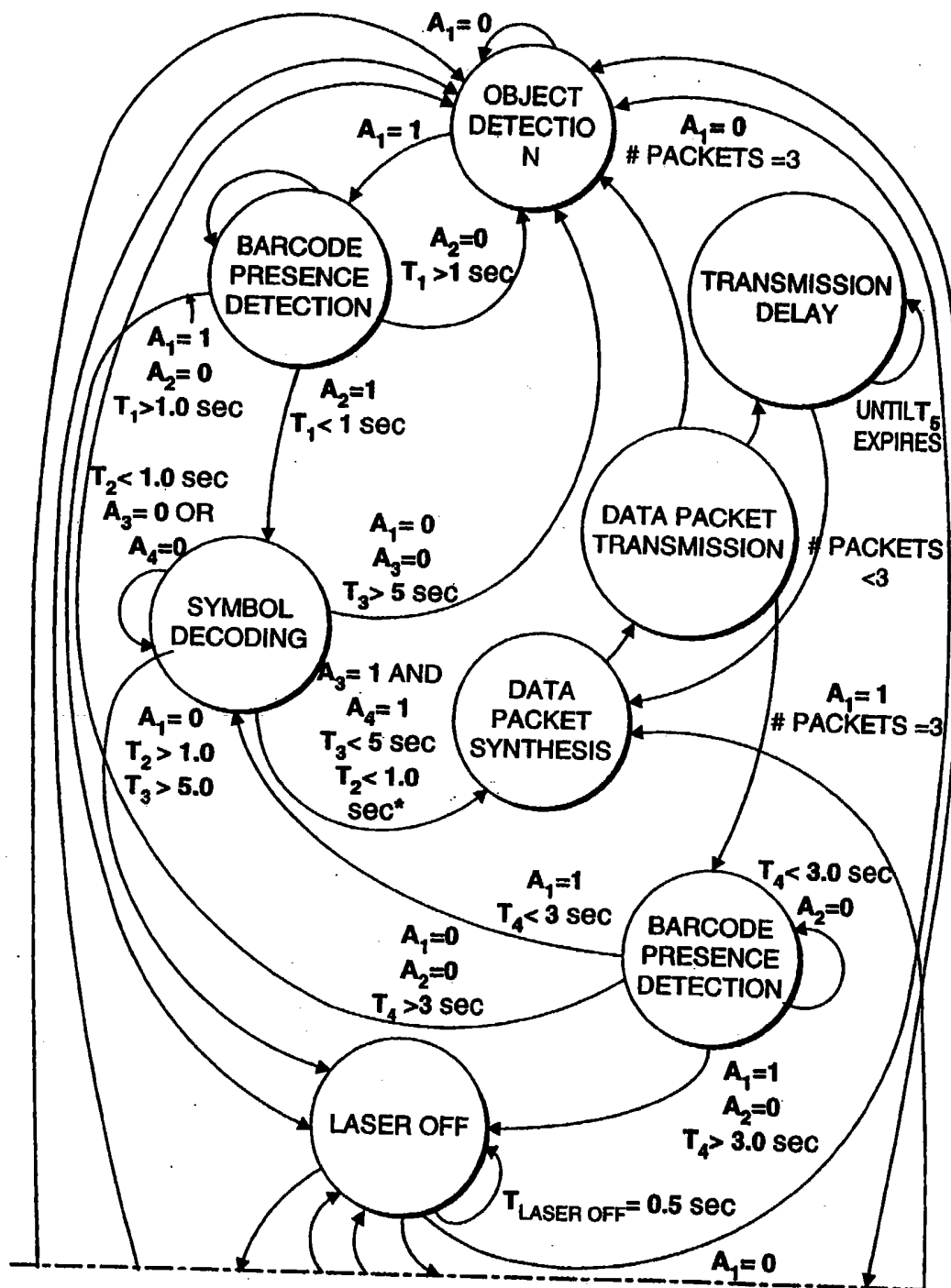


FIG. 31A

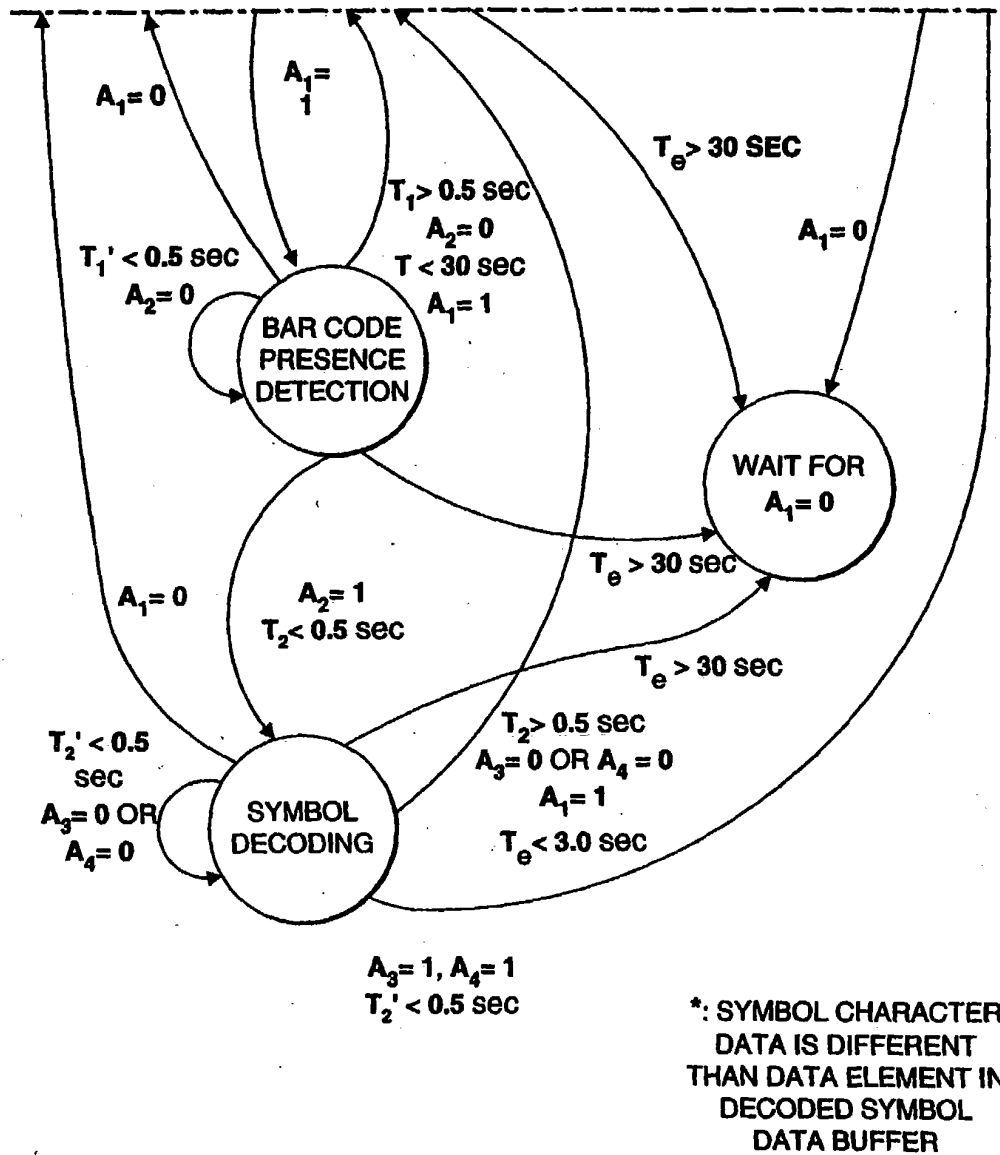


FIG. 31B

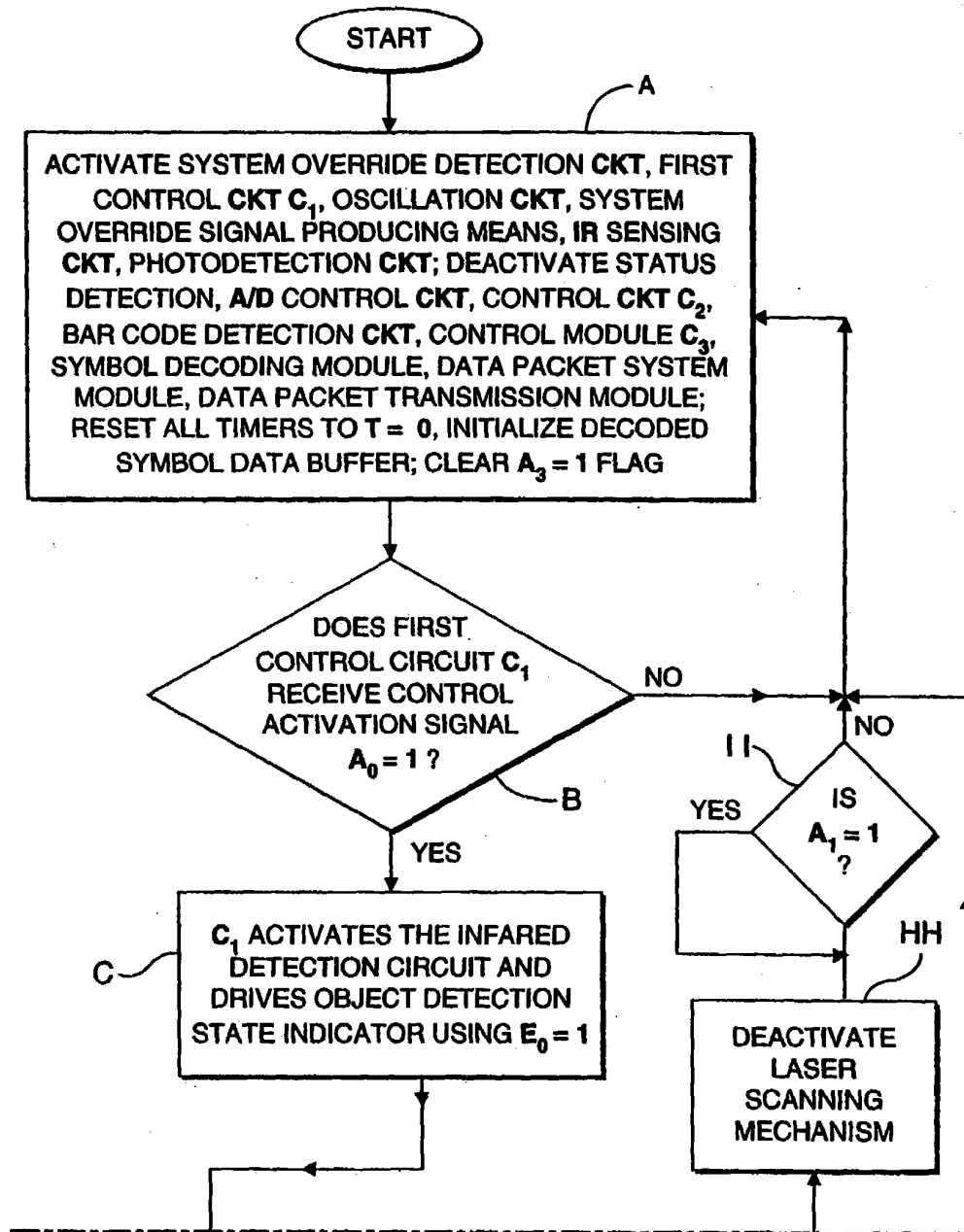


FIG. 32A1

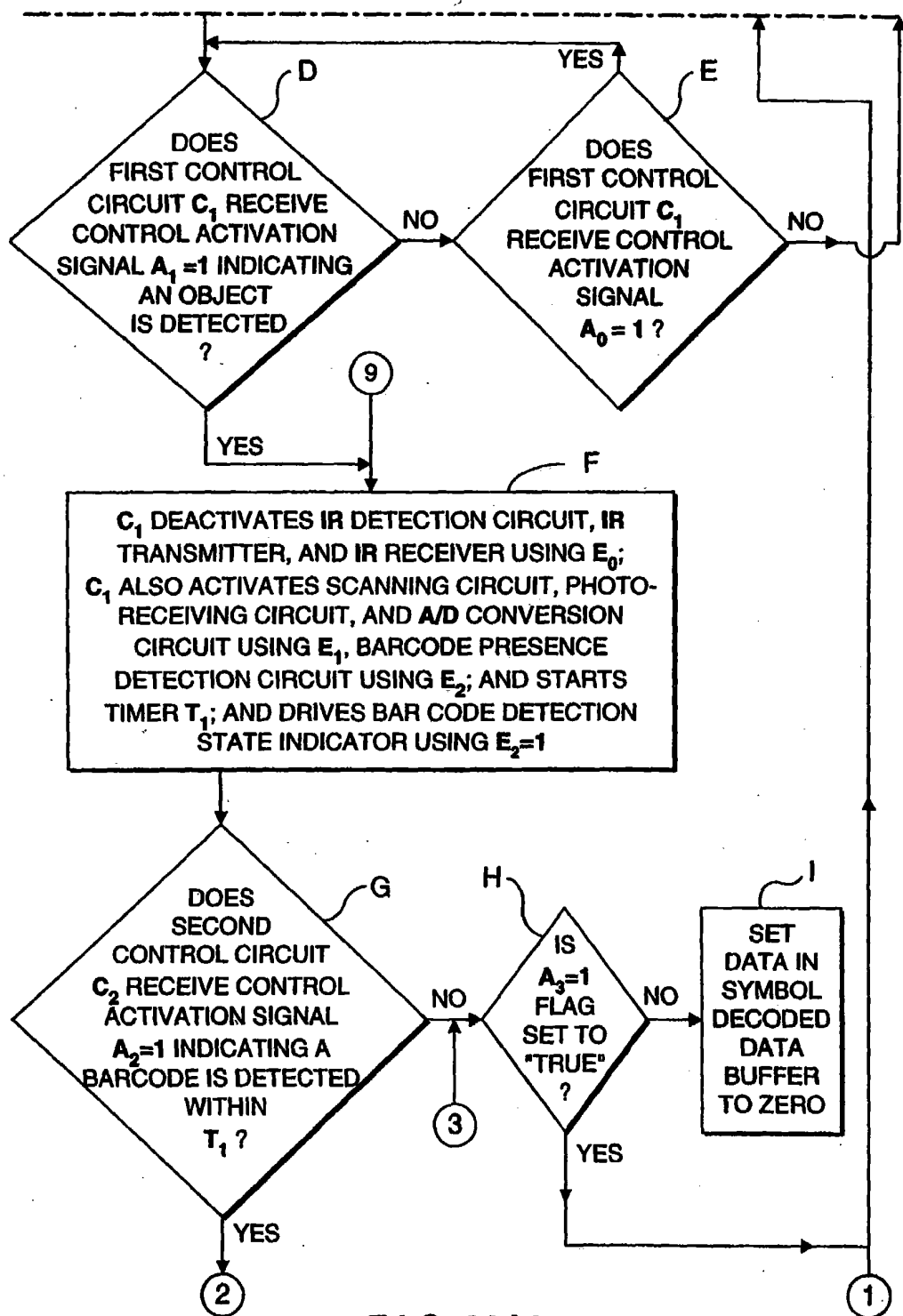


FIG. 32A2

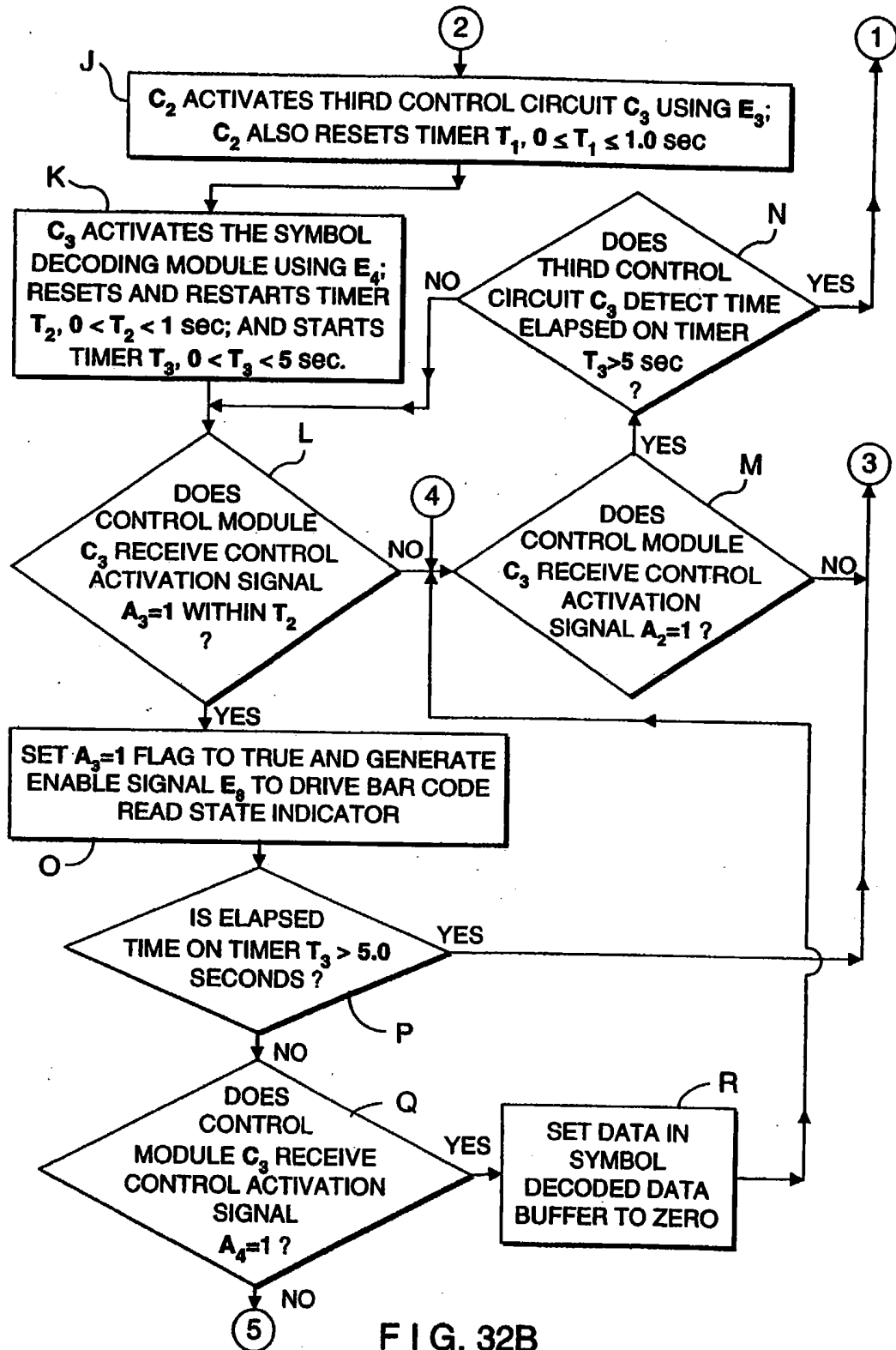


FIG. 32B

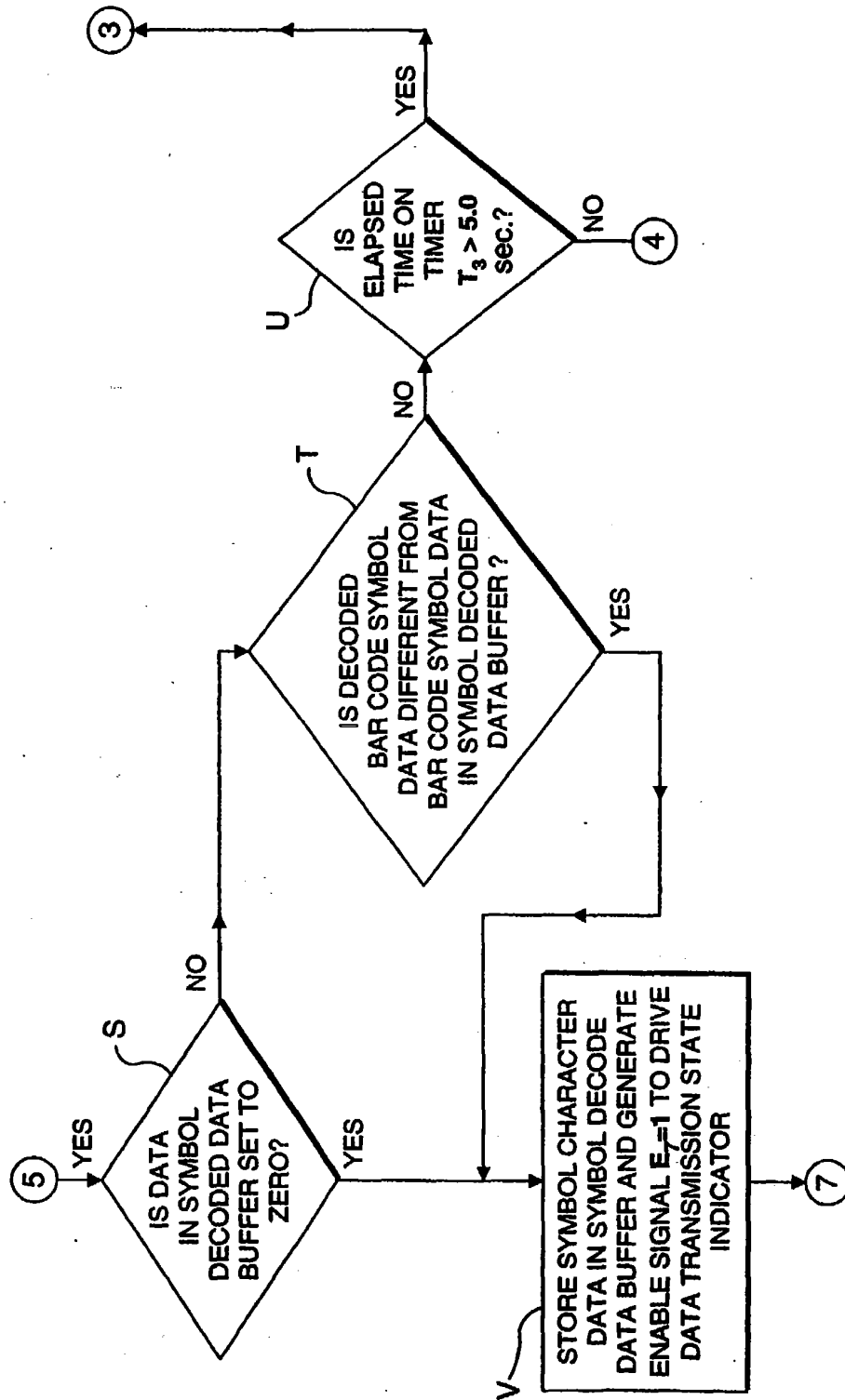


FIG. 32C

105/161

7

THIRD CONTROL CIRCUIT C_3 CONTINUES ACTIVATION OF LASER DIODE, SCANNING MOTOR, PHOTORECEIVING CIRCUIT, A/D CONVERSION CIRCUIT; DEACTIVATES SYMBOL DECODING MODULE; AND COMMENCES ACTIVATION DATA PACKET SYNTHESIS MODULE

W

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE SETS PACKET NUMBER TO 1 AND INCREMENTS DATA PACKET GROUP NUMBER MODULE COUNTER

X

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE CONSTRUCTS DATA PACKET CONSISTING OF SYMBOL CHARACTER DATA, TRANSMITTER NUMBER, DATA PACKET GROUP NUMBER, CHECK CHARACTER AND FRAMING CHARACTERS

Y

C_3 ACTIVATES DATA PACKET TRANSMISSION CIRCUIT

Z

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE OUTPUTS PACKET TO DATA PACKET TRANSMISSION CIRCUIT

AA

C_3 DETERMINES IS PACKET NUMBER < 3 ?

NO

8

YES

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE INCREMENTS DATA PACKET GROUP NUMBER

CC

C_3 ALLOWS T_5 TO EXPIRE IN ORDER TO DELAY TRANSMISSION BASED ON LAST TWO DIGITS OF TRANSMITTER NUMBER

DD

FIG. 32D

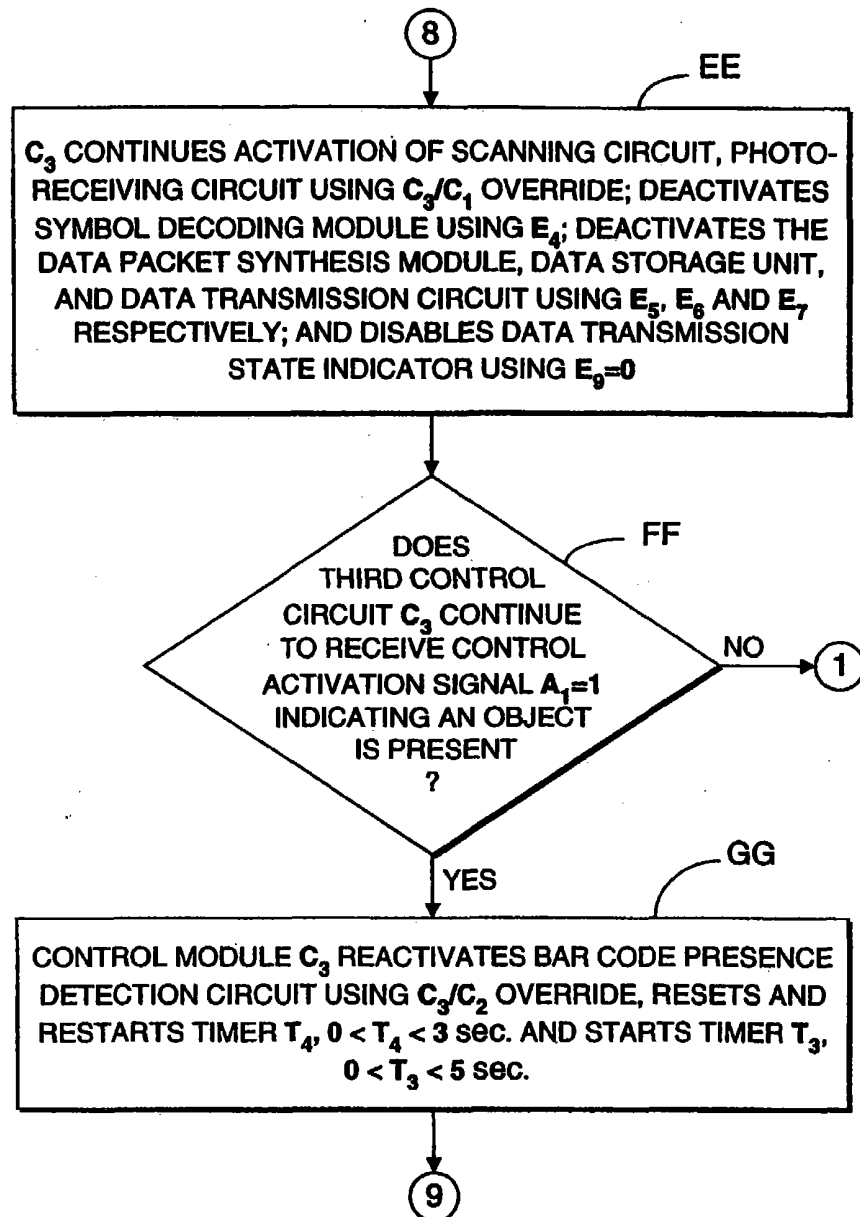


FIG. 32E

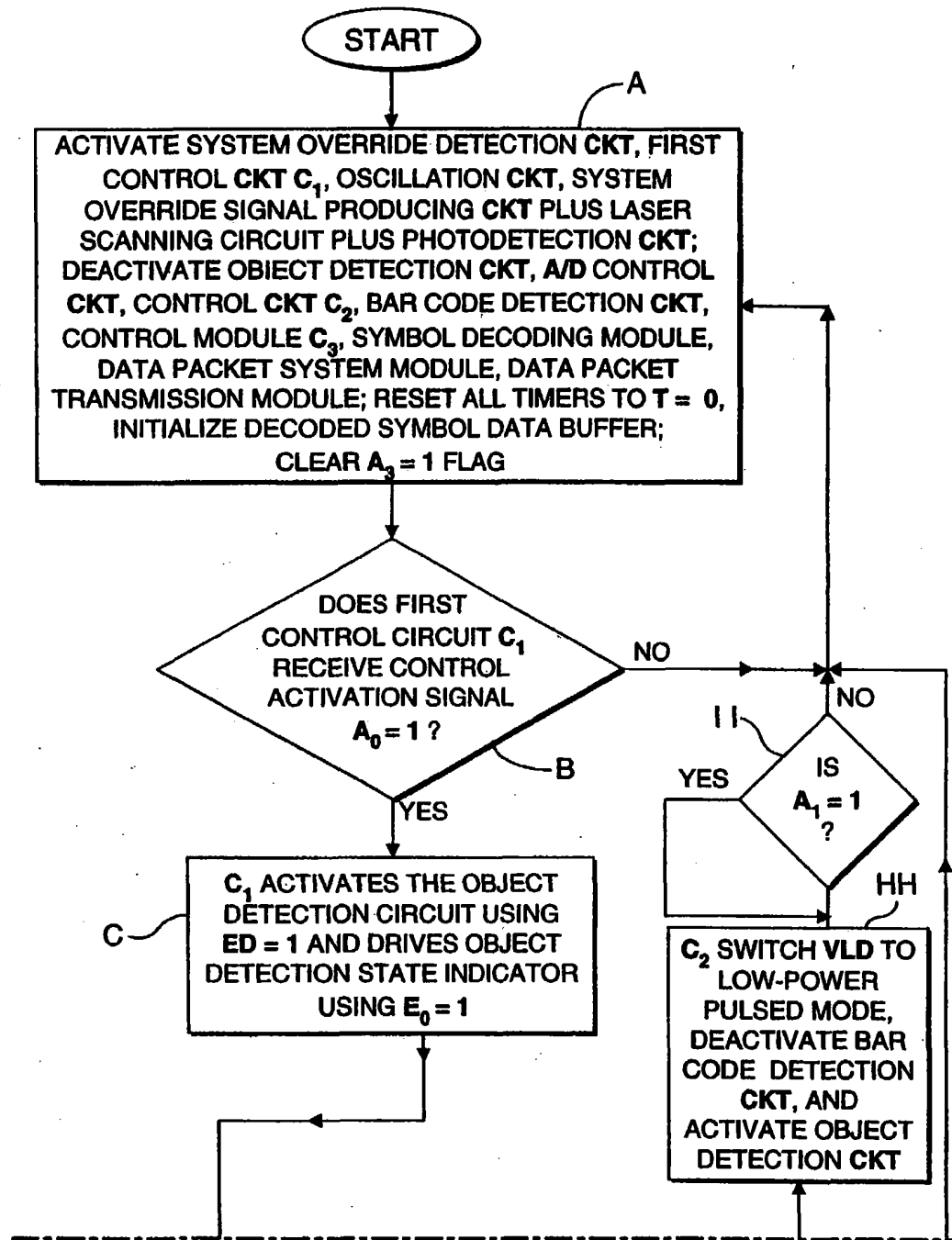
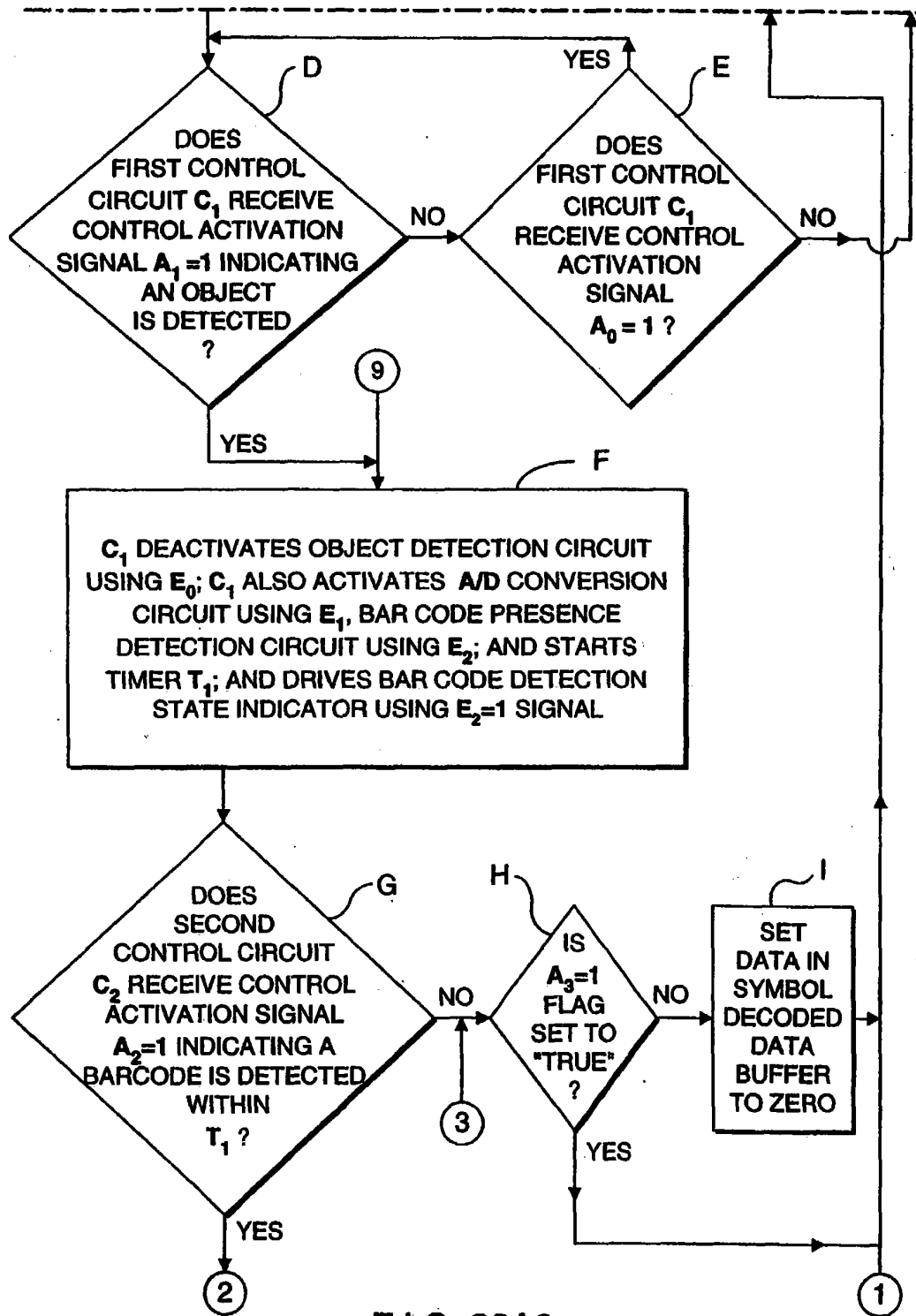


FIG. 33A1



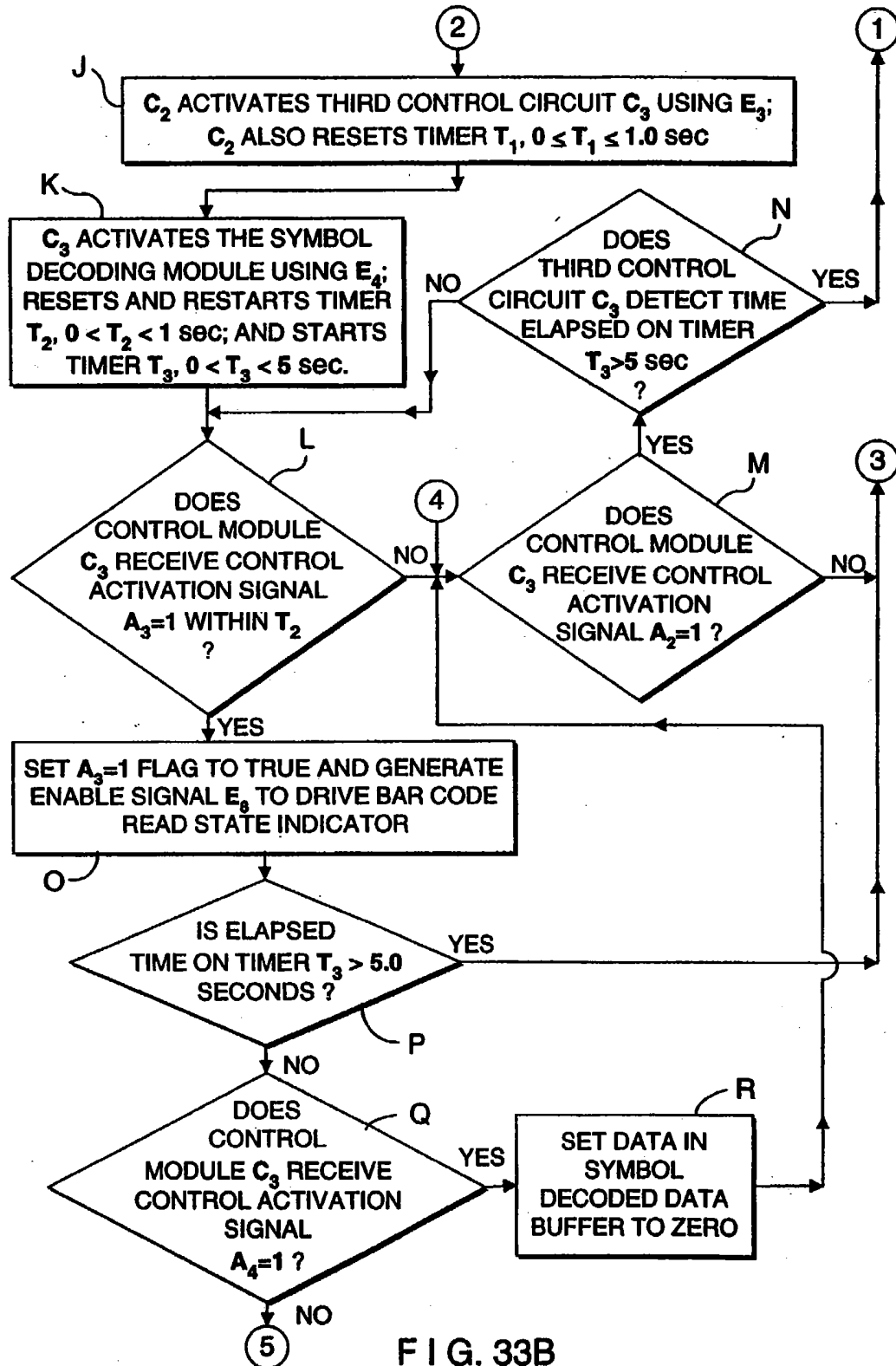
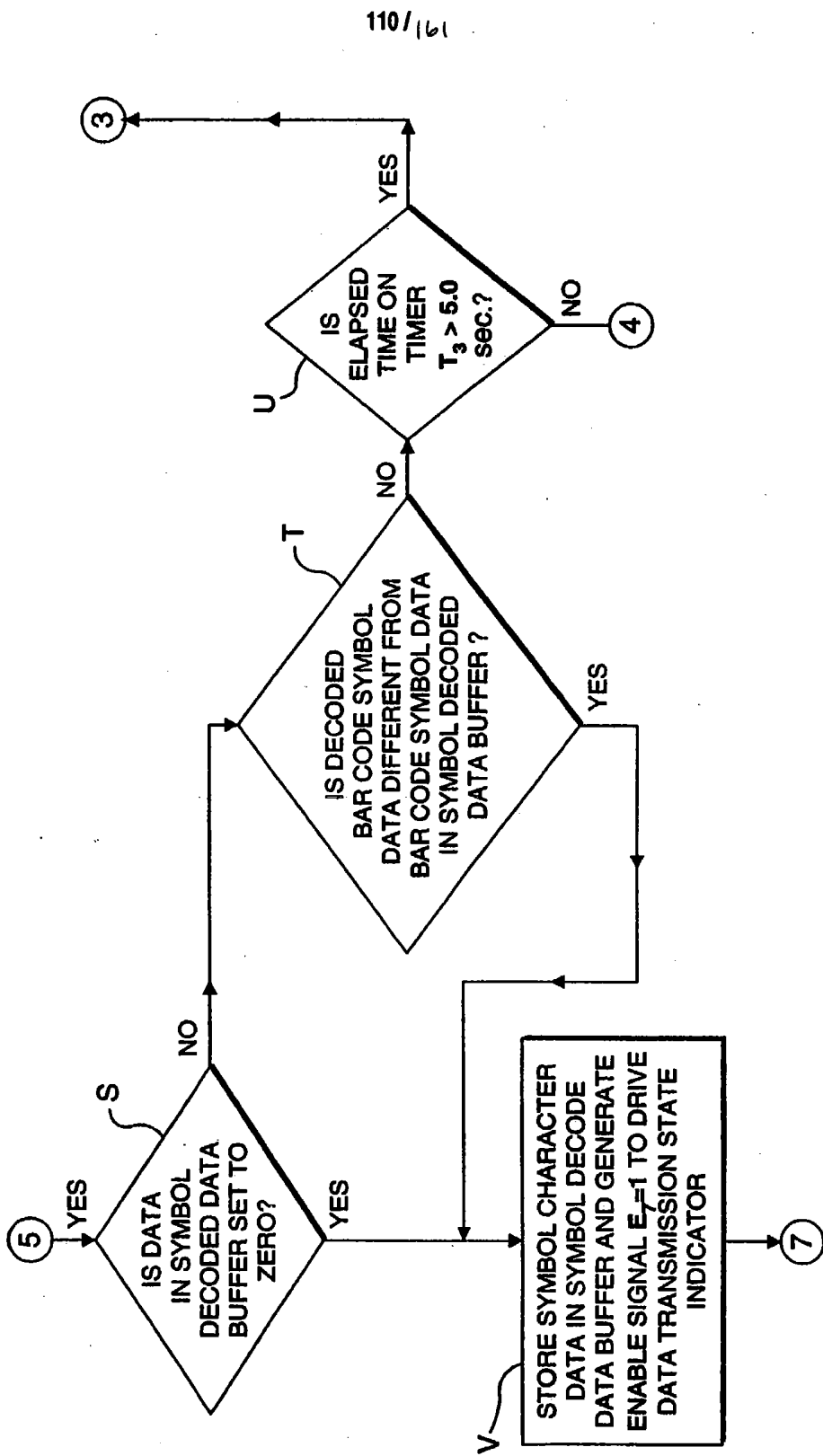


FIG. 33B



111/161

7

THIRD CONTROL CIRCUIT C_3 CONTINUES ACTIVATION OF LASER DIODE, SCANNING MOTOR, PHOTORECEIVING CIRCUIT, A/D CONVERSION CIRCUIT; DEACTIVATES SYMBOL DECODING MODULE; AND COMMENCES ACTIVATION DATA PACKET SYNTHESIS MODULE

W

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE SETS PACKET NUMBER TO 1 AND INCREMENTS DATA PACKET GROUP NUMBER MODULE COUNTER

X

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE CONSTRUCTS DATA PACKET CONSISTING OF SYMBOL CHARACTER DATA, TRANSMITTER NUMBER, DATA PACKET GROUP NUMBER, CHECK CHARACTER AND FRAMING CHARACTERS

Y

C_3 ACTIVATES DATA PACKET TRANSMISSION CIRCUIT

Z

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE OUTPUTS PACKET TO DATA PACKET TRANSMISSION CIRCUIT

AA

C_3 DETERMINES IS PACKET NUMBER < 3 ?

BB

NO → 8

YES

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE INCREMENTS DATA PACKET GROUP NUMBER

CC

C_3 ALLOWS T_5 TO EXPIRE IN ORDER TO DELAY TRANSMISSION BASED ON LAST TWO DIGITS OF TRANSMITTER NUMBER

DD

FIG. 33D

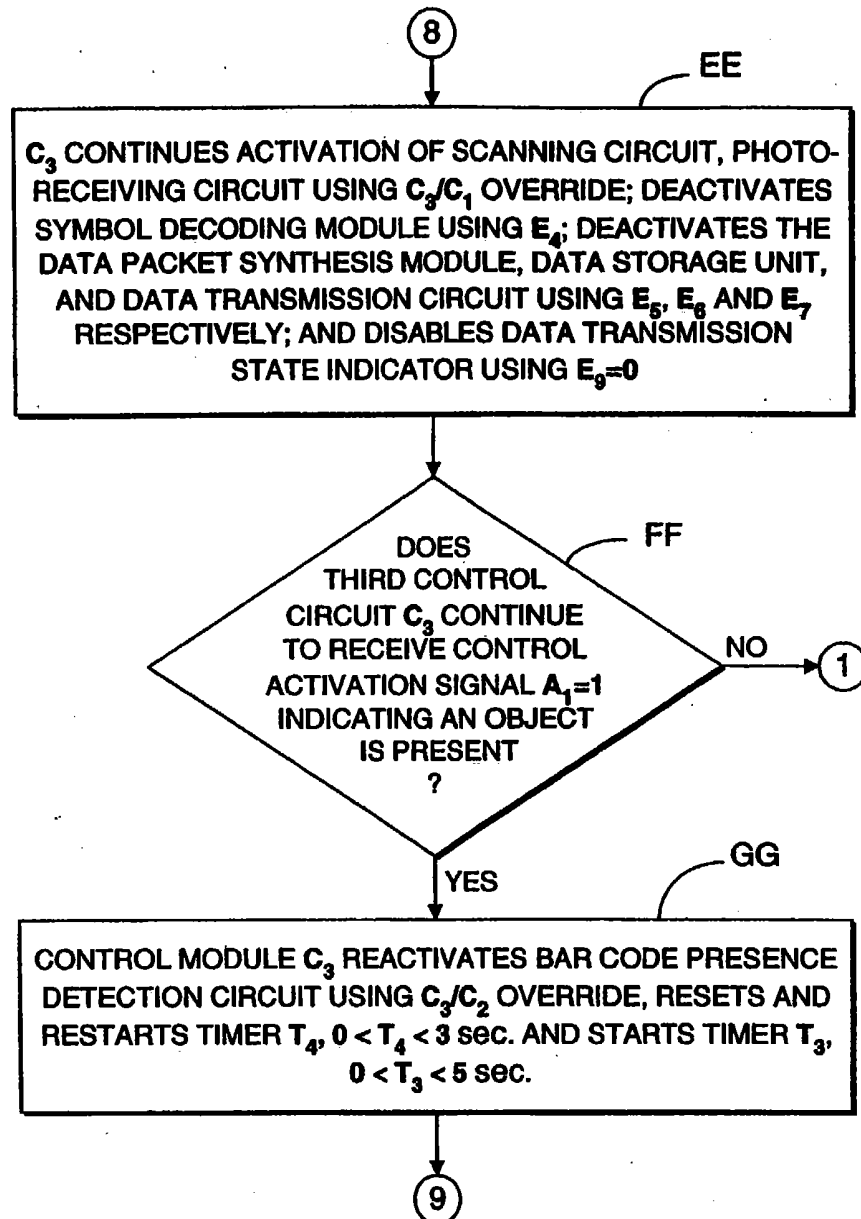
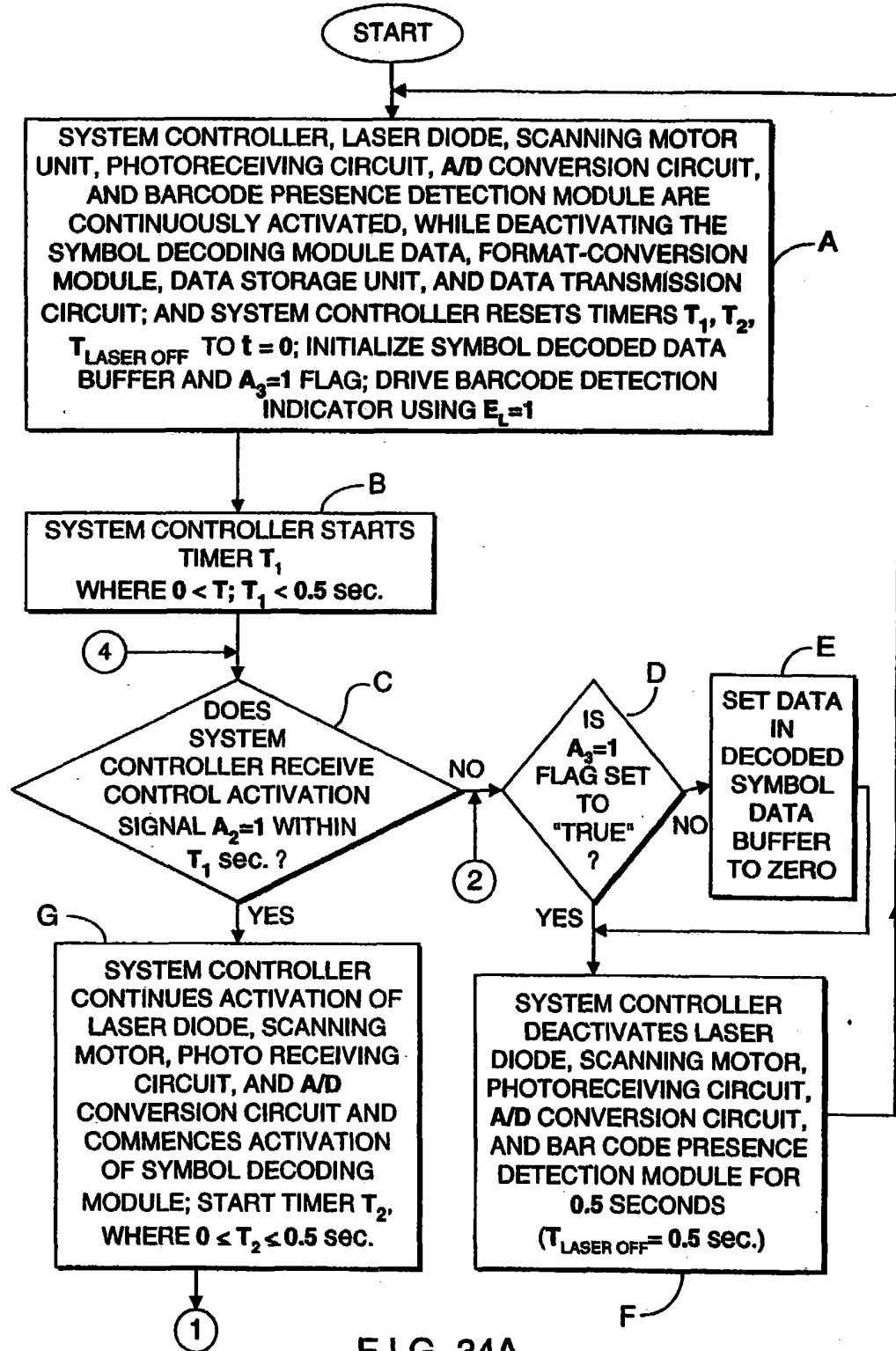


FIG. 33E



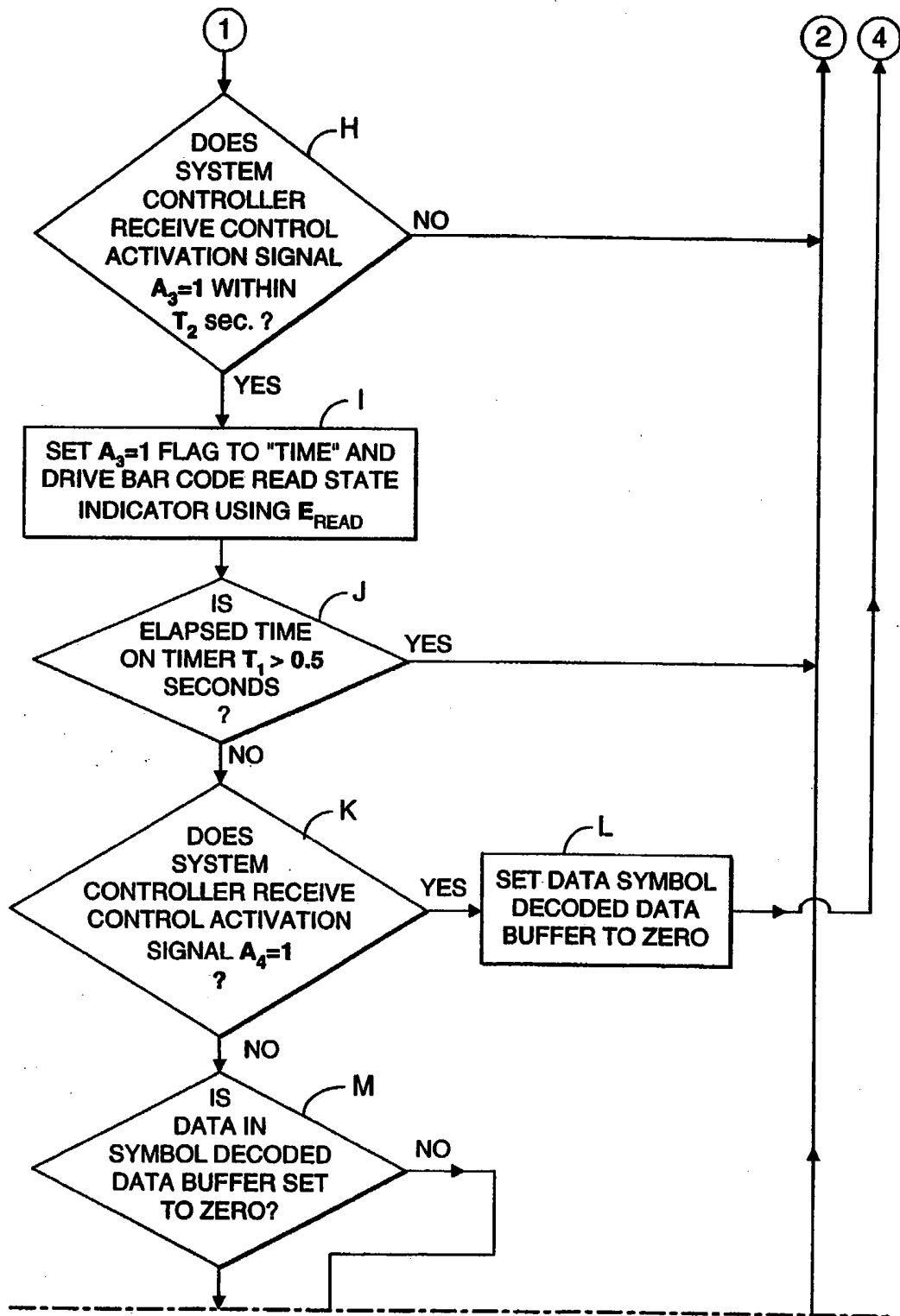


FIG. 34B1

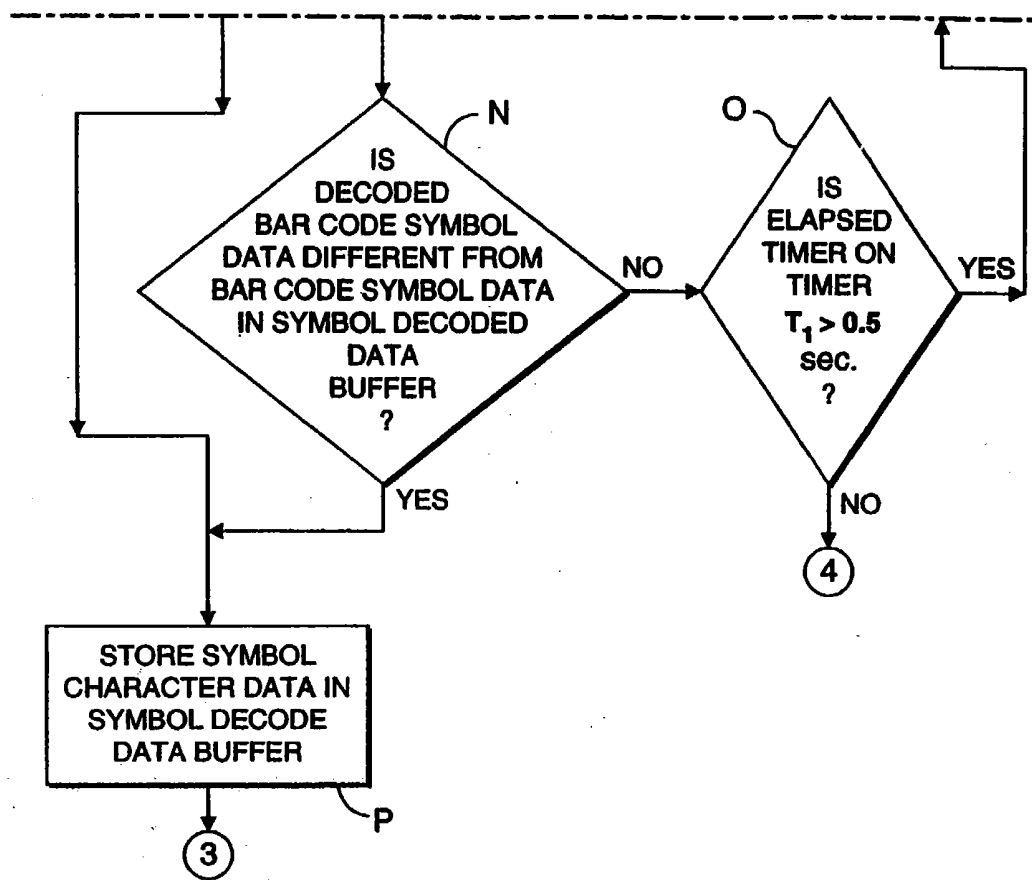


FIG. 34B2

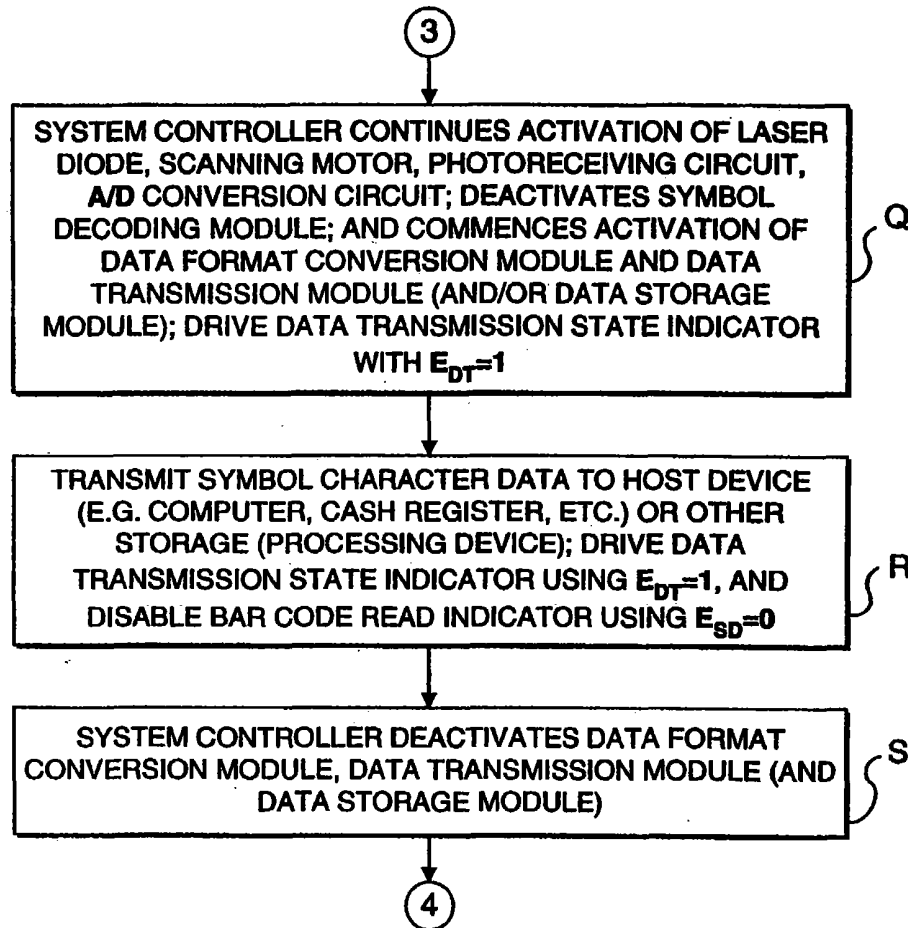
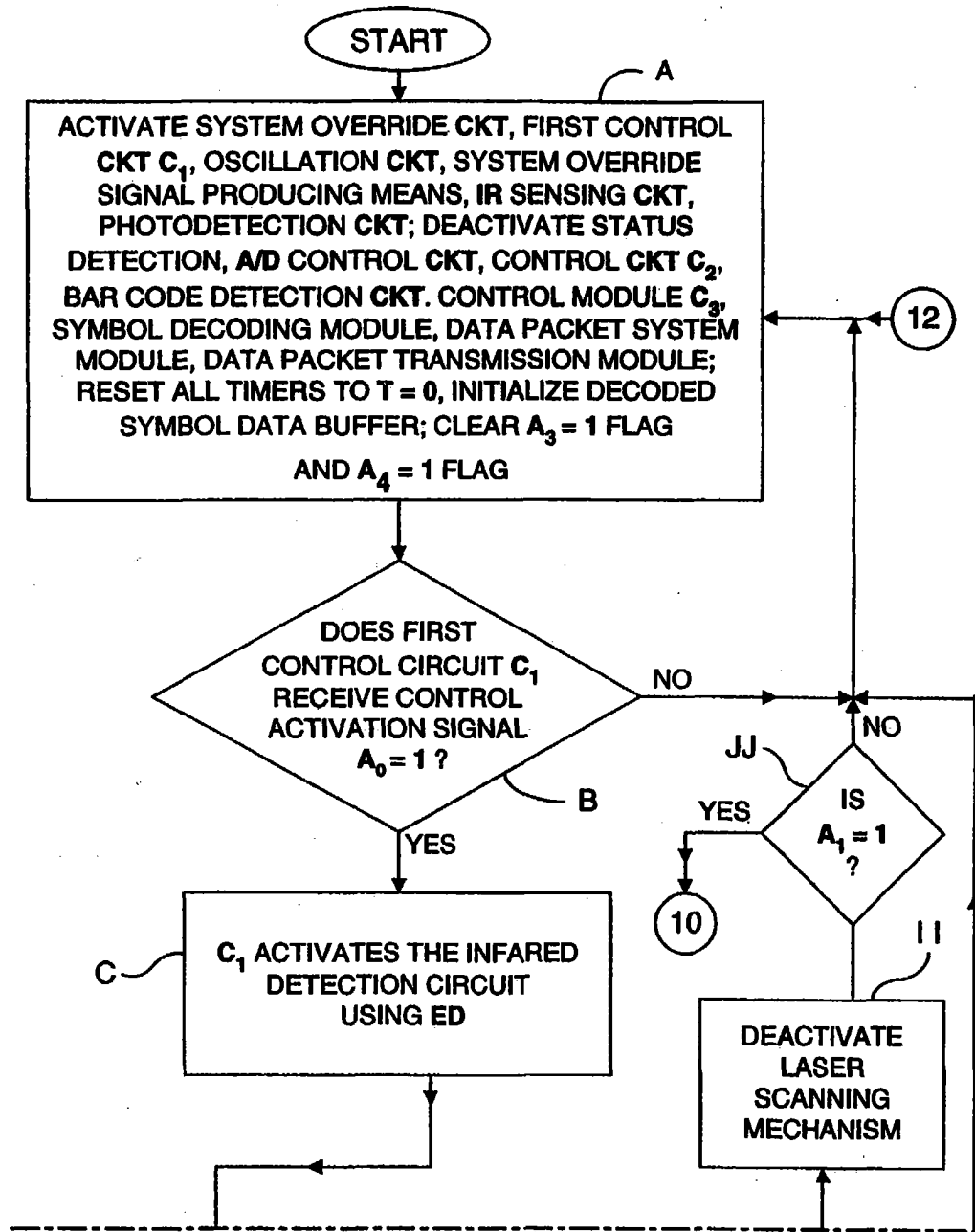


FIG. 34C



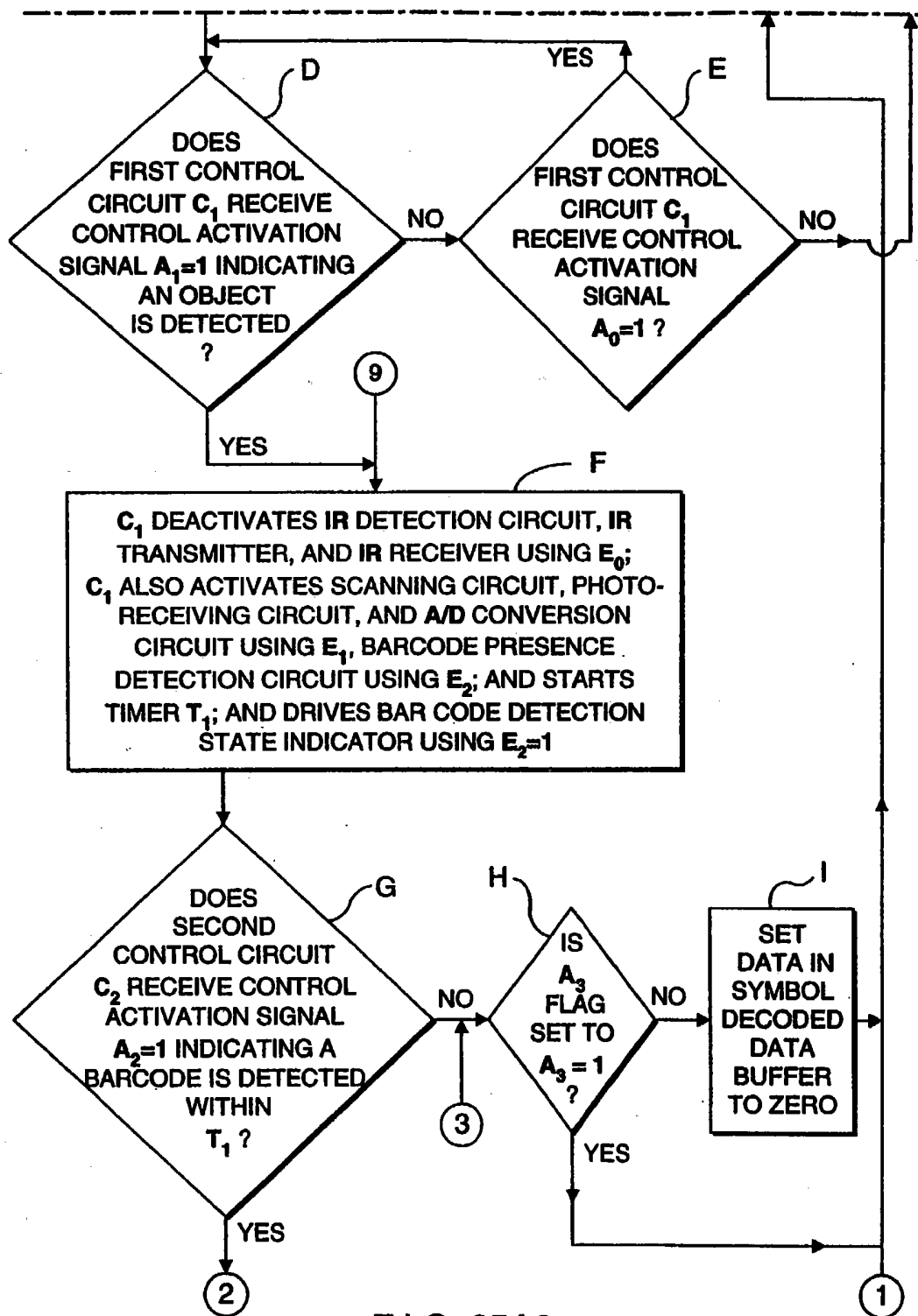


FIG. 35A2

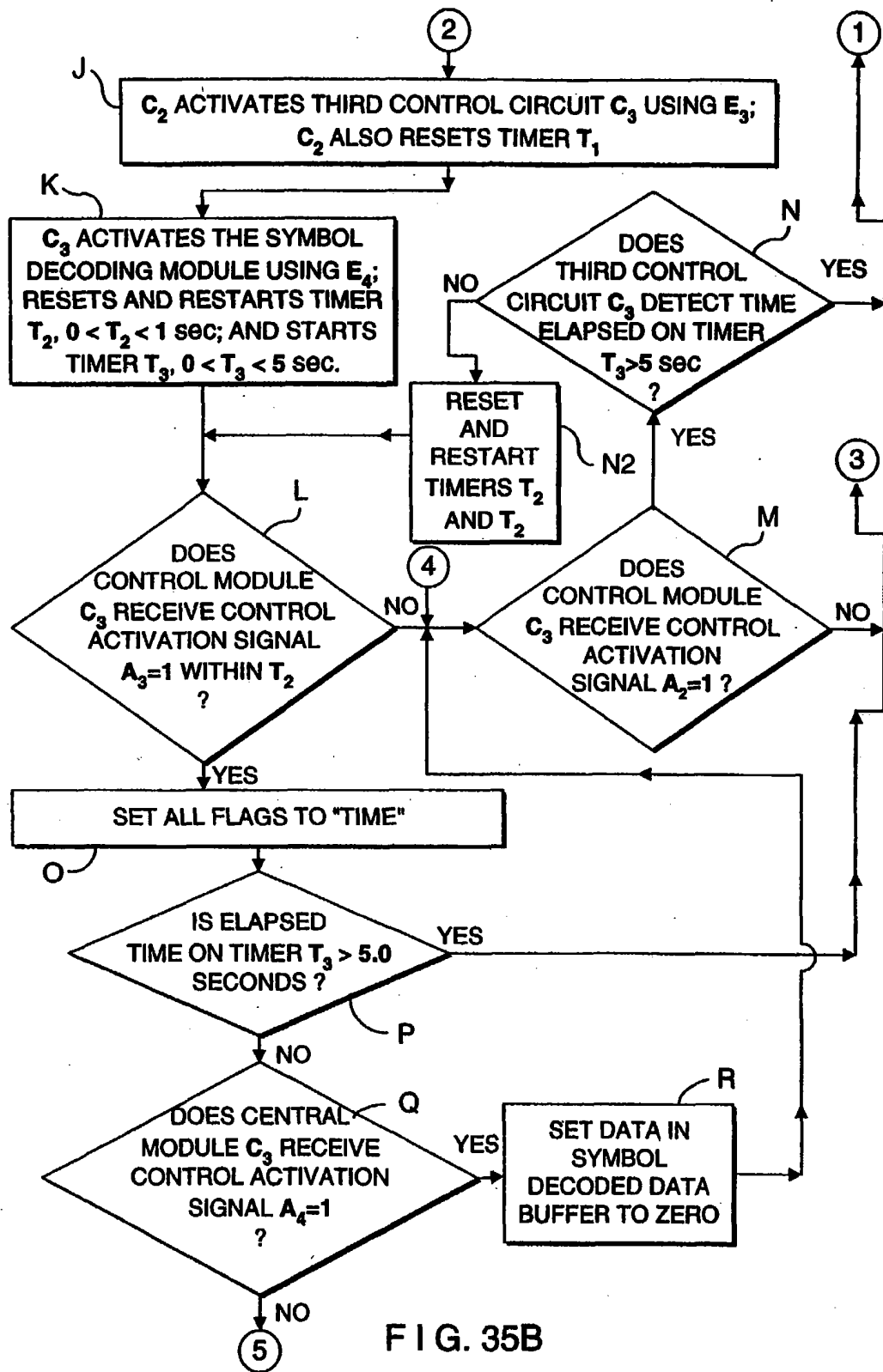


FIG. 35B

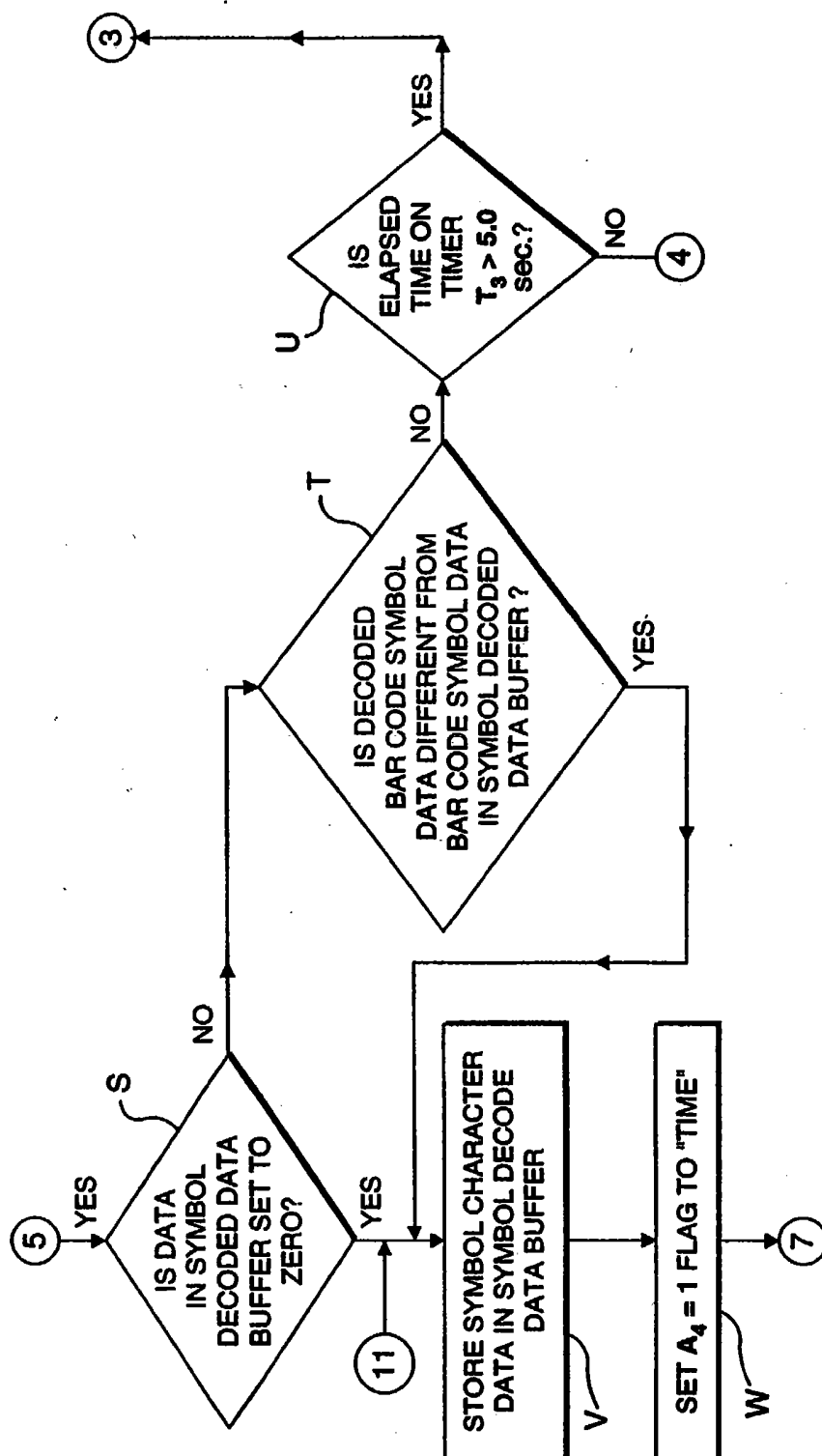


FIG. 35C

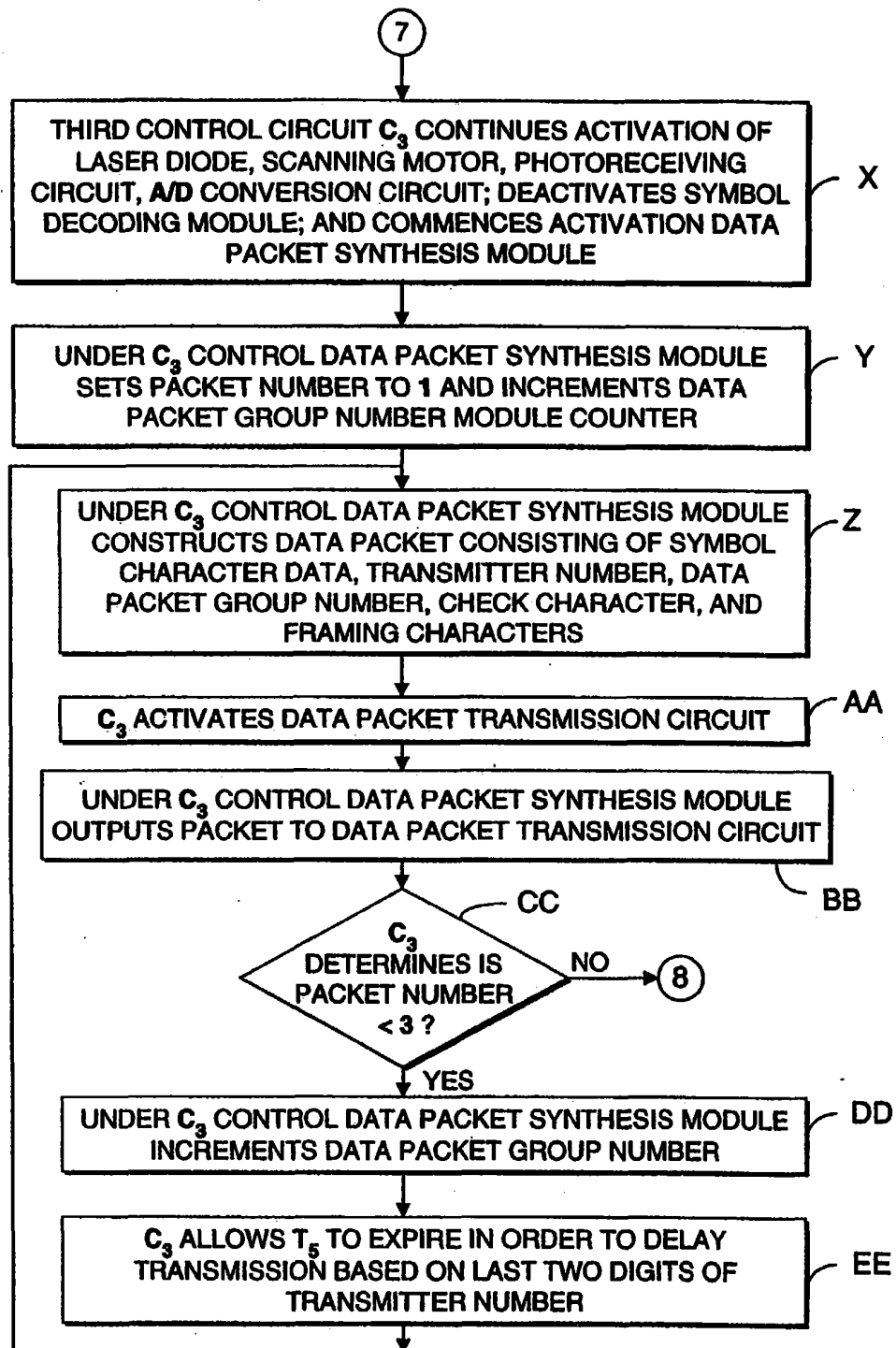


FIG. 35D

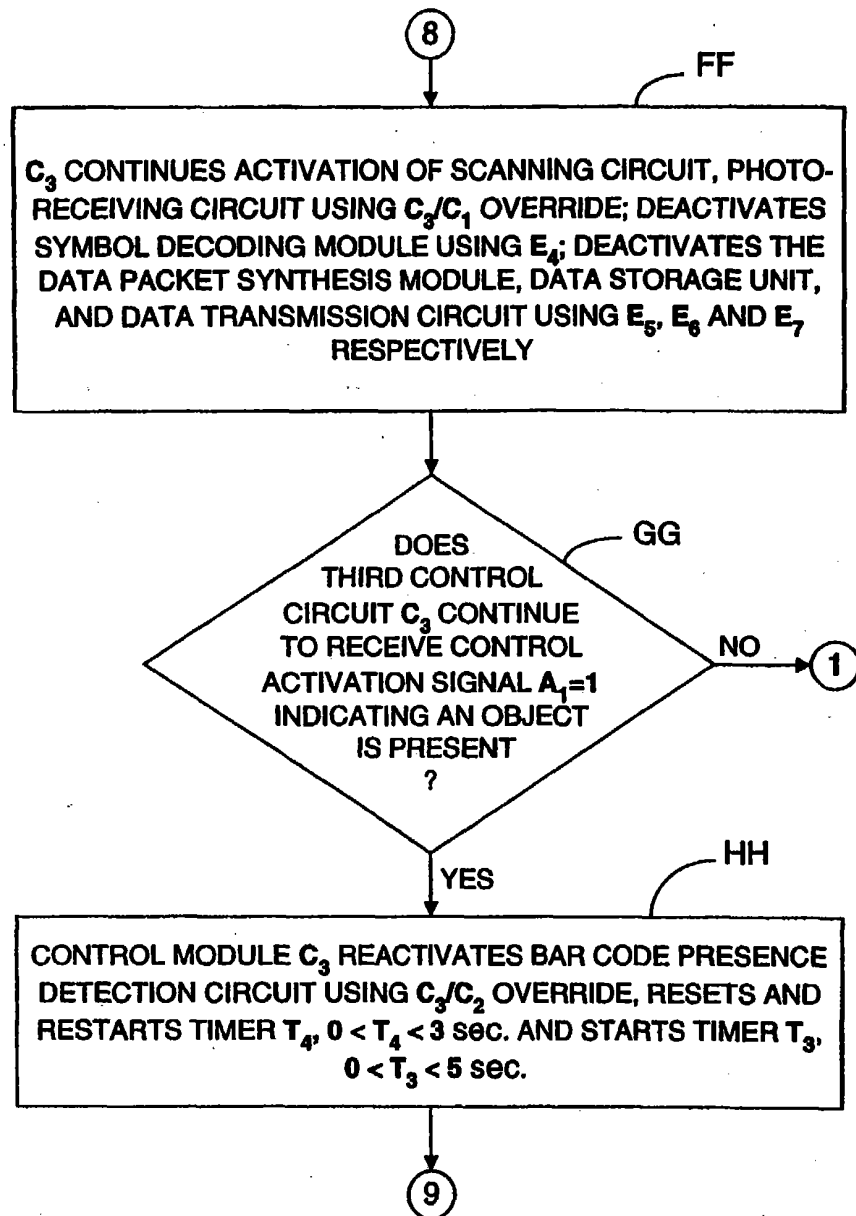


FIG. 35E

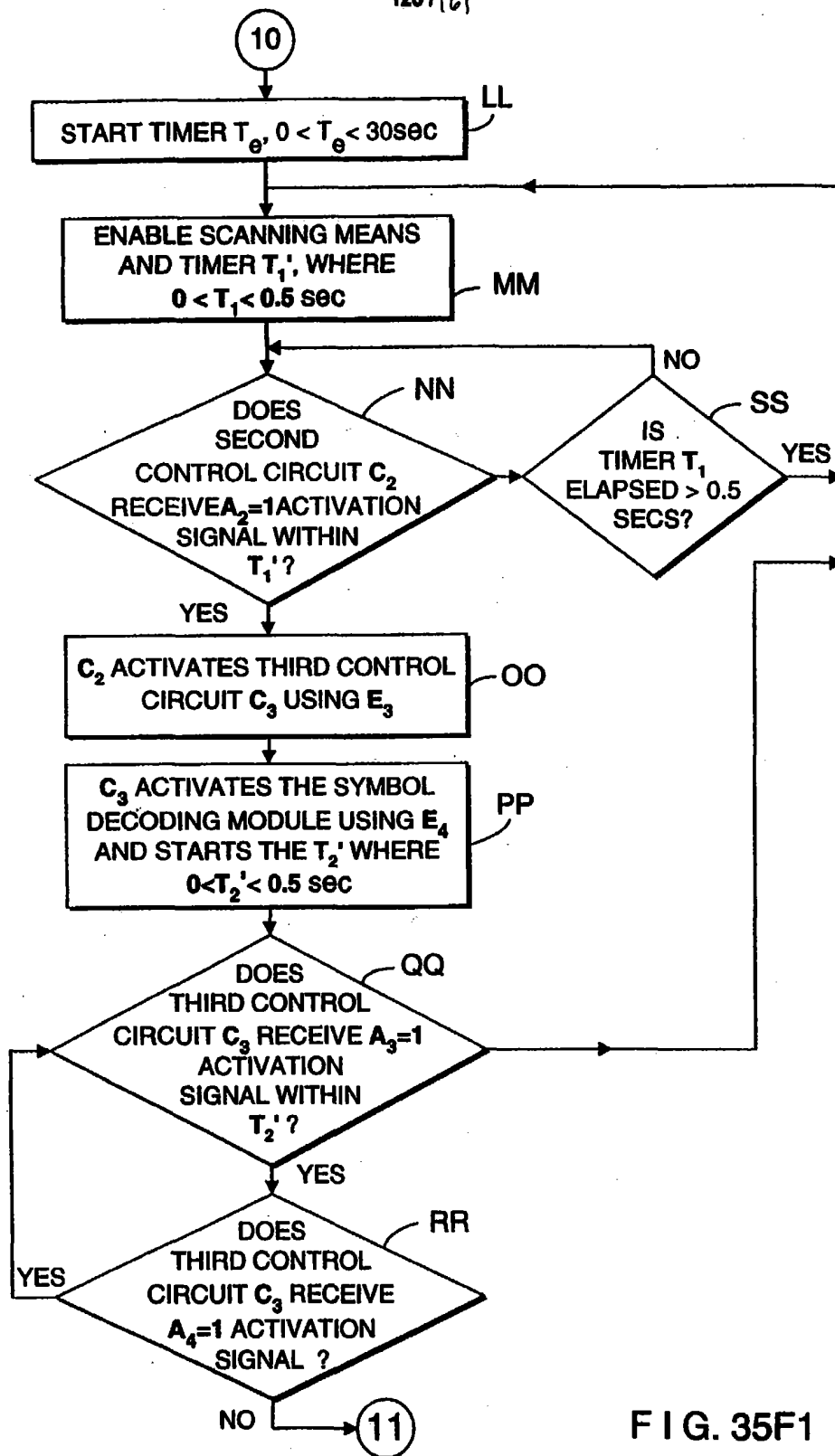
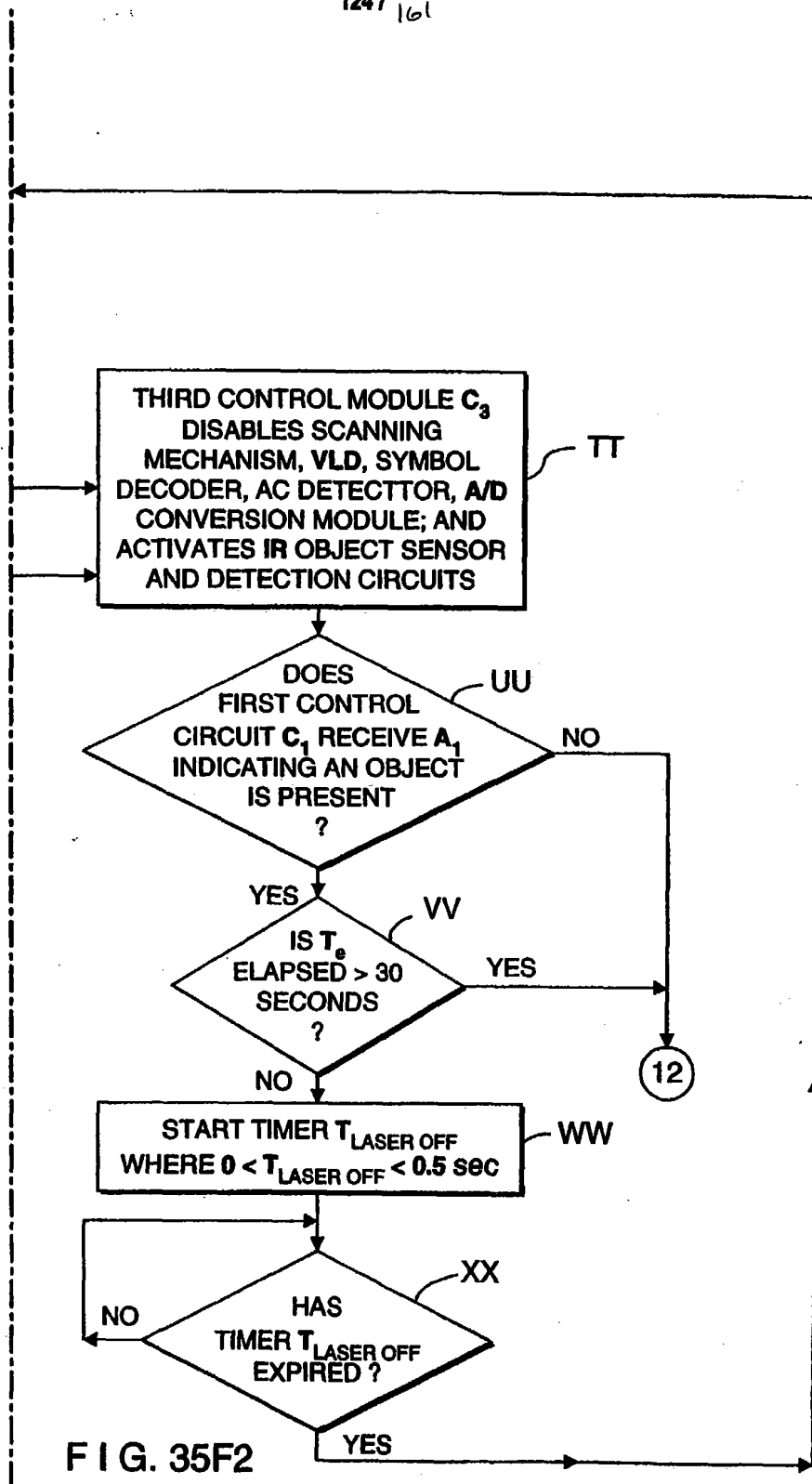
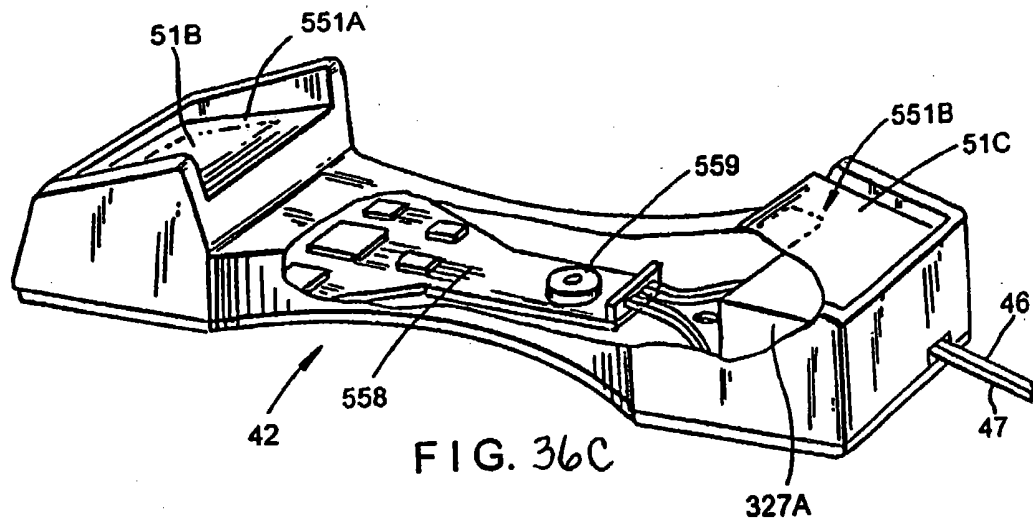
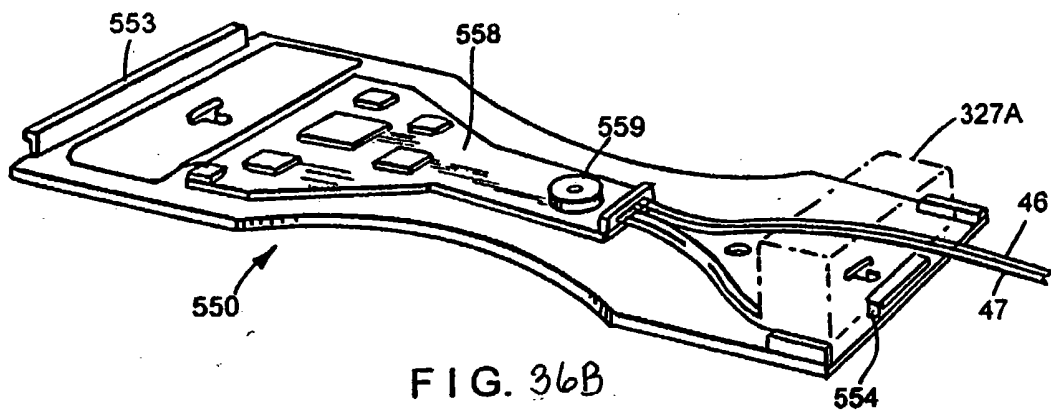
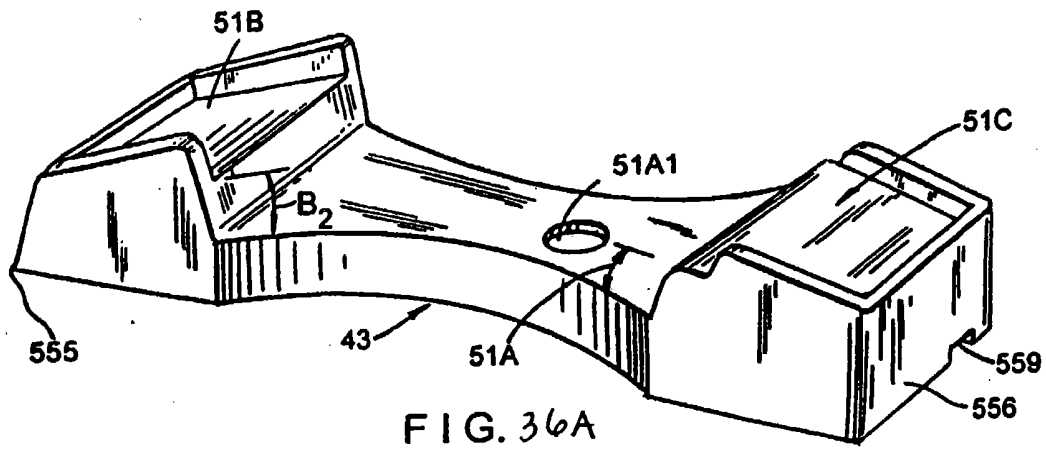


FIG. 35F1



125/161



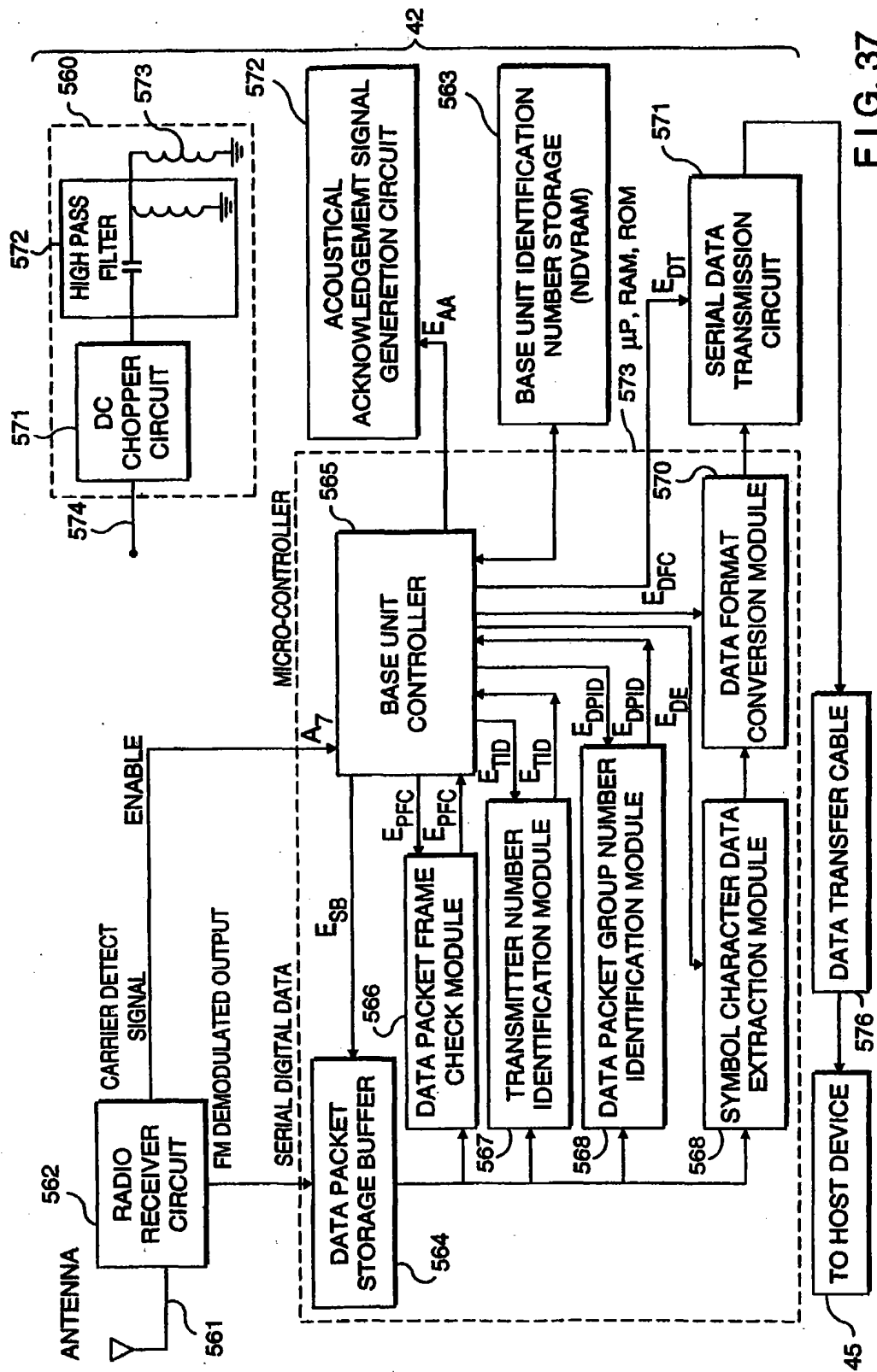
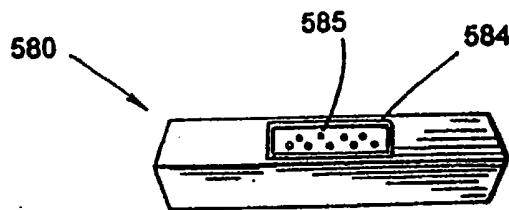
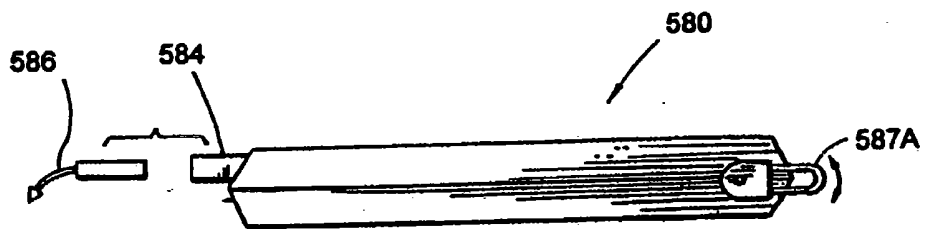
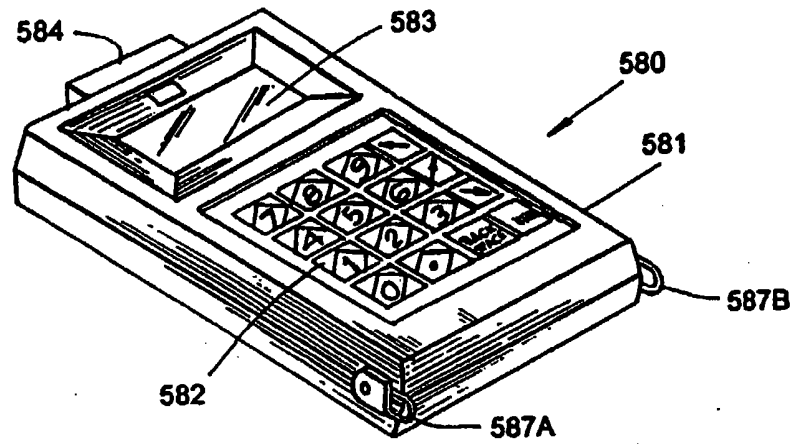


FIG. 37

127/101



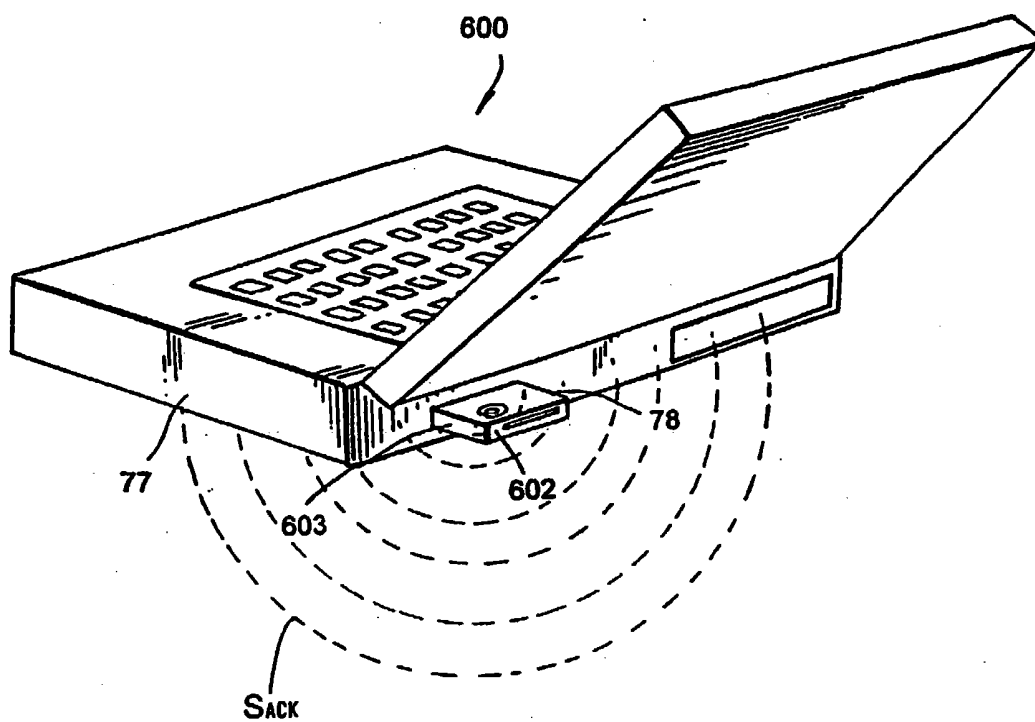
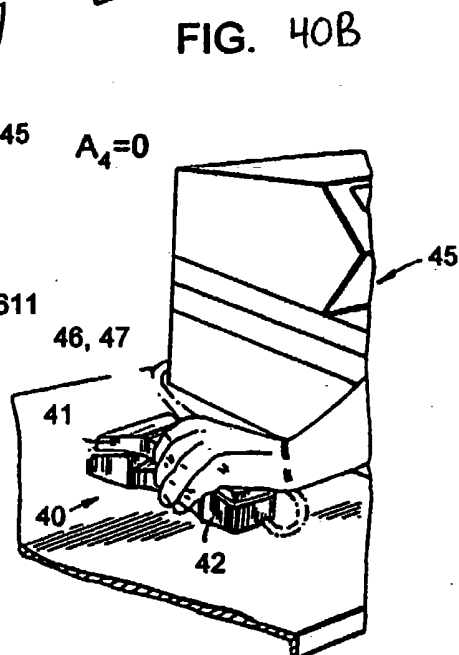
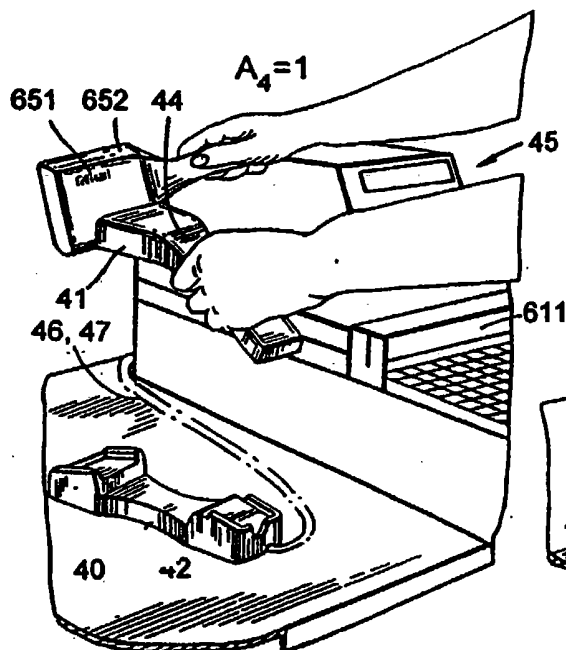
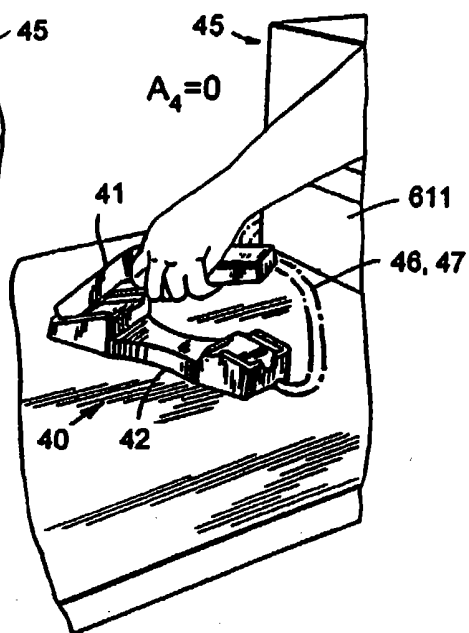


FIG. 39



130/161

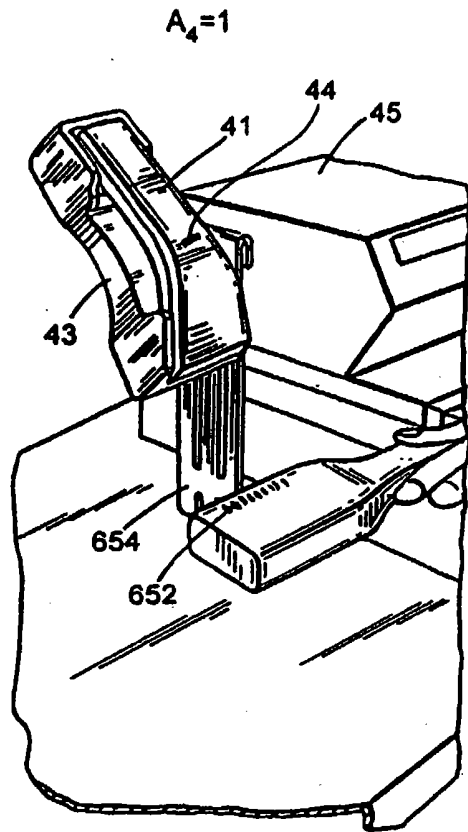


FIG. 41A

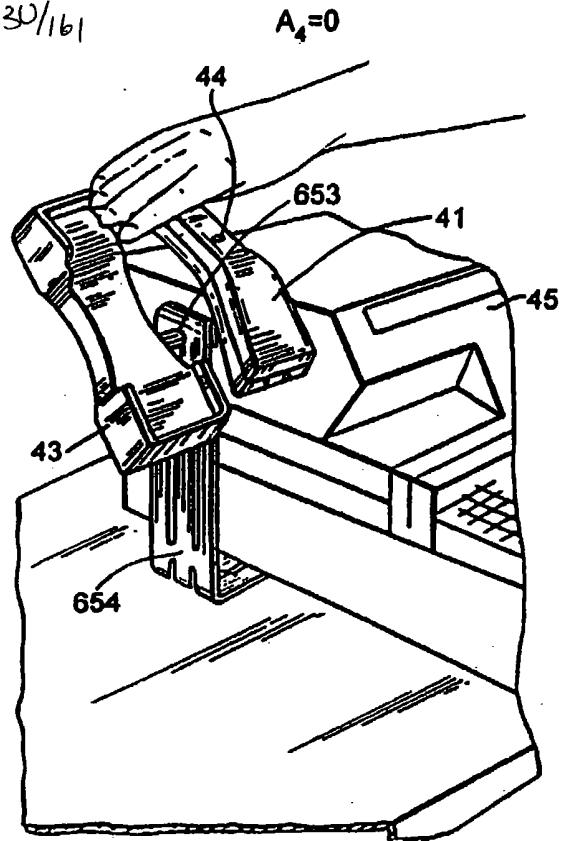


FIG. 41B

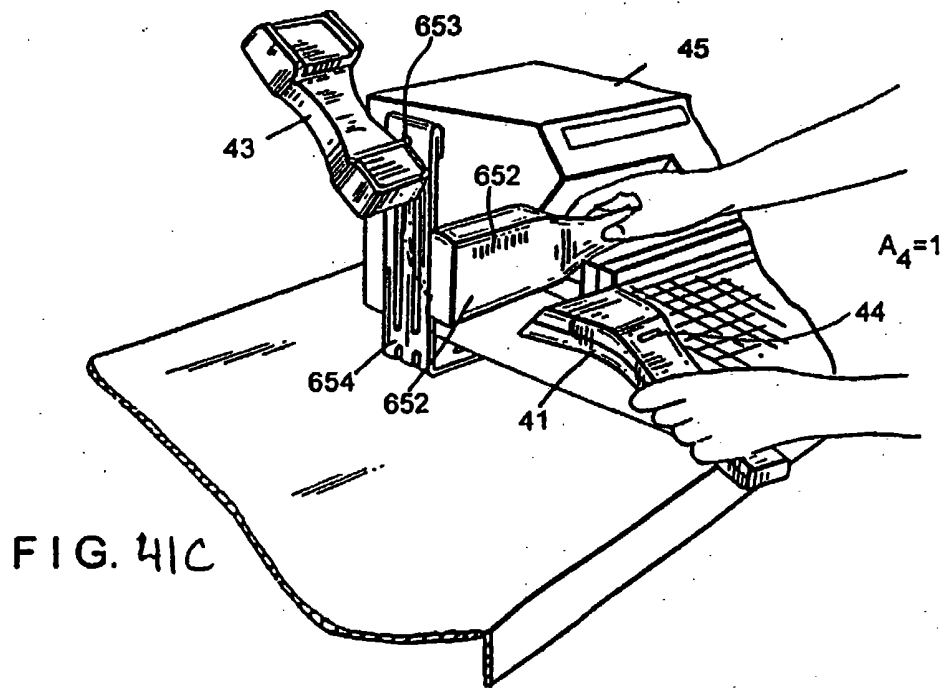


FIG. 41C

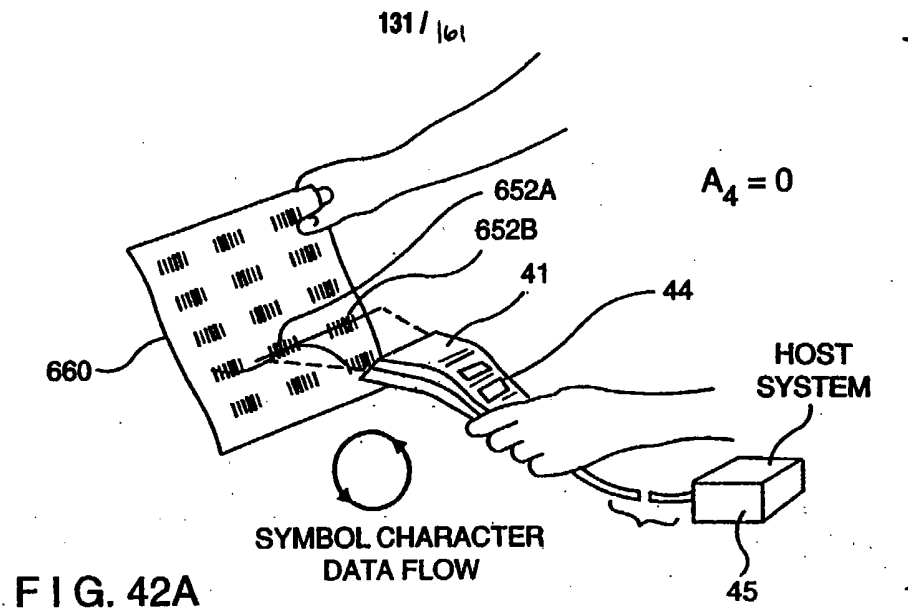


FIG. 42A

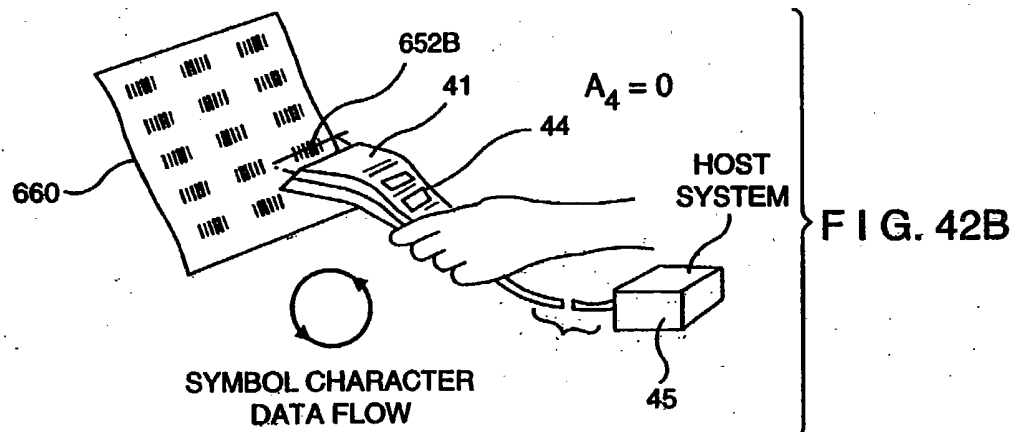


FIG. 42B

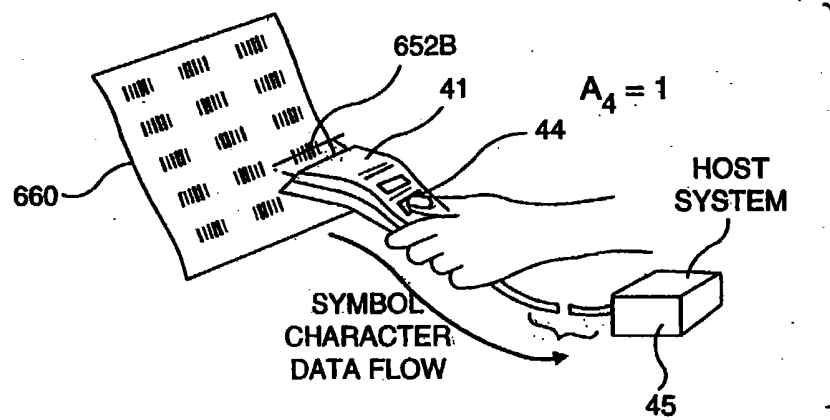
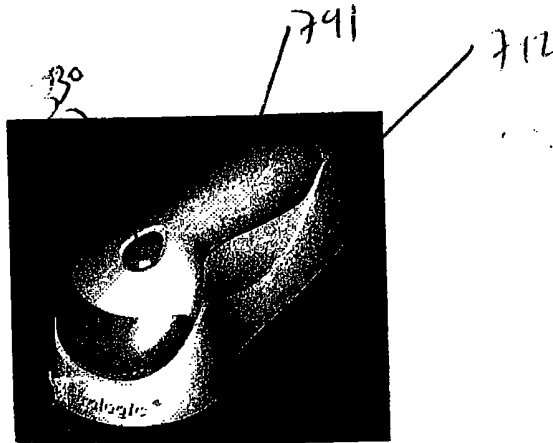


FIG. 42C

132/161

790
↓



Automatic Barcode
Reading
System with

2-WAY
RF Communication
Link

FIG. 43A

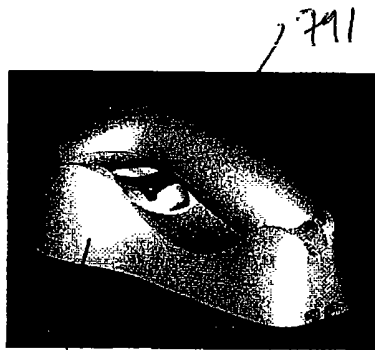


FIG. 43B

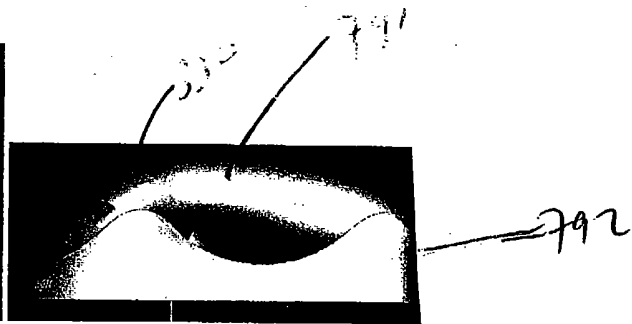


FIG. 43C



FIG. 43D

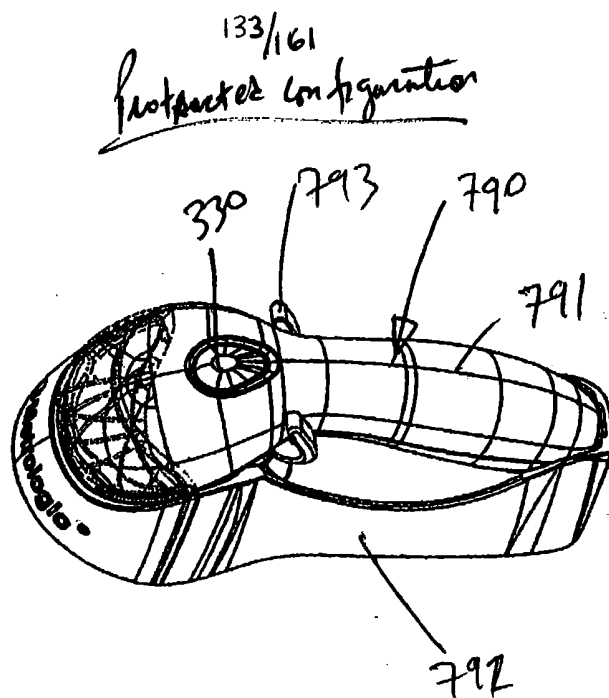


FIG. 43F

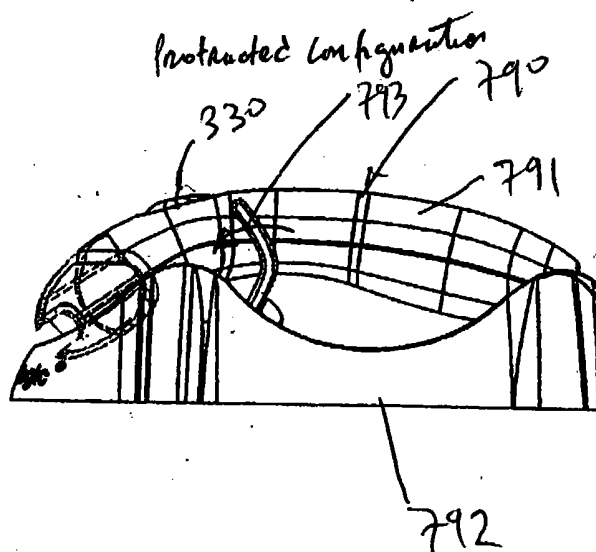


FIG. 43E

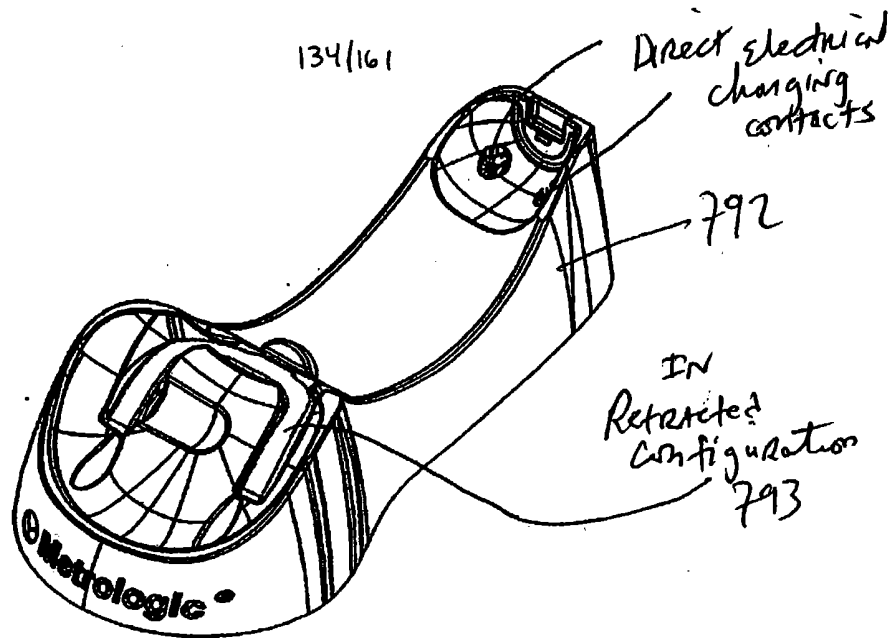


FIG. 43G

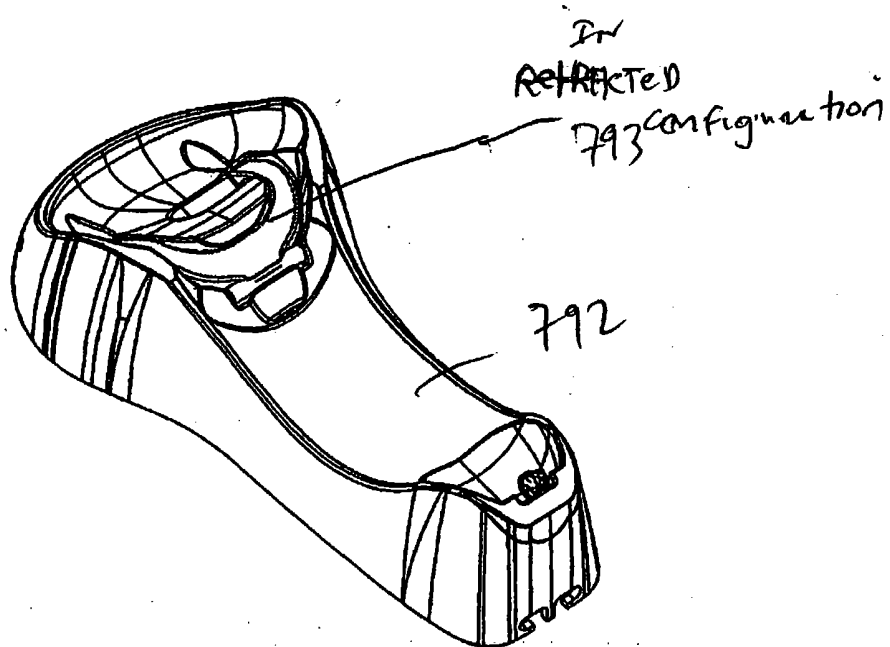


FIG. 43A

135/161

Reflected Configuration
793

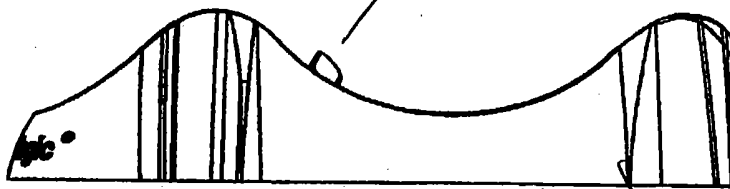


FIG. 43 I 792

Projected Configuration
793

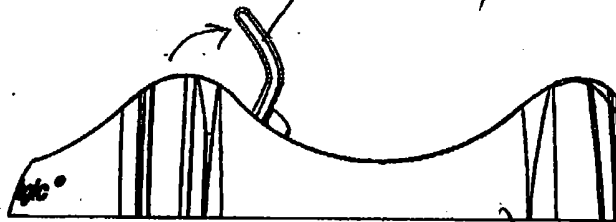


FIG. 43 J 792

136/161

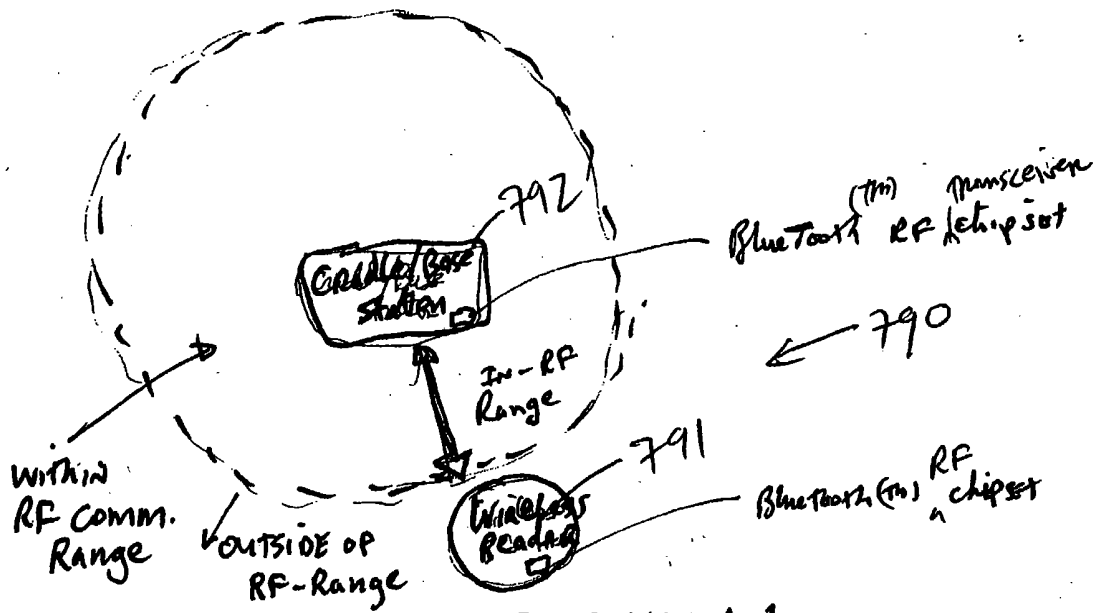


FIG. 44A1

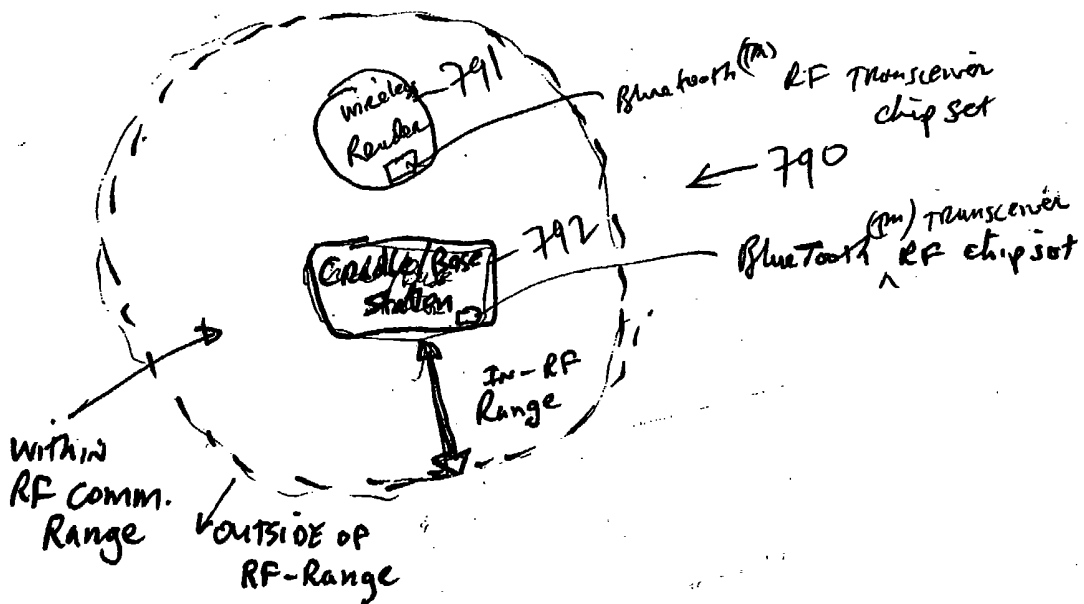
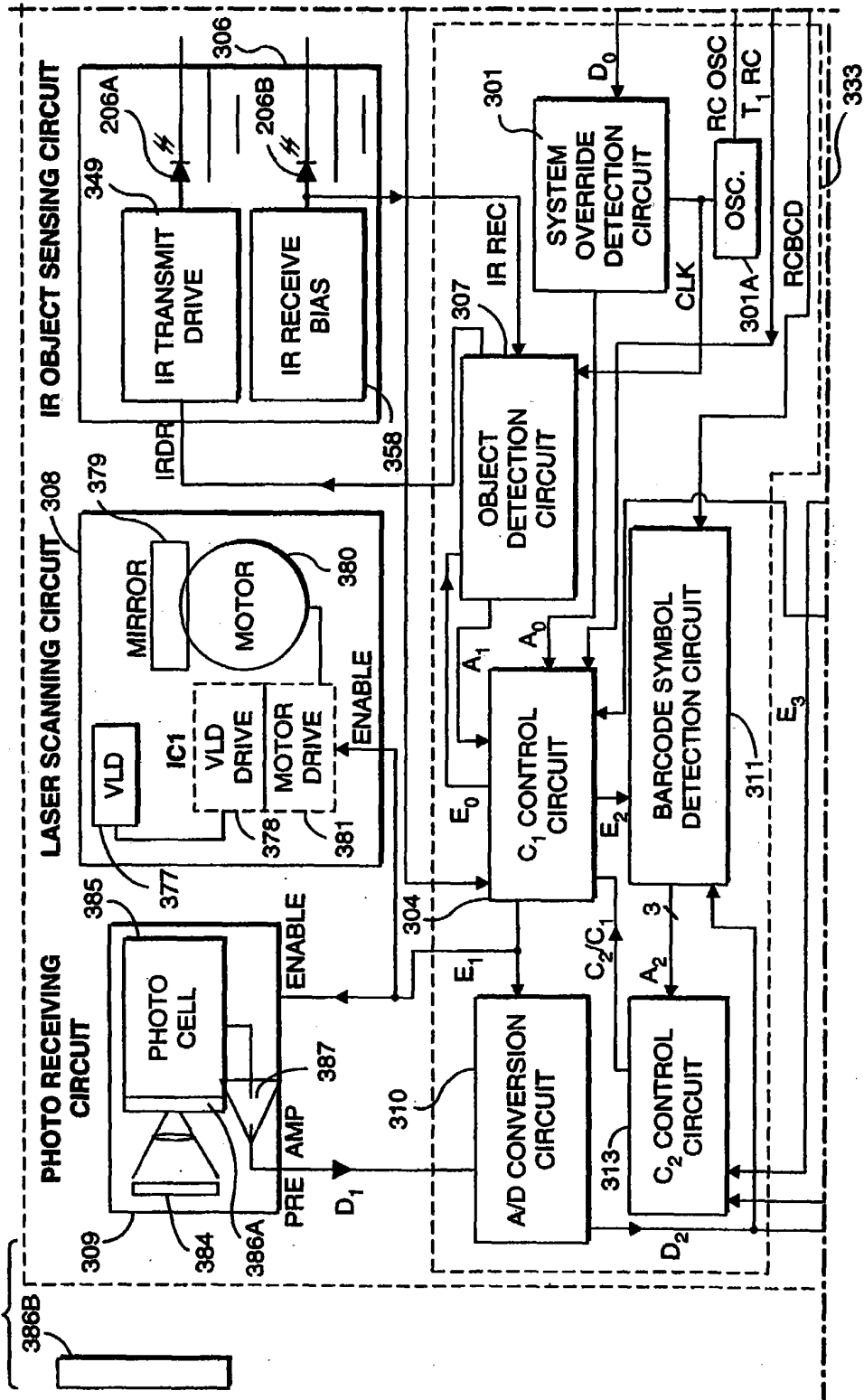
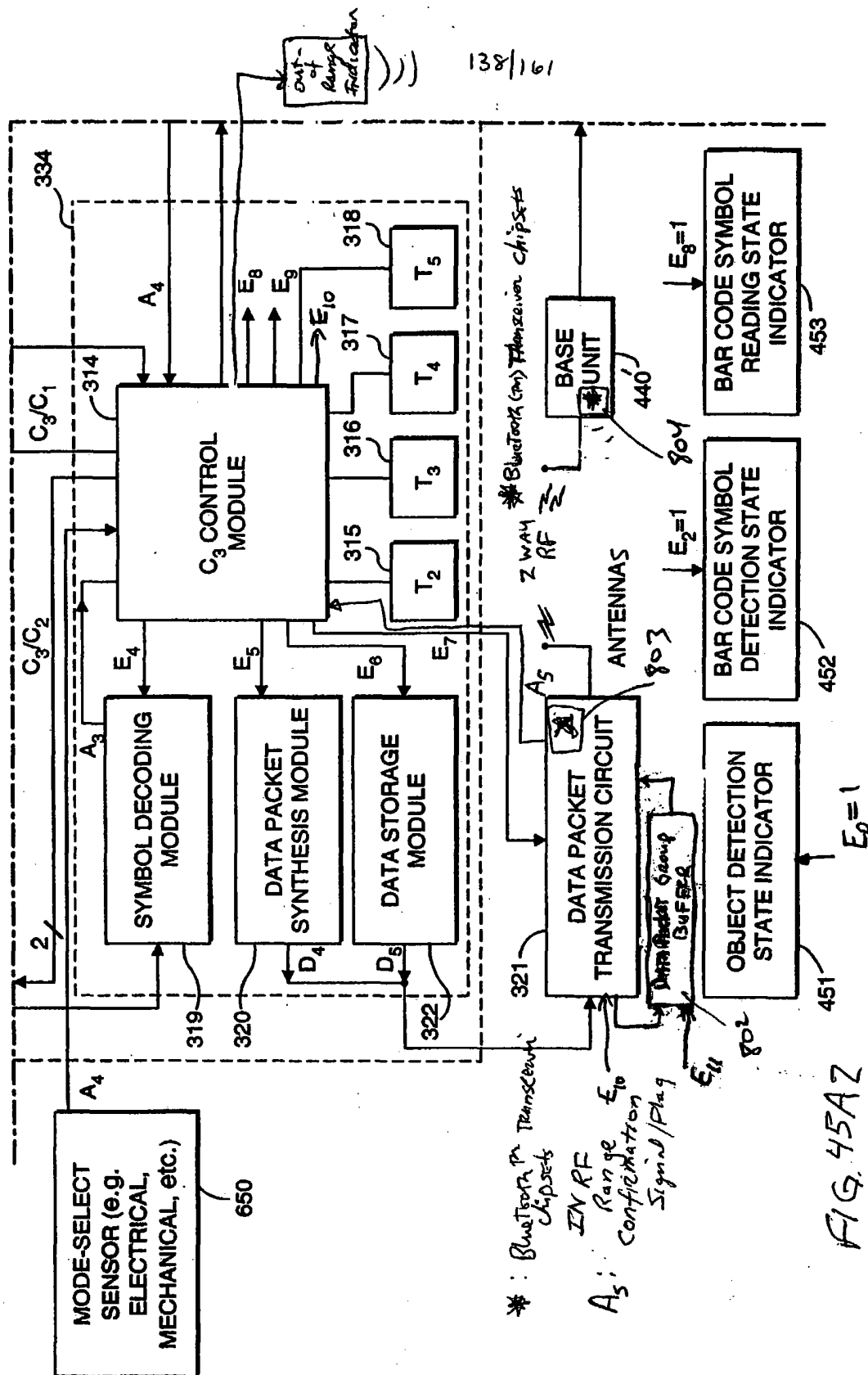
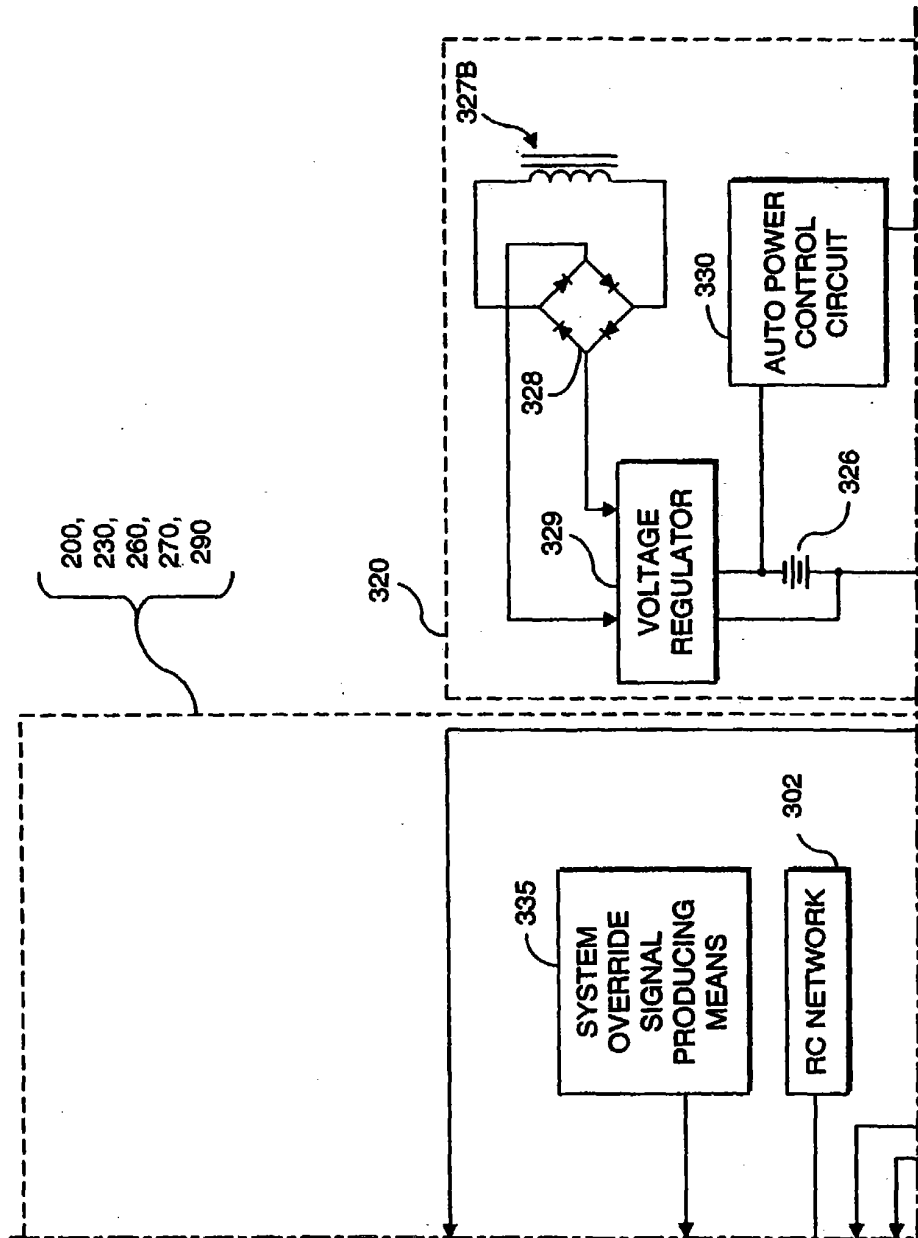


FIG. 44A2

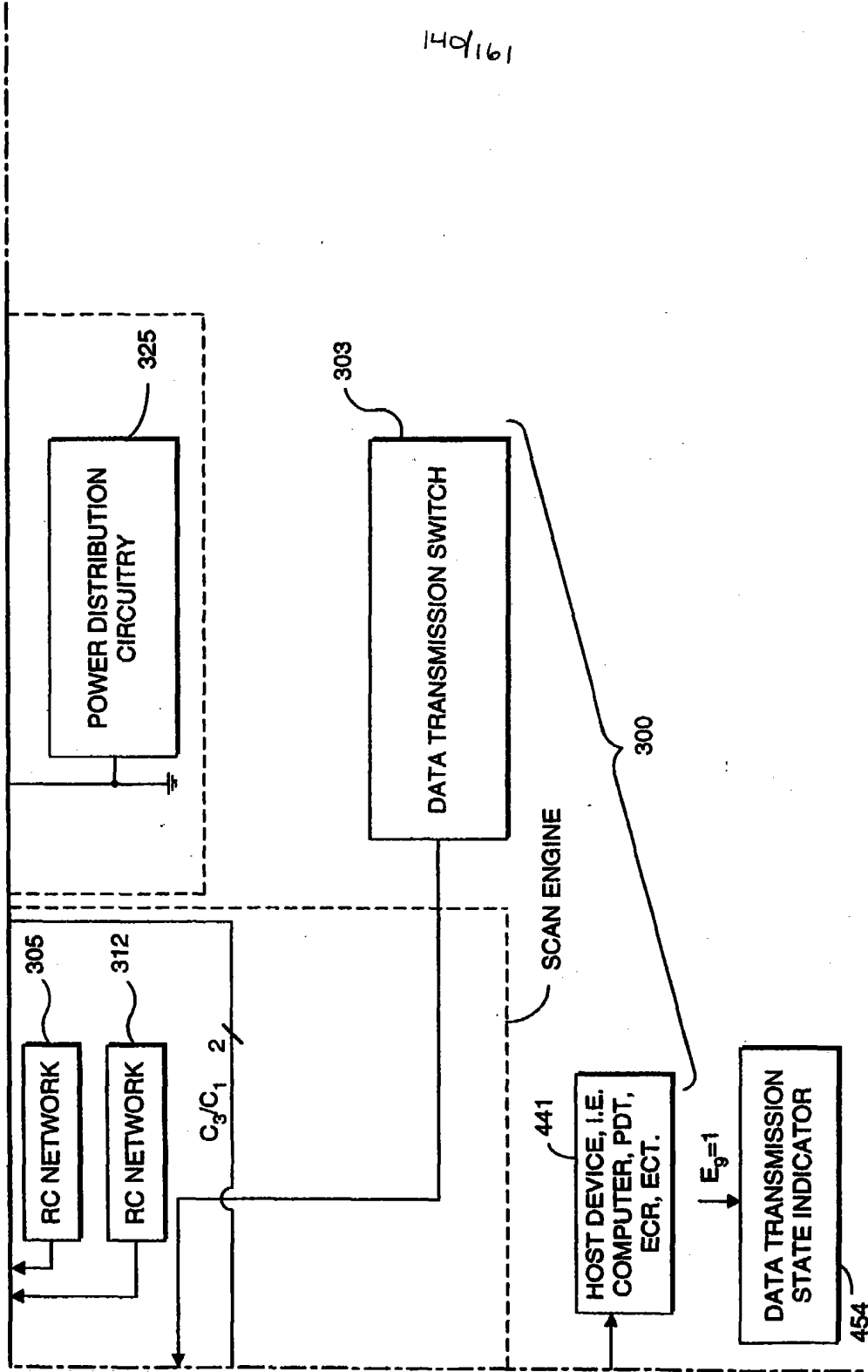
137/161







140/161



141/161

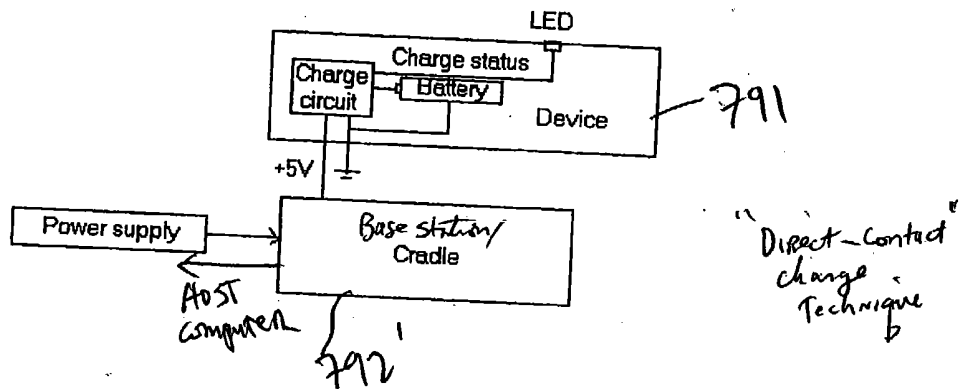


FIG. 45B

142/161

DATA Packet
Transmission VIA
2-WAY RF WITH Automatic
RF-Range Dependent
Control

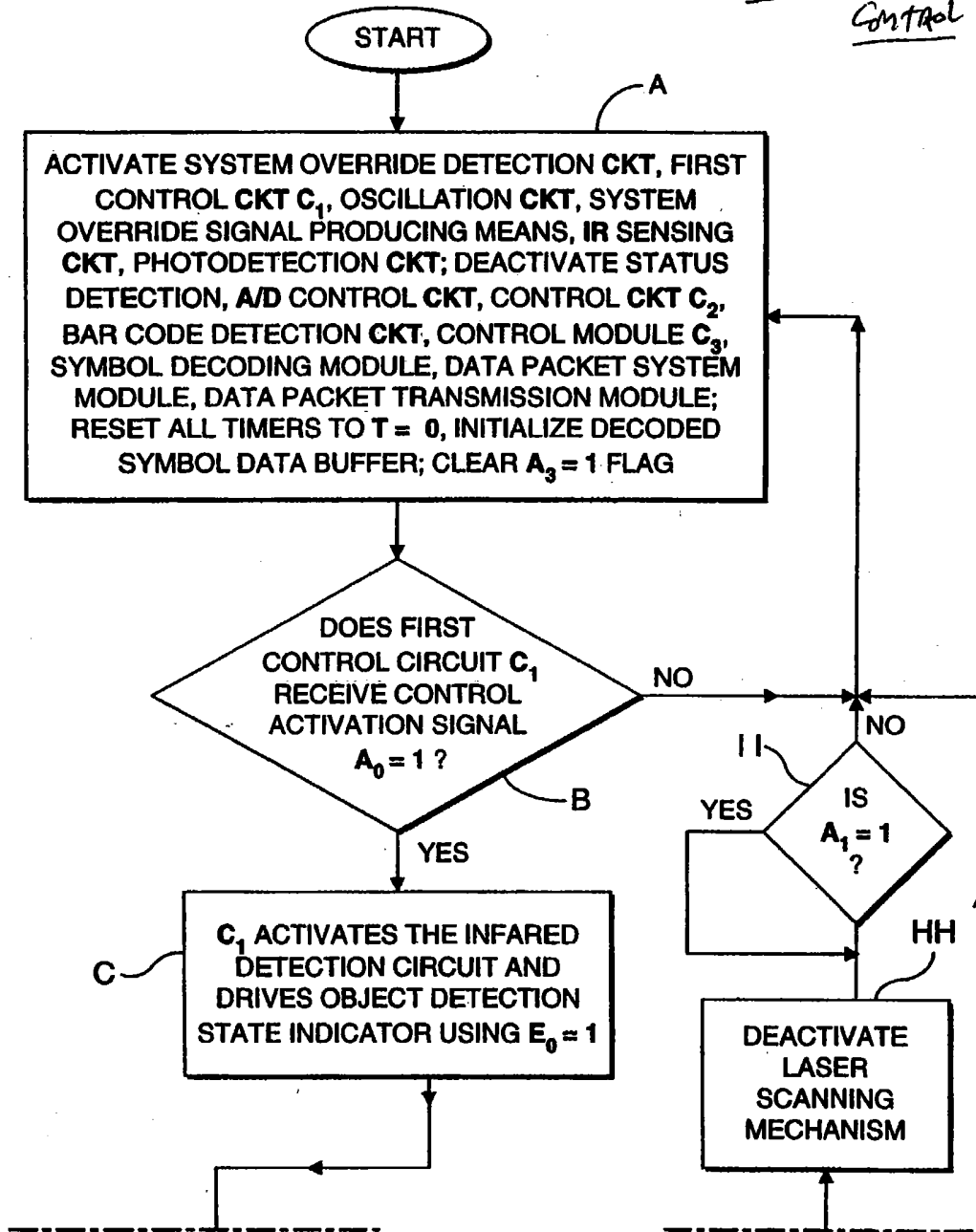


FIG. 46A1

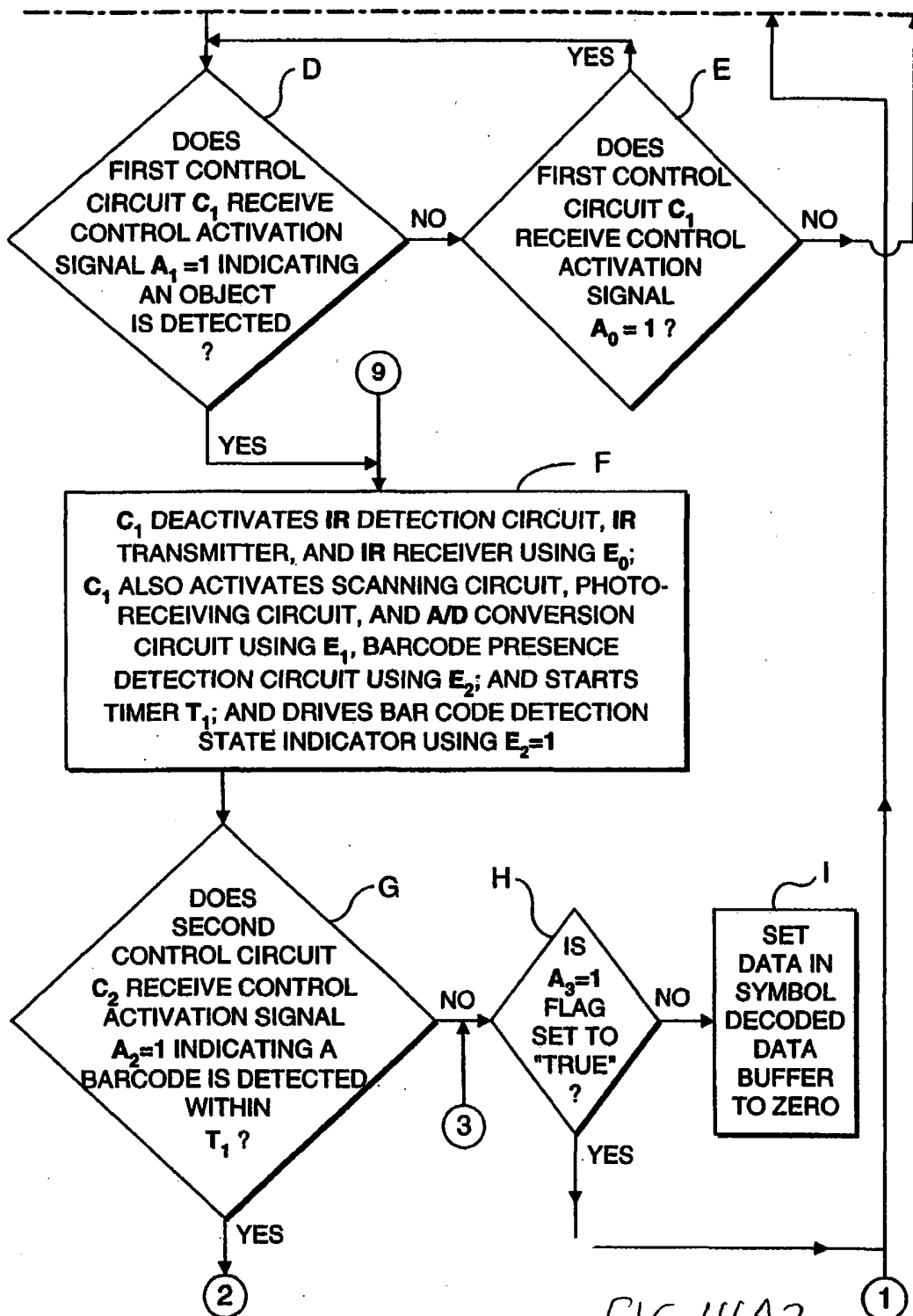
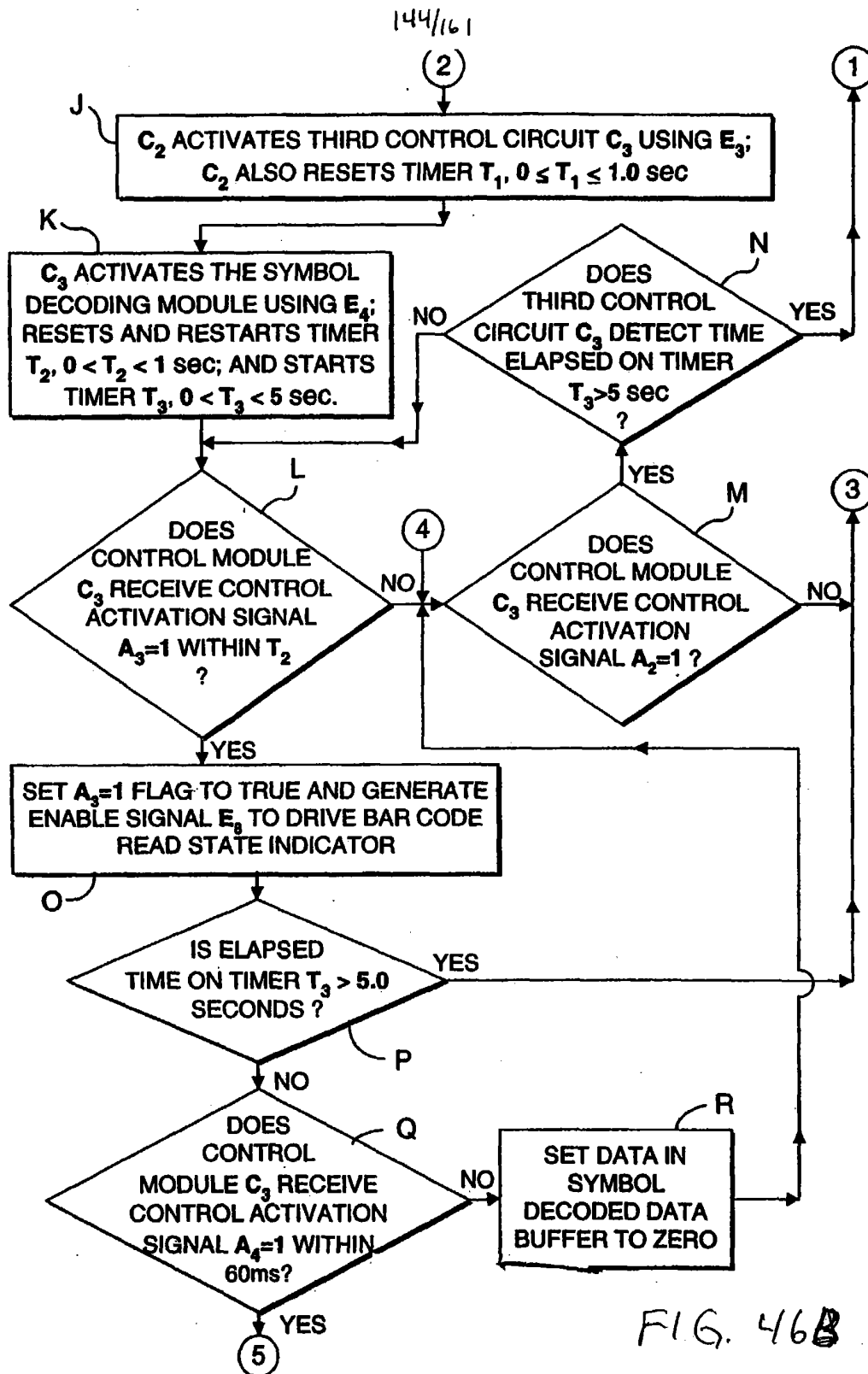


FIG 46A2



145/161

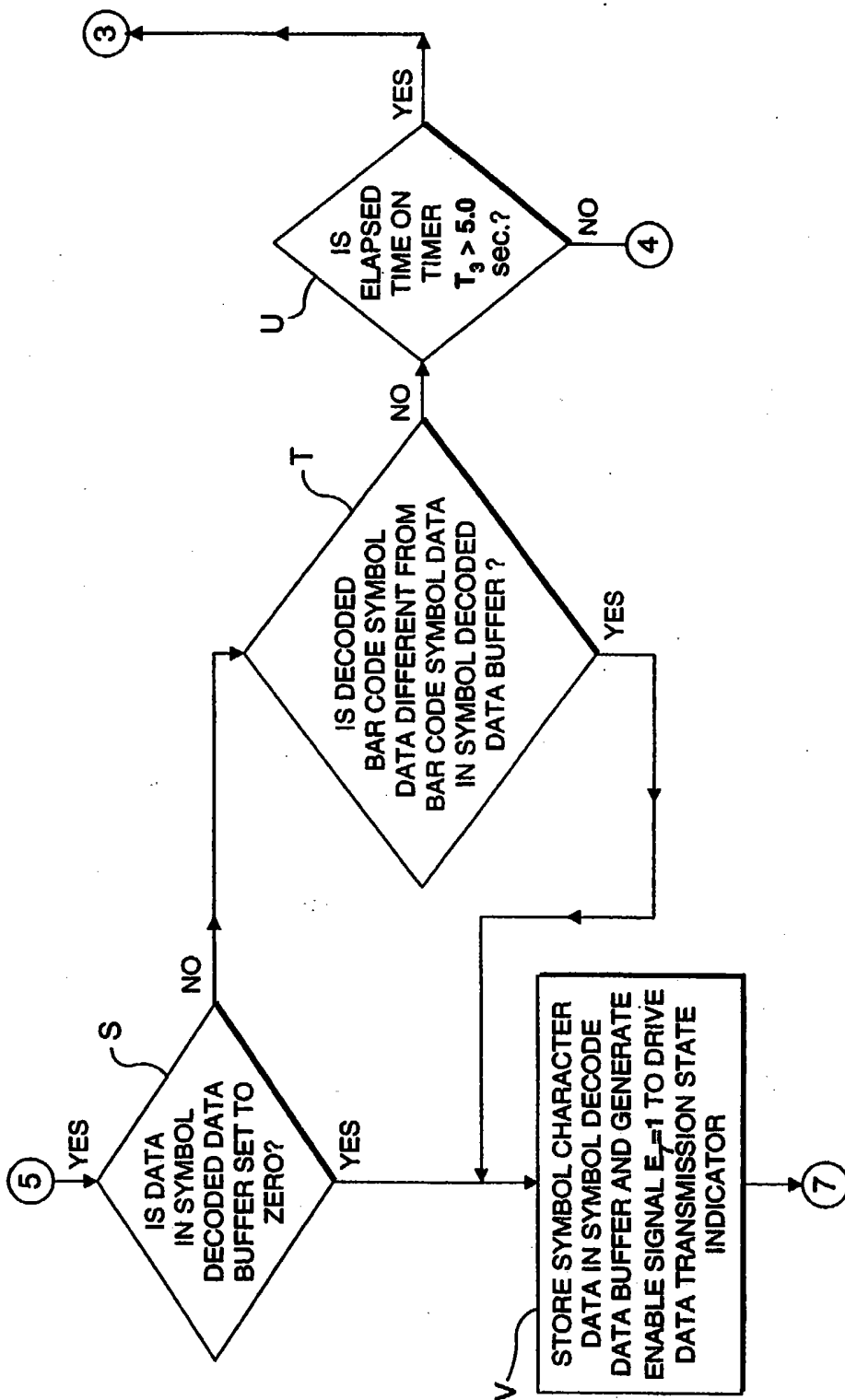


FIG 46C1

146/161

7

THIRD CONTROL CIRCUIT C_3 CONTINUES ACTIVATION OF LASER DIODE, SCANNING MOTOR, PHOTORECEIVING CIRCUIT, A/D CONVERSION CIRCUIT; DEACTIVATES SYMBOL DECODING MODULE; AND COMMENCES ACTIVATION DATA PACKET SYNTHESIS MODULE

W

UNDER C_3 CONTROL DATA PACKET SYNTHESIS MODULE CONSTRUCTS DATA PACKET CONSISTING OF SYMBOL CHARACTER DATA, TRANSMITTER NUMBER, DATA PACKET GROUP NUMBER, CHECK CHARACTER AND FRAMING CHARACTERS

X

IS IN-RF RANGE CONFIRMATION SIGNAL $A_5=1$?

NO

YES

Y

ARE DATA PACKET GROUP(S) STORED IN DATA PACKET GROUP BUFFER?

NO

Z

YES

C_3 CONTROL MODULE GENERATES ENABLE SIGNAL $E_{11}=1$ RELOADING BUFFERED DATA PACKET GROUP(S) INTO DATA PACKET TRANSMISSION CIRCUIT

AA

CONTROL MODULE C_3 GENERATES ENABLE SIGNAL $E_{10}=1$ LOADING CURRENTLY SYNTHESIZED DATA PACKET GROUP INTO DATA PACKET TRANSMISSION CIRCUIT

BB

11

FIG. 46C2

147/161

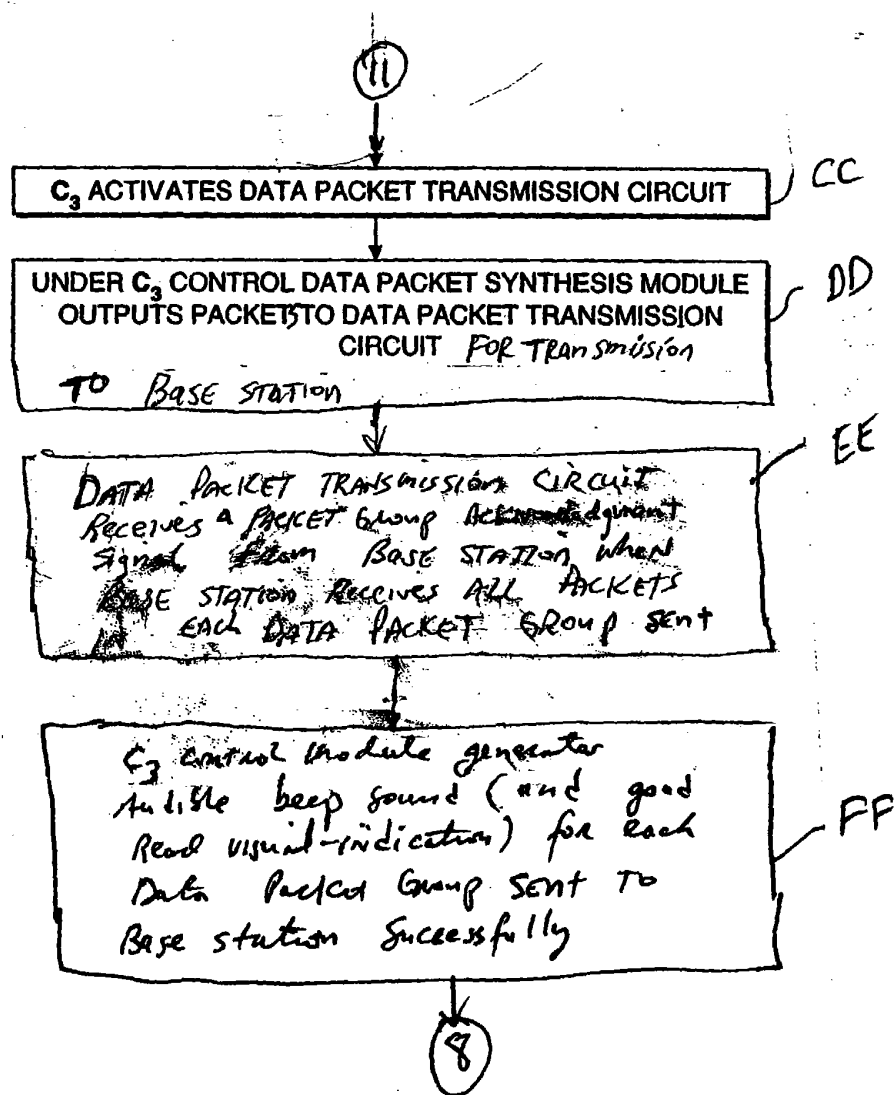


FIG. 46C3

1481161

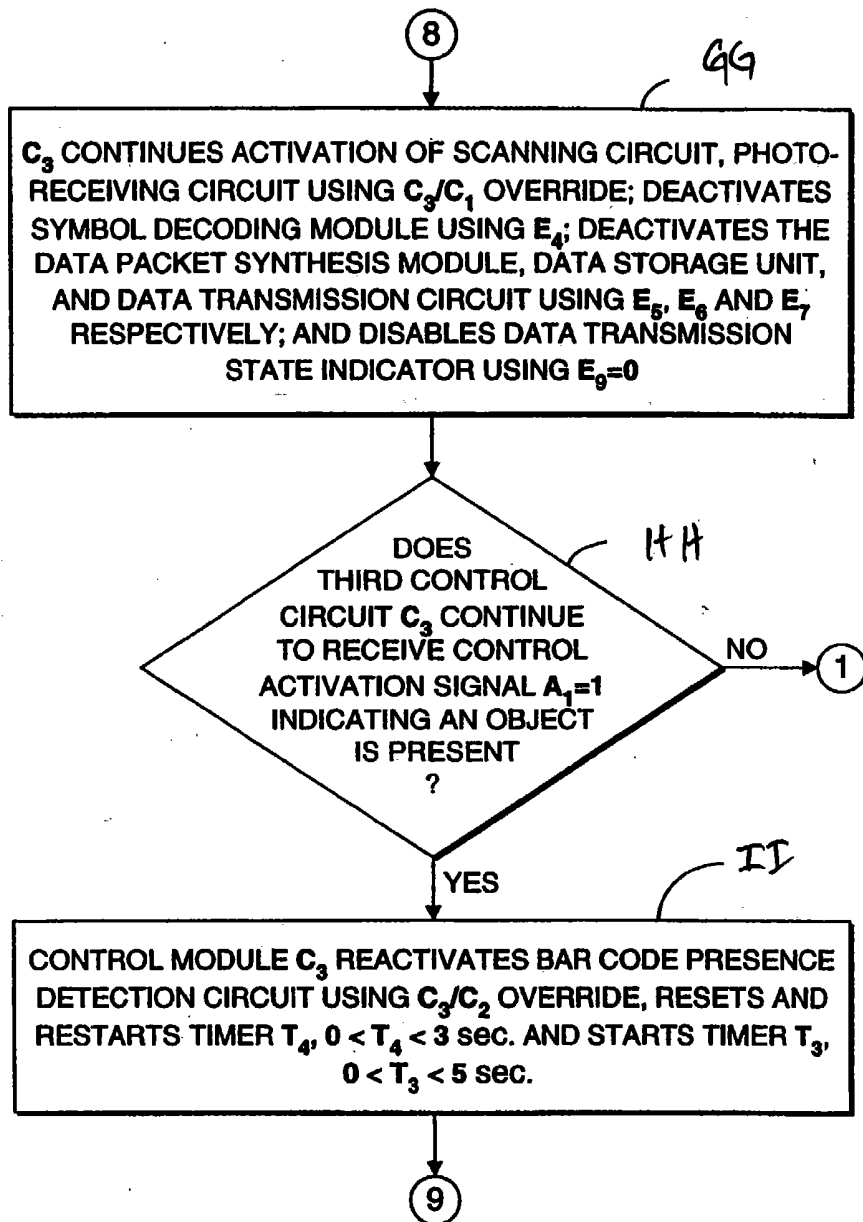


FIG 46 C4

149/161

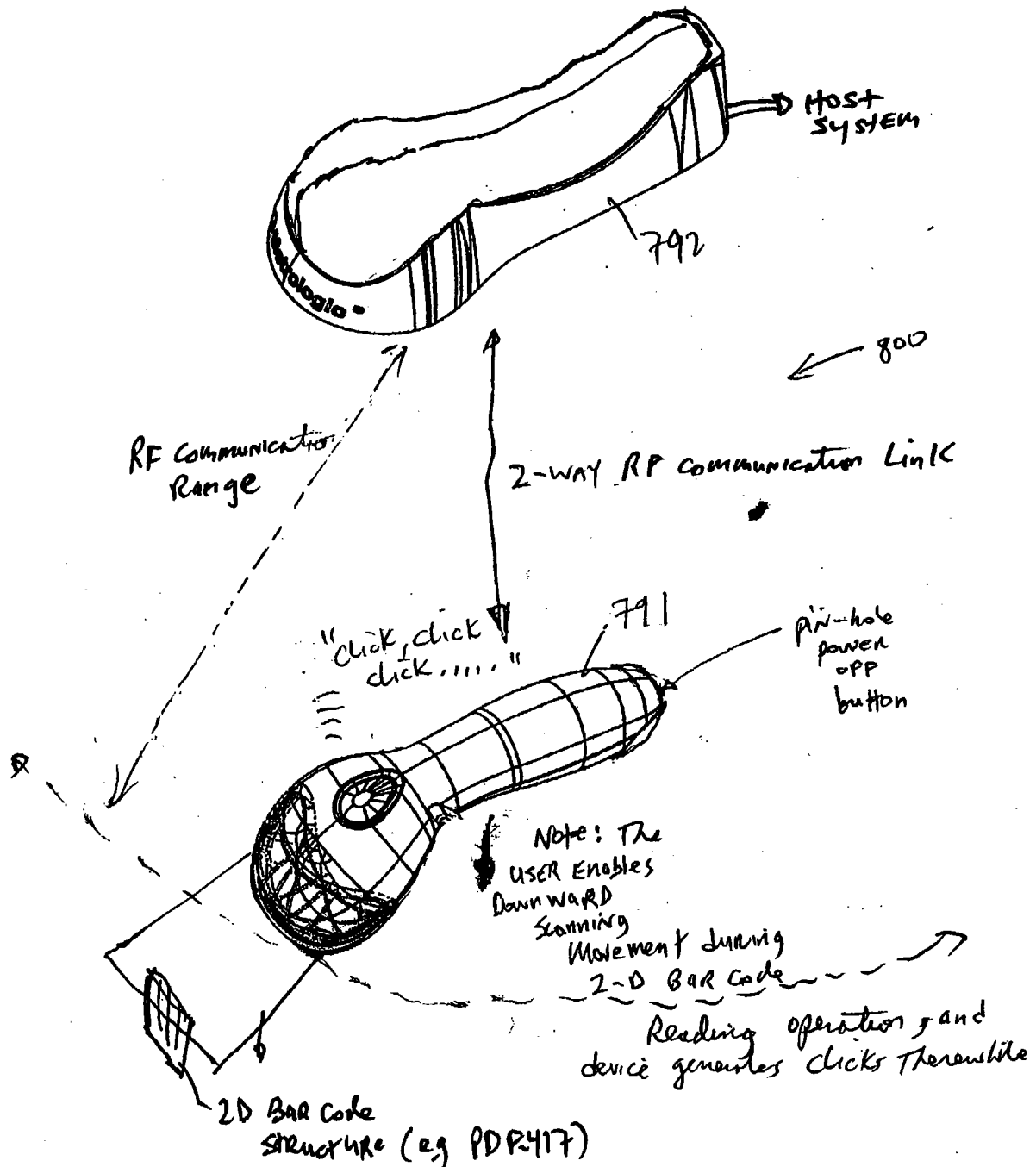
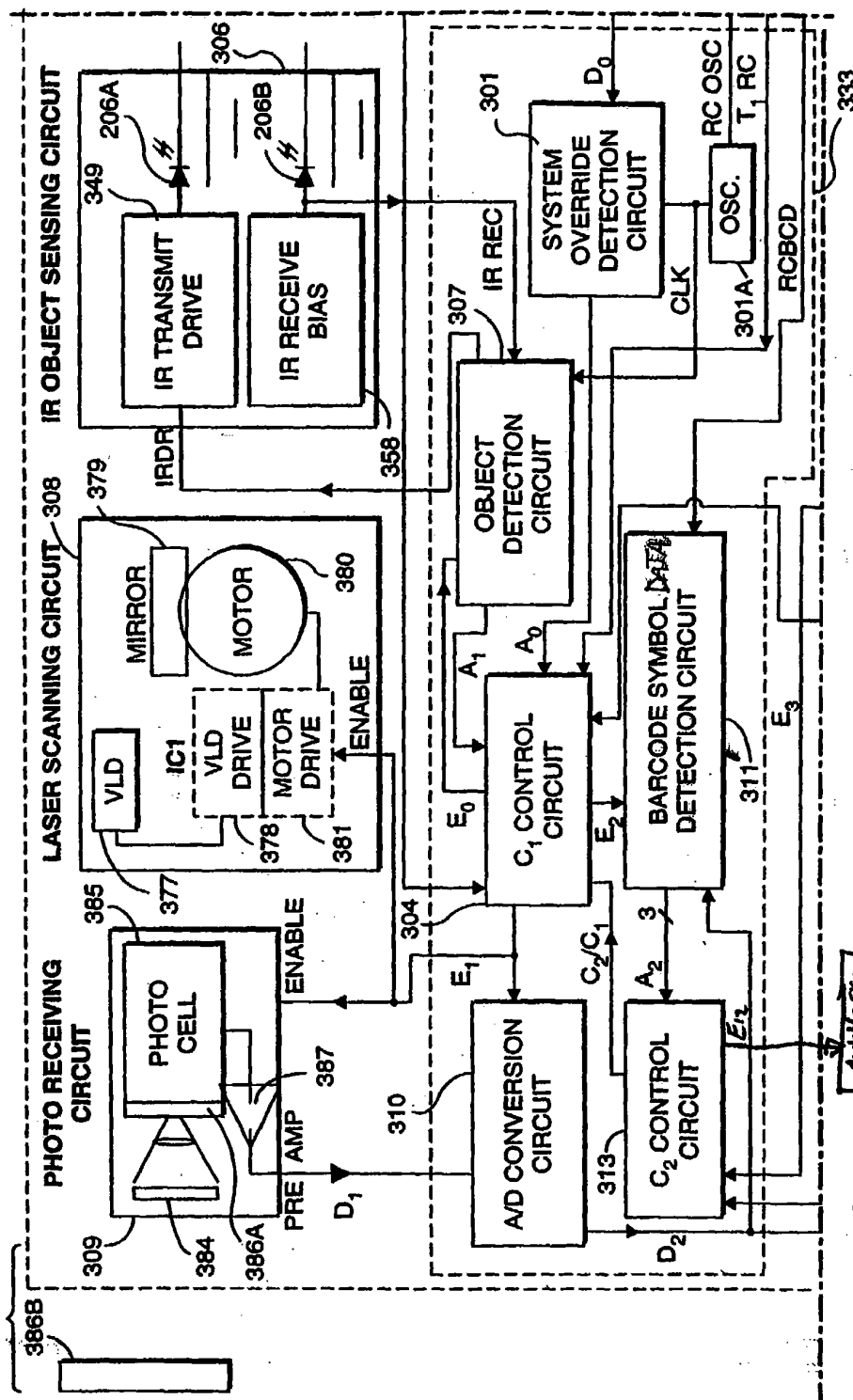


FIG. 47

150/161

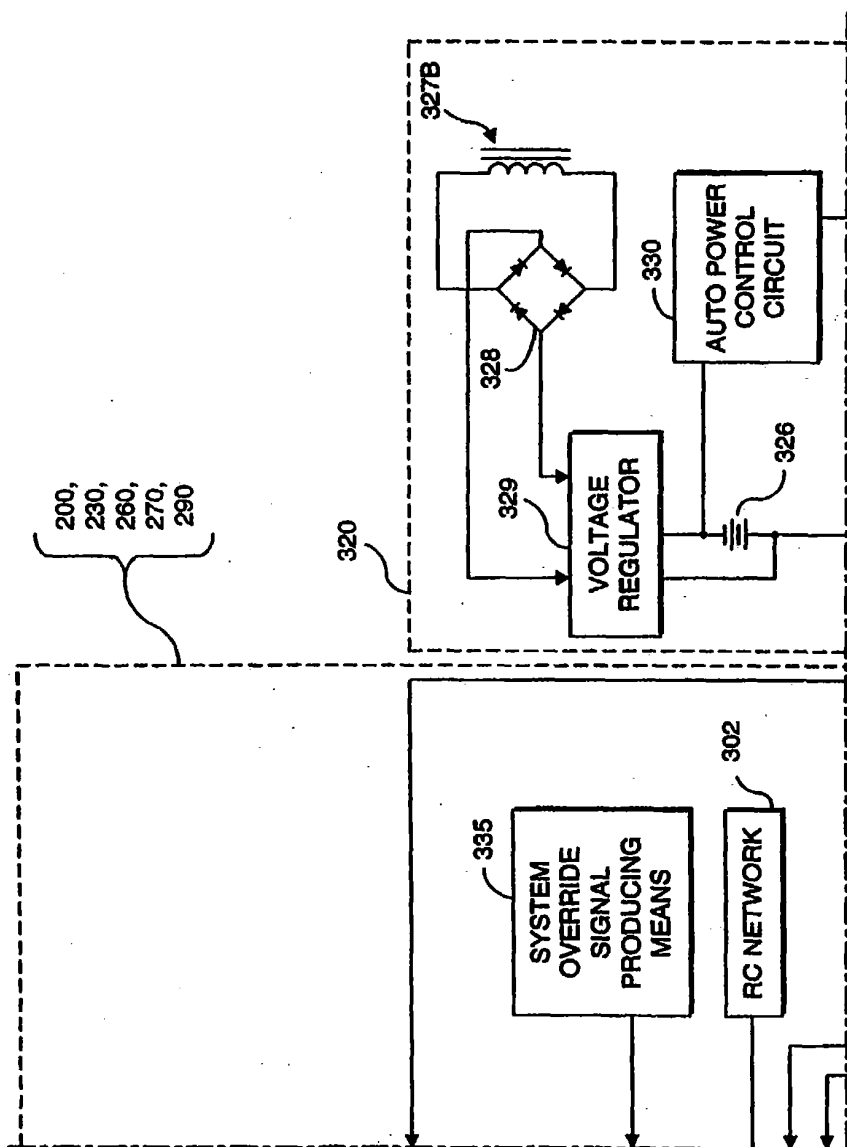


806 FIG. 47A1
(pico-electric transducer)

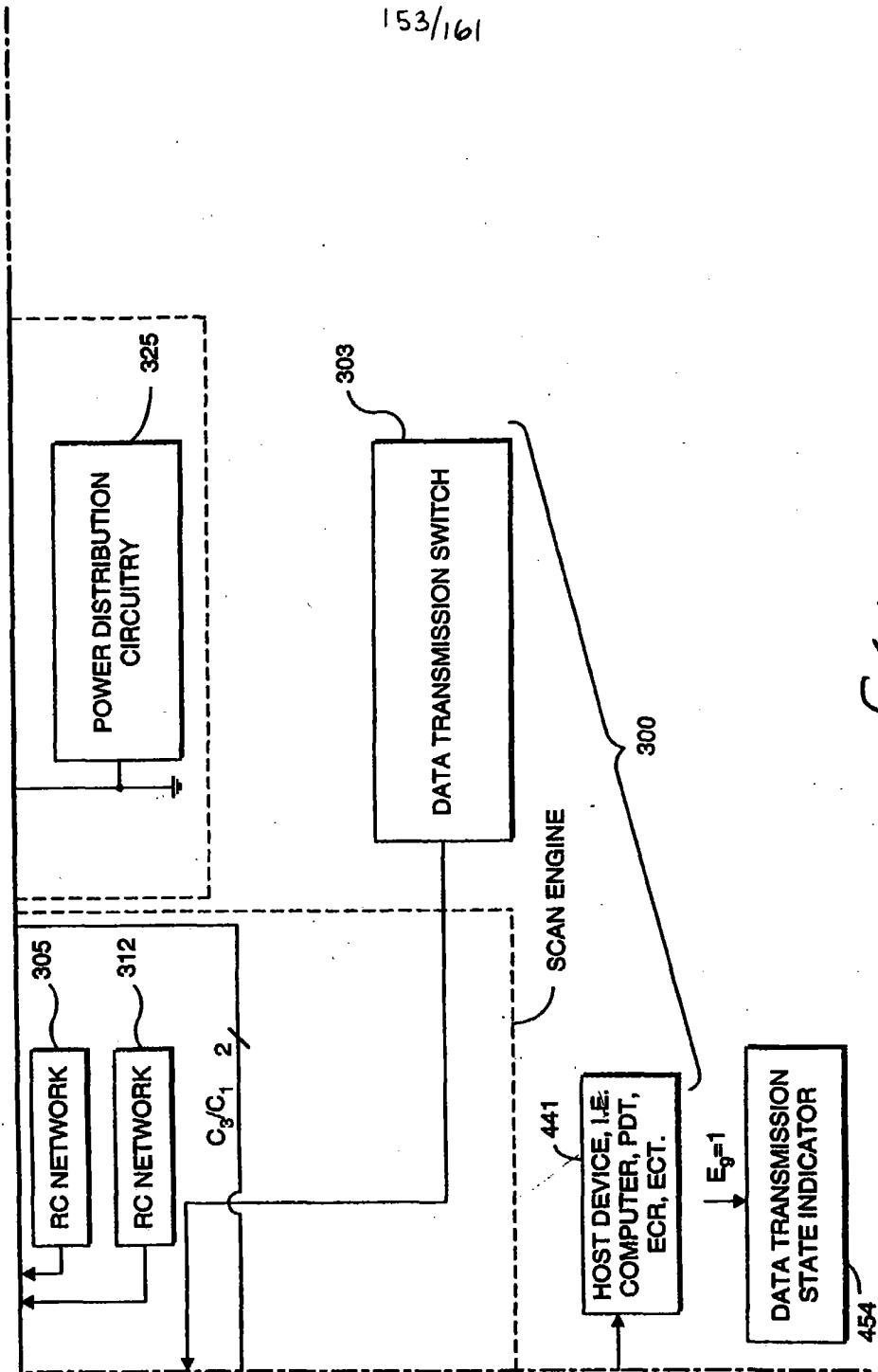
800
Audio 555
Data Capture
Buffer
Indicator

152/161

FIG 47A3



153/161



154/161

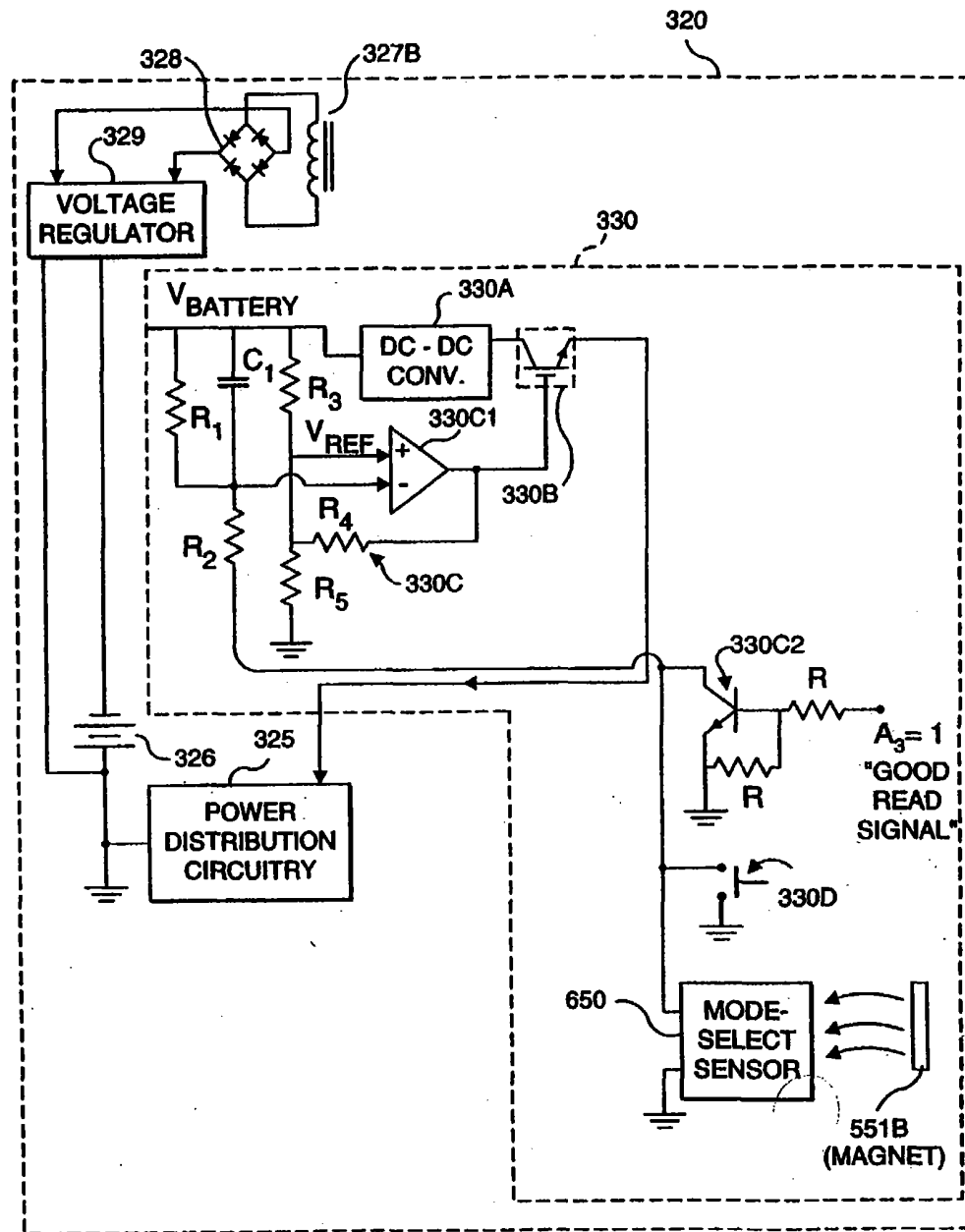


FIG. 47B

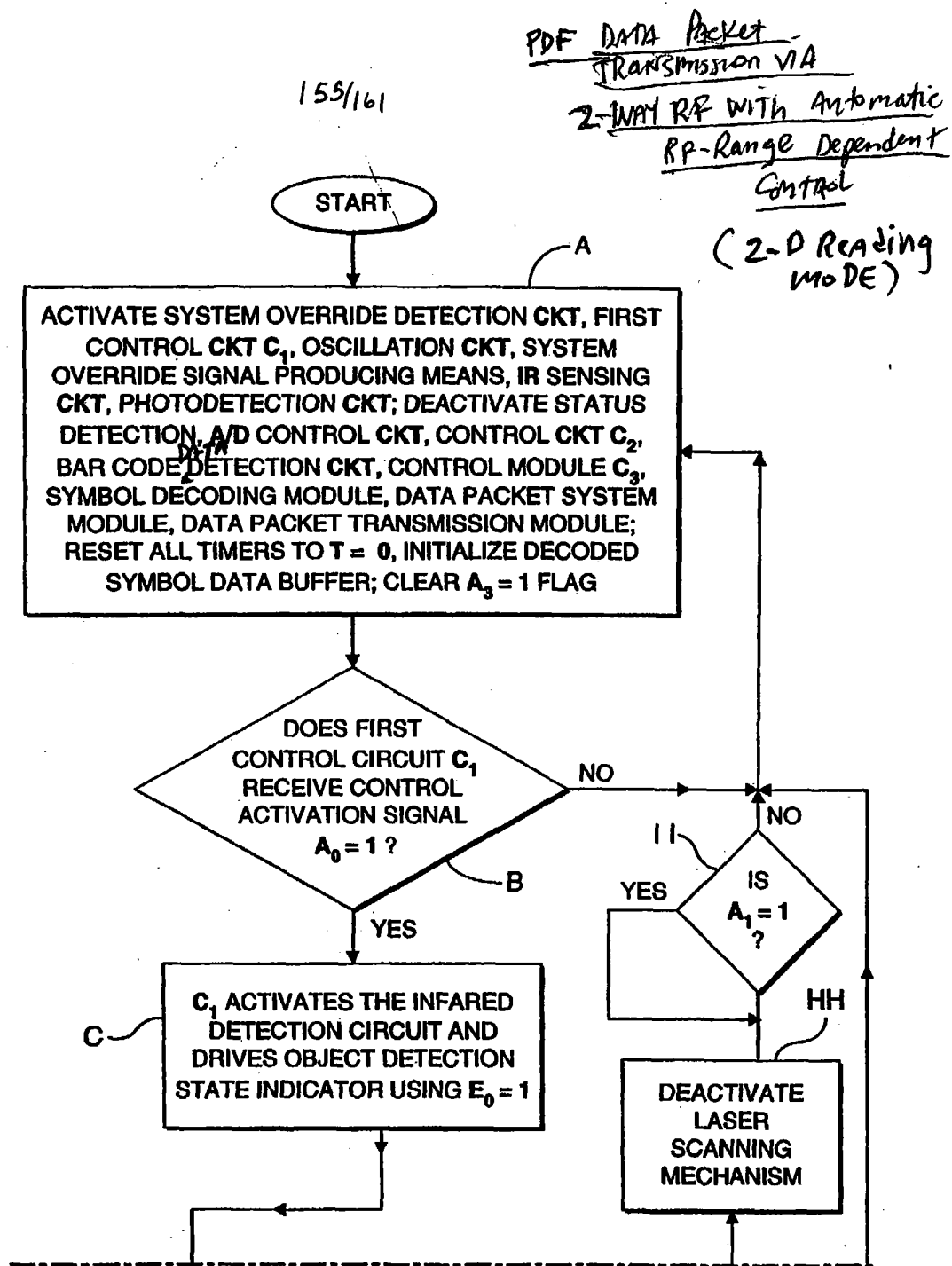
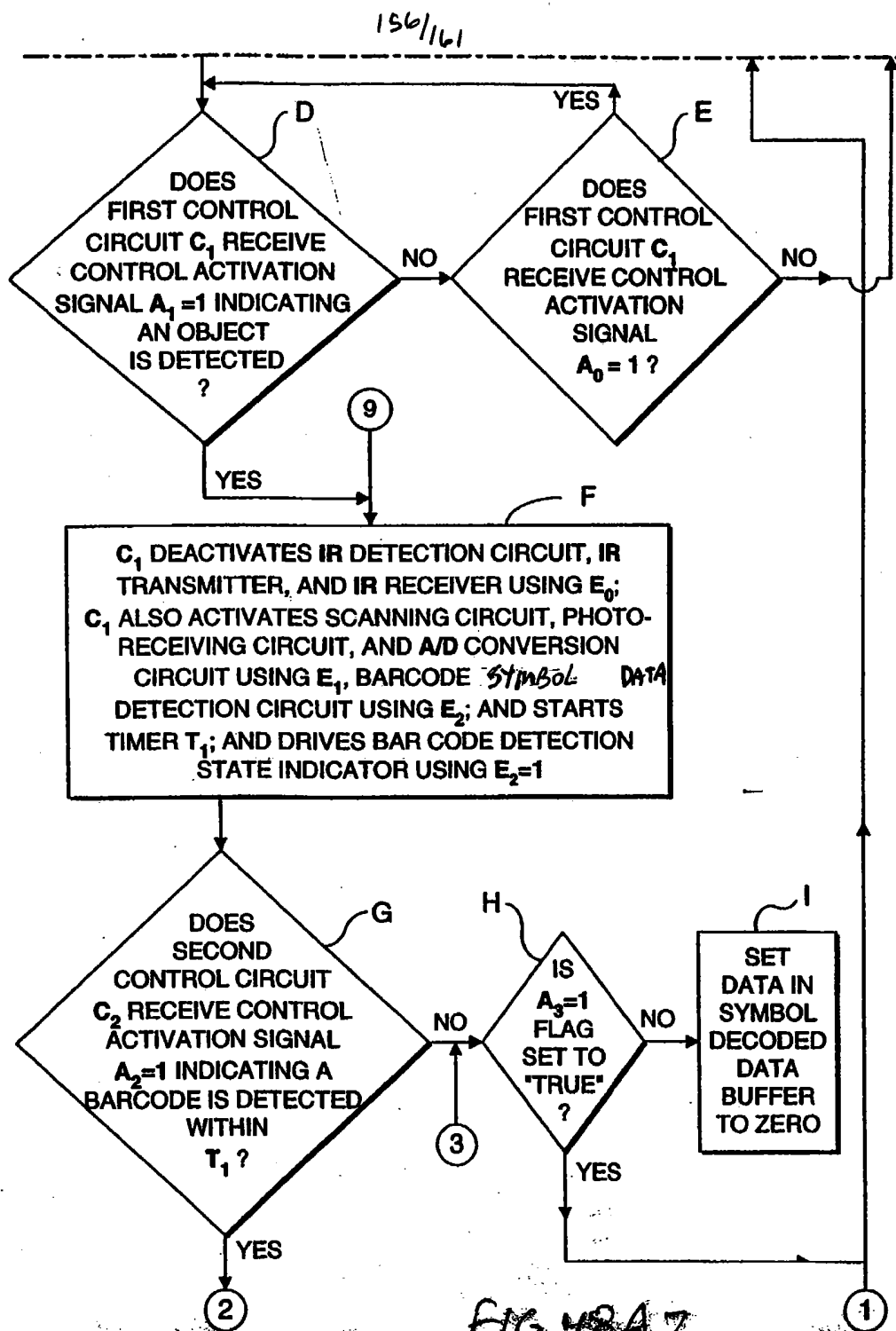
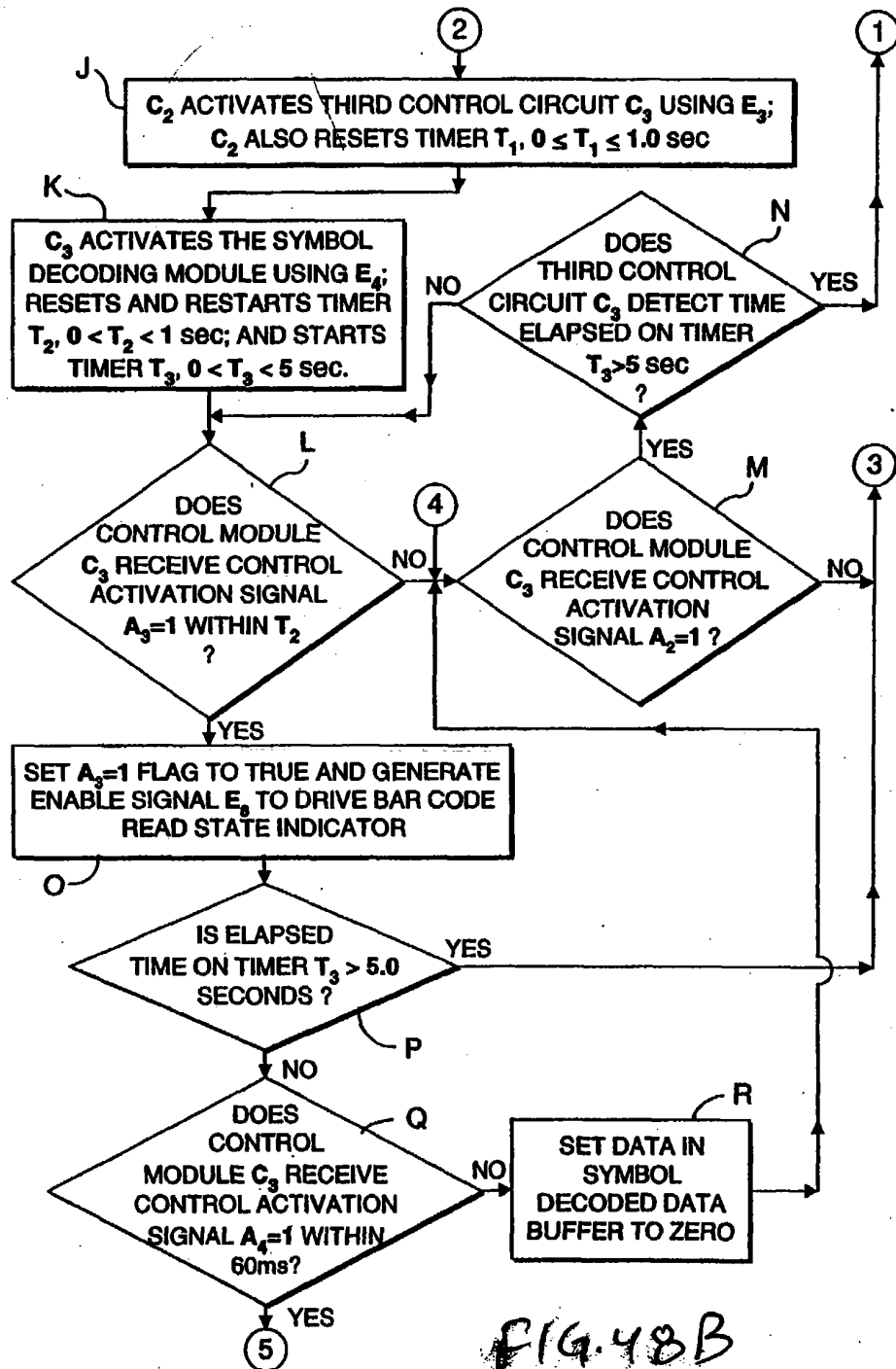


FIG. 48A1



157/161



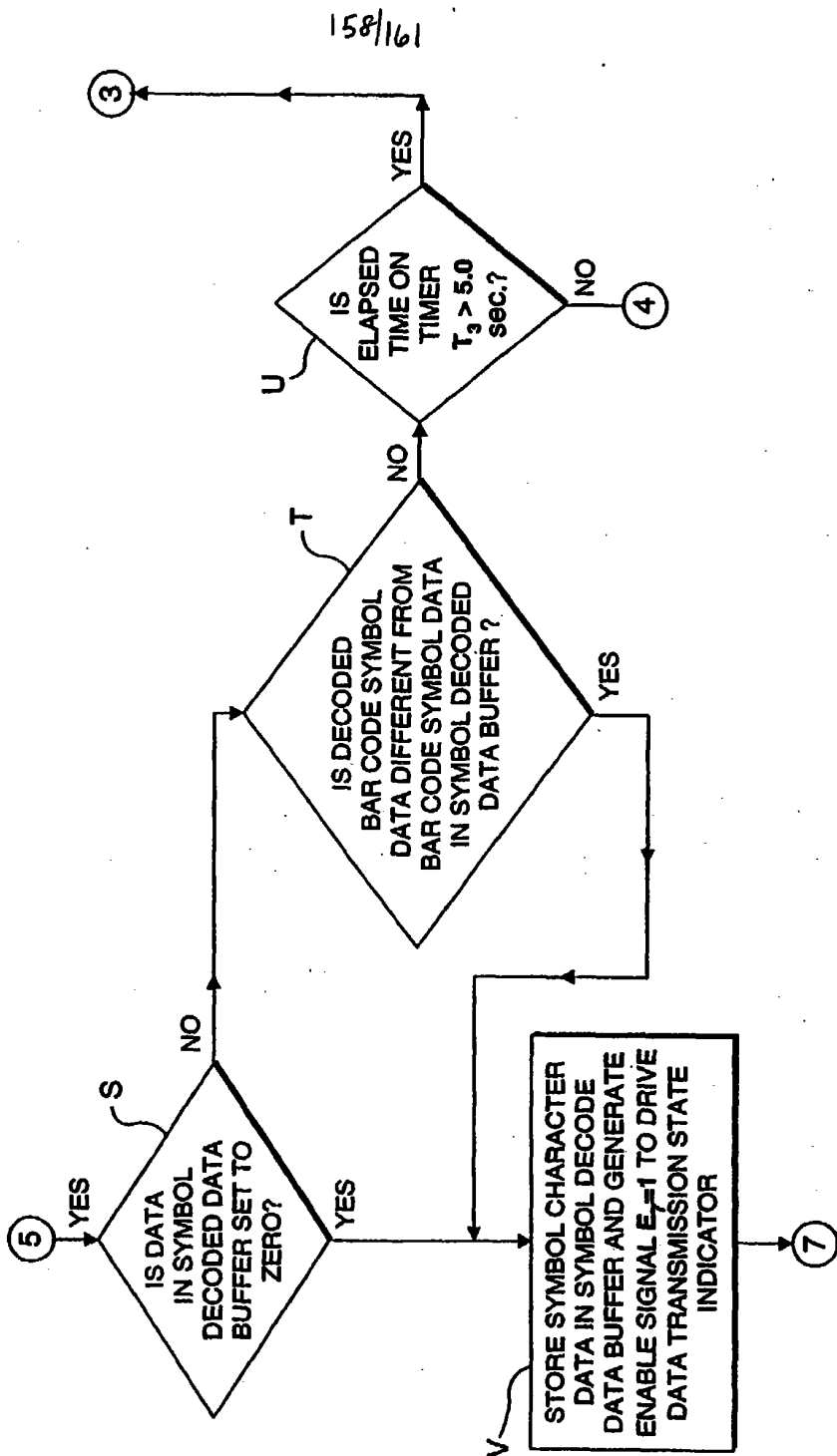


FIG. 48C1

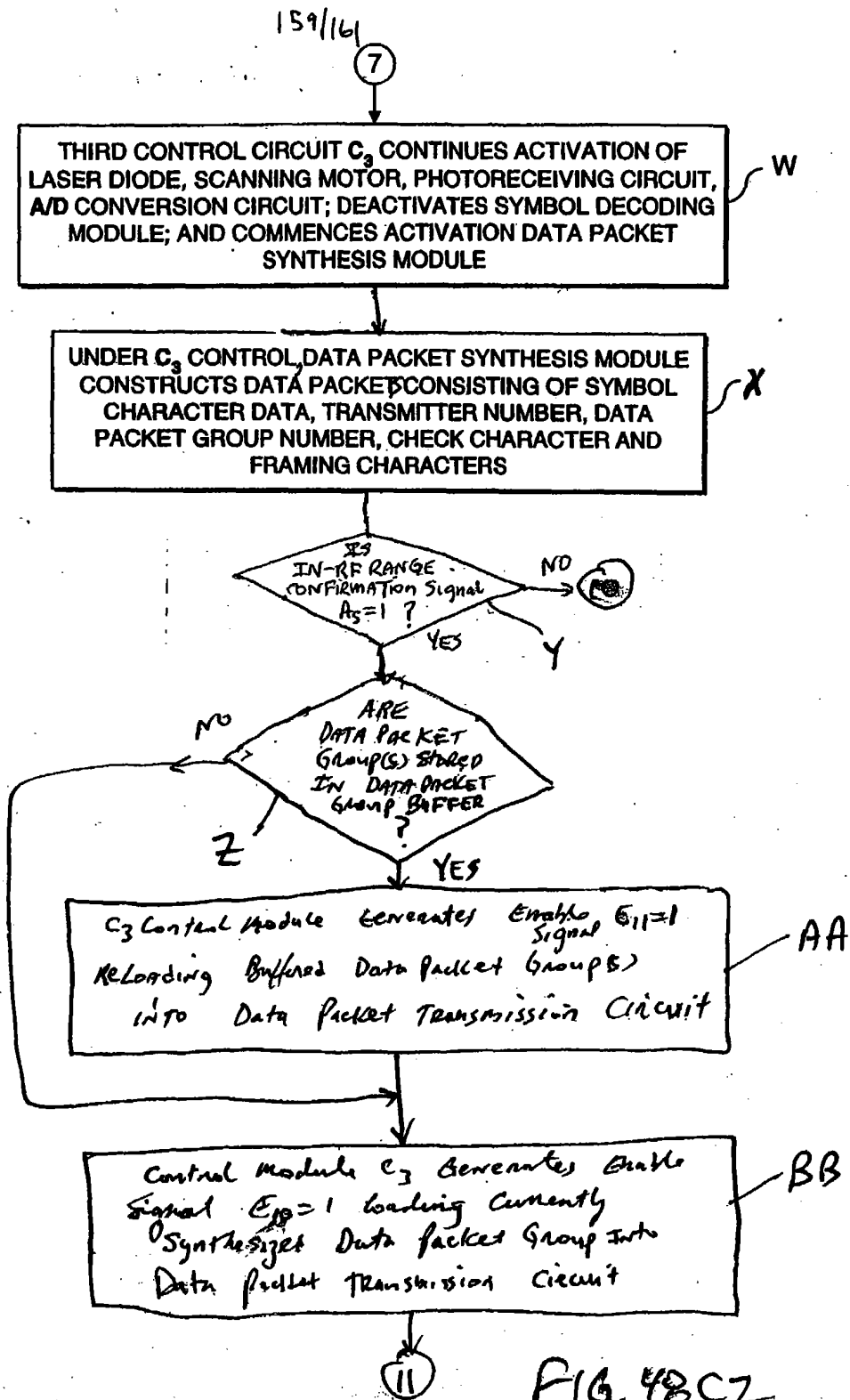


FIG. 48CZ

160/161

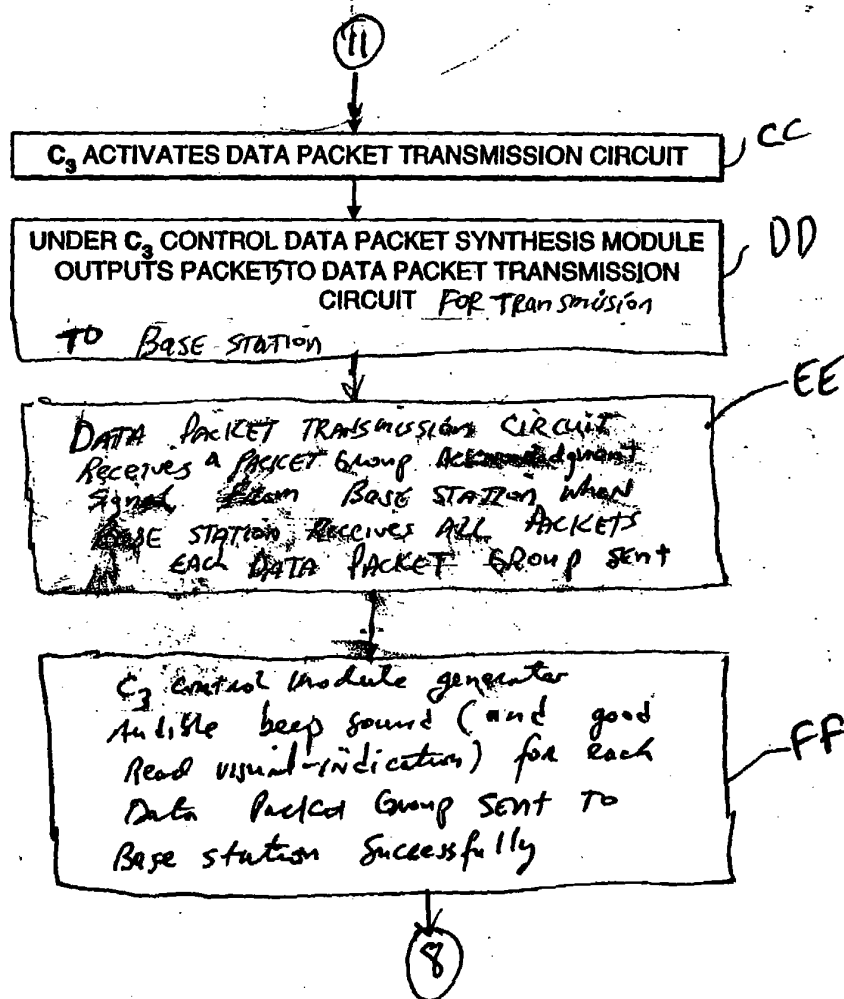


FIG. 48C3

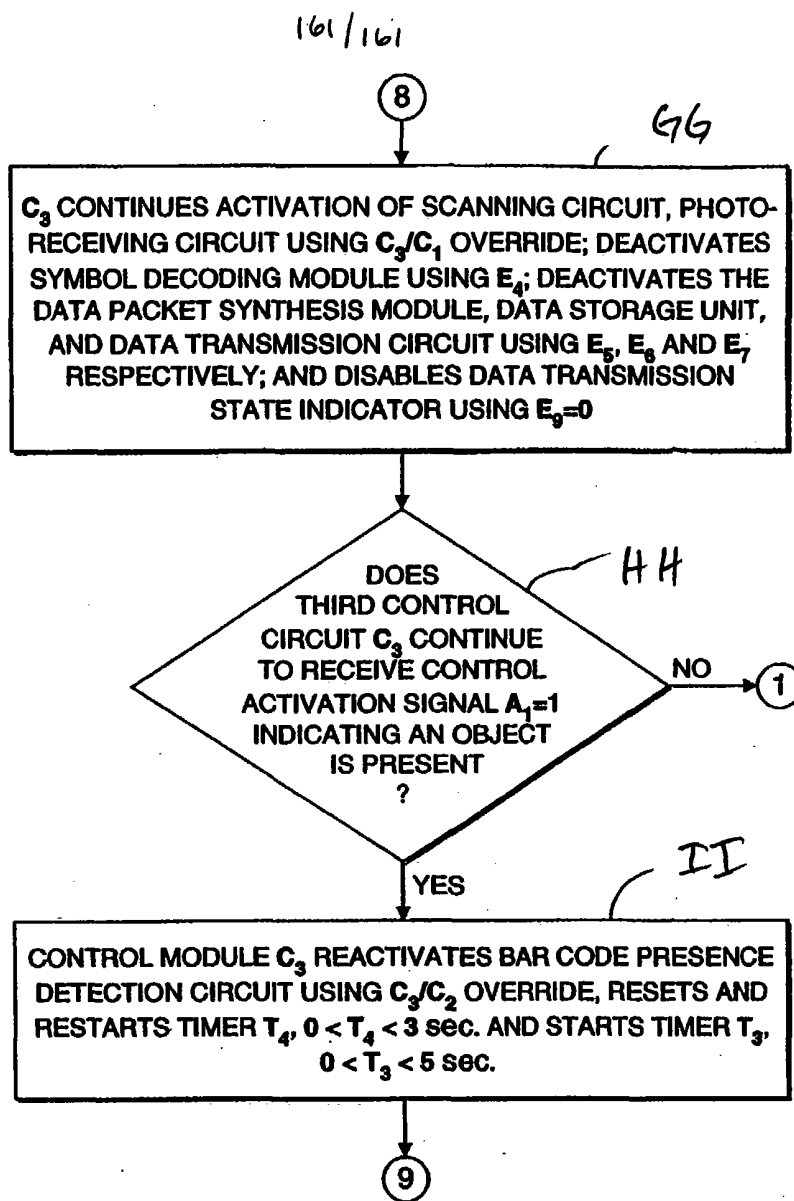


FIG. 48C4